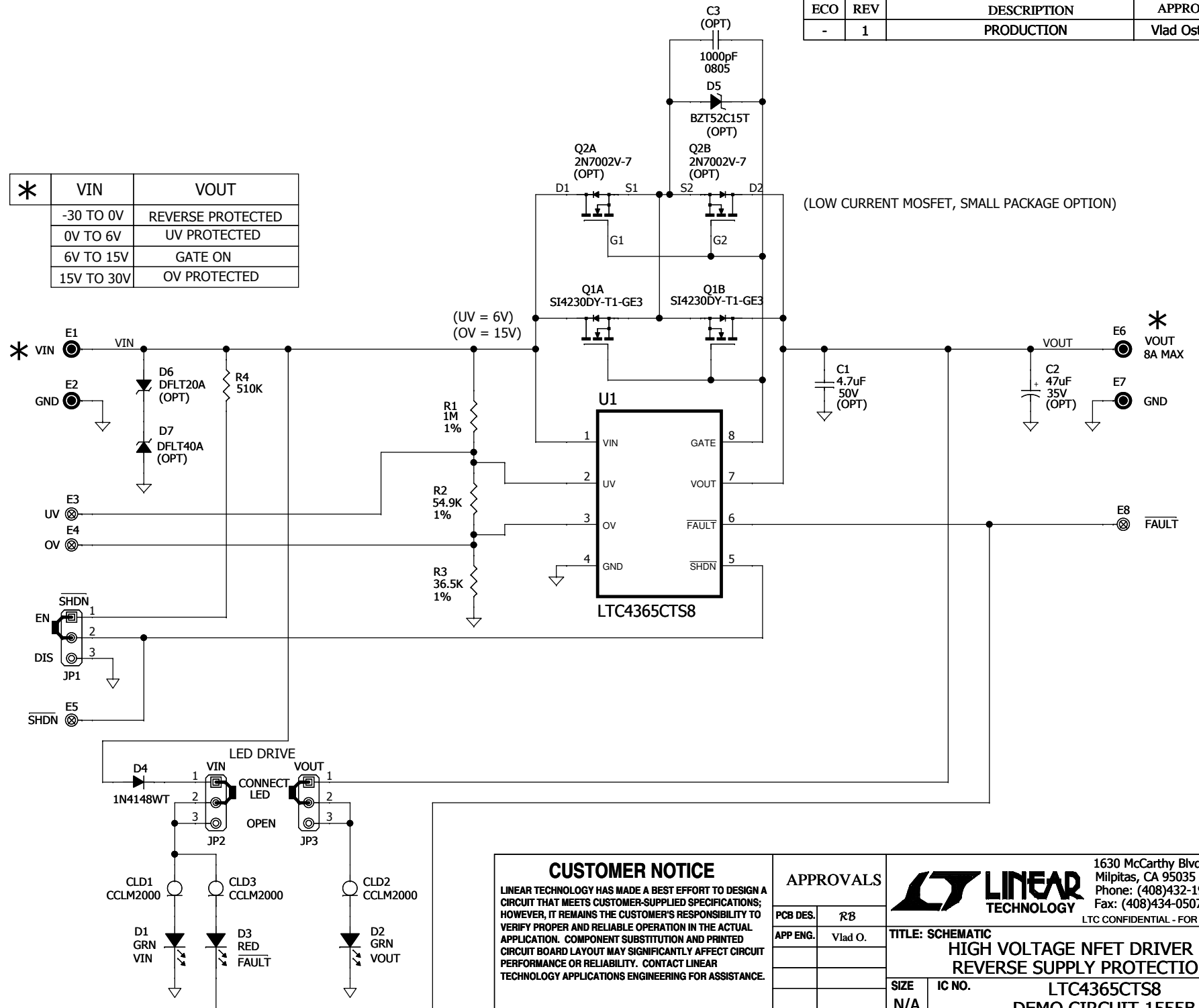


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION	Vlad Ostrerov	09/25/12

* VIN	VOUT
-30 TO 0V	REVERSE PROTECTED
0V TO 6V	UV PROTECTED
6V TO 15V	GATE ON
15V TO 30V	OV PROTECTED



(LOW CURRENT MOSFET, SMALL PACKAGE OPTION)

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	RB
APP ENG.	Vlad O.
SCALE = NONE	

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 Milpitas, CA 95035 www.linear.com  
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 Fax: (408)434-0507  
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**LINEAR TECHNOLOGY**

**TITLE: SCHEMATIC**  
**HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION**

SIZE	IC NO.	REV.
N/A	LTC4365CTS8	1
MODIFY DATE: Sep 25, 2012		SHEET 1 OF 1