



MSL1

* Pb Free Part

Customer Name	Standard Specification	TAIYO YUDEN Mobile Technology Co.,Ltd.	
System	GPS+GLONASS (50/100 ohm)	Date	March 31, 2010
Part Number	FAR-F6KB-1G5859-B4HR	Version 2.0b	

Table 1.Electrical specifications

Passband:1565.42 ~ 1605.8860 MHz						
Parameter	Frequency	Specification			Unit	Remark
		Fujitsu Proposal Ver.2.0b				
		Min.	Typ.	Max.		
Operating temperature		-30	-	+85	°C	
Insertion loss	1574.42-1576.42 MHz	-	1.1	1.4	dB	*1
Insertion loss	1565.42-1585.42 MHz		1.4	1.8	dB	*1
Insertion loss	1597.5515-1605.8860 MHz	-	1.2	2.0	dB	*1
VSWR(input/output)	1574.42-1576.42 MHz	-	1.3	2.0	-	
	1565.42-1585.42 MHz		1.6	2.0	-	
	1597.5515-1605.8860 MHz	-	1.3	2.0	-	
Absolute Attenuation	10 – 794 MHz	47	53	-	dB	
	794 – 925 MHz	46	51	-	dB	
	925 – 960 MHz	40	51	-	dB	
	1427 – 1463 MHz	35	40	-	dB	
	1710 – 1785 MHz	35	39	-	dB	
	1850 – 1910 MHz	40	45	-	dB	
	1920 – 1980 MHz	40	46	-	dB	
	2401 – 2483 MHz	45	50	-	dB	
Amplitude imbalance (S21/S31)	1574.42-1576.42 MHz	-1.5	-0.2/-0.2	+1.5	dB	
	1565.42-1585.42 MHz	-4.5	-1.4/+0.2	+1.5	dB	
	1597.5515-1605.8860 MHz	-1.88	-0.2/+0.3	+1.5	dB	
Phase imbalance (∠S21-∠S31)+180)	1574.42-1576.42 MHz	-10	-1/-1	+10	deg.	
	1565.42-1585.42 MHz	-10	-1/+6	+10	deg	
	1597.5515-1605.8860 MHz	-10	-2/+4	+10	deg	
Input impedance		50			ohm	
Output impedance		100//18nH			ohm	
Device Size		1.4typ. x 1.0typ. x 0.5max.			mm	

*1) Specification of insertion loss includes loss that comes from the test board (0.15dB).



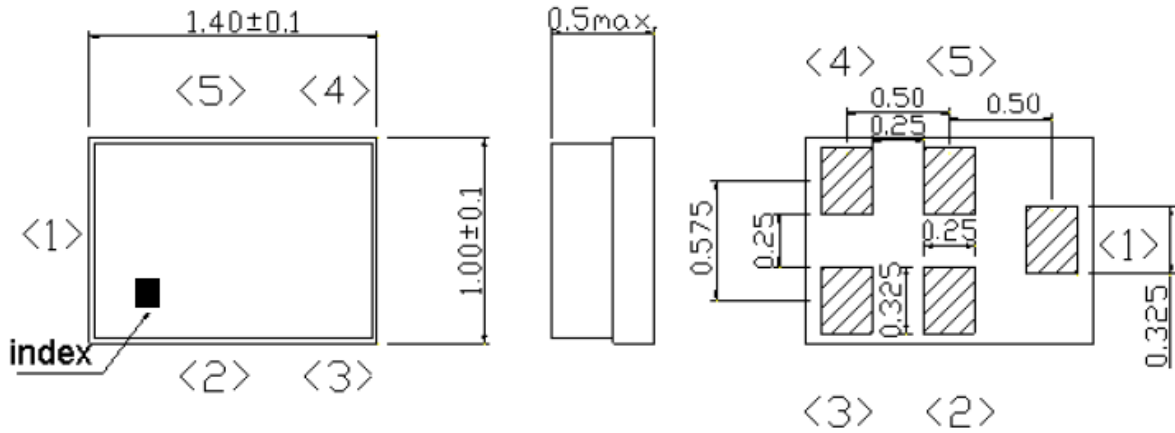
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Dimensions

Device size: 1.4typ. x 1.0typ. x 0.5max.

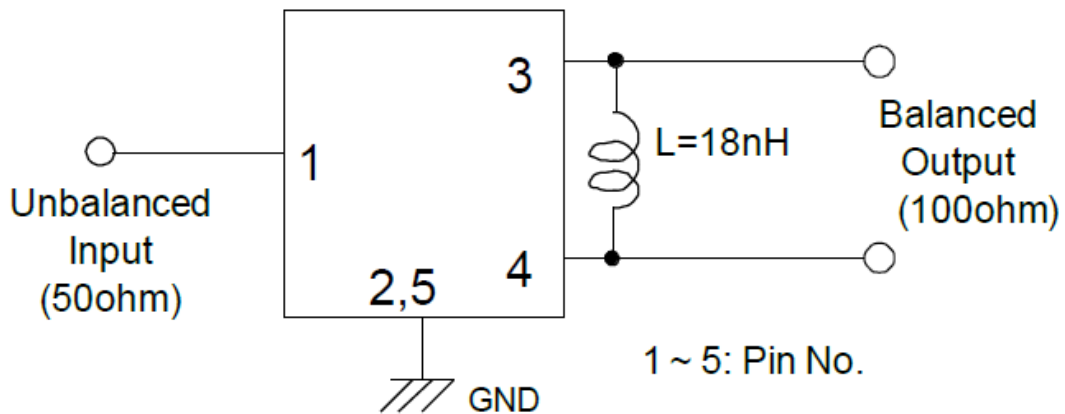


Unit : mm

Pin Configuration

Pin No.	Pin name	Description
1	IN	Unbalanced pin
2	GND	Ground
3	OUT	Balanced pin
4	OUT	Balanced pin
5	GND	Ground

Evaluation Circuit





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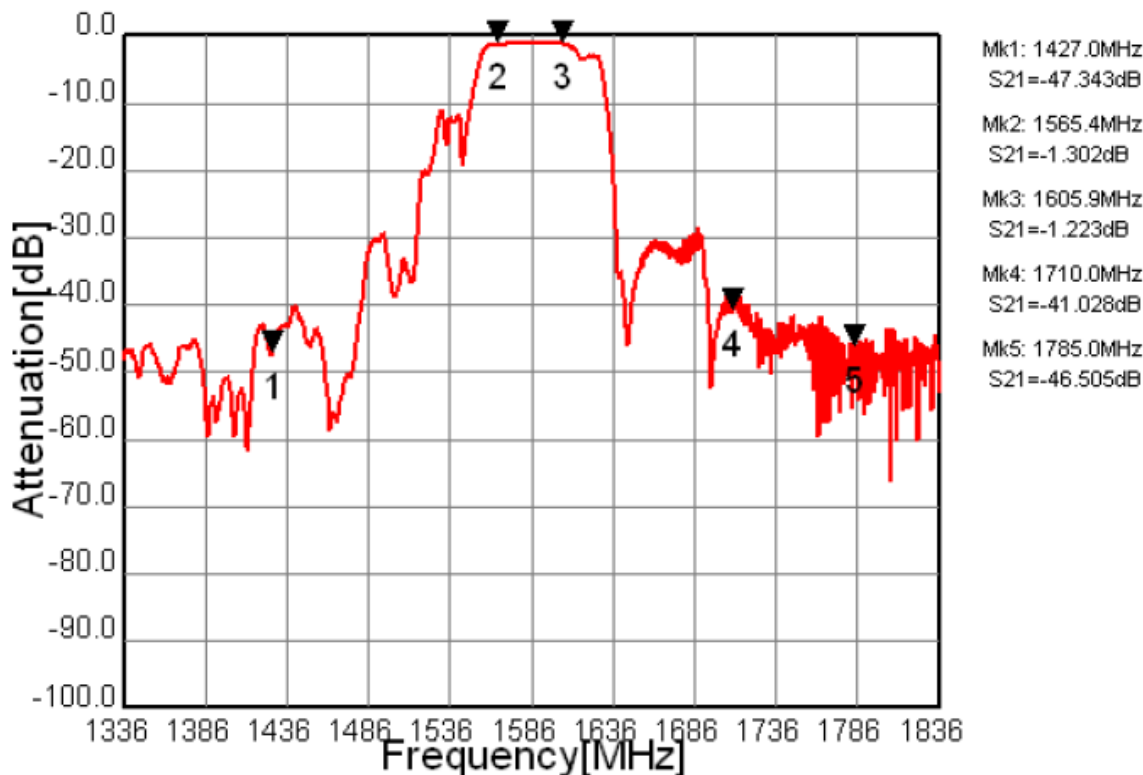


Fig.1 Pass-band Characteristics

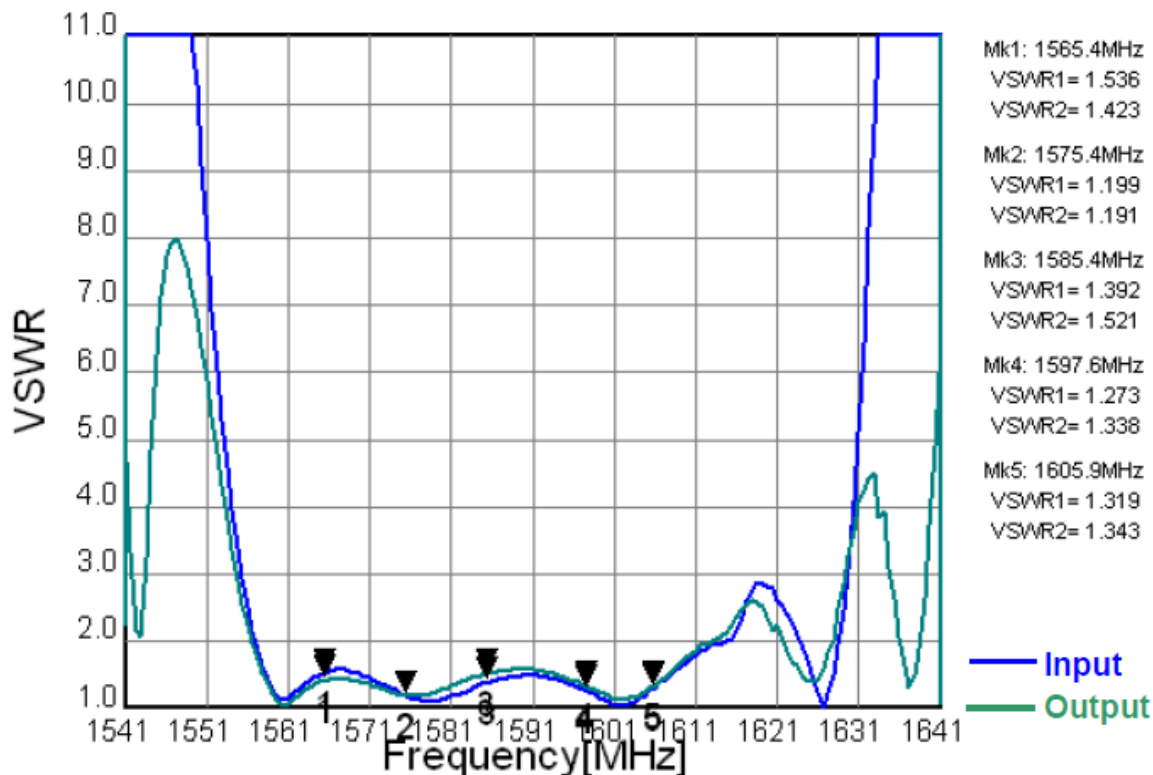


Fig.2 VSWR



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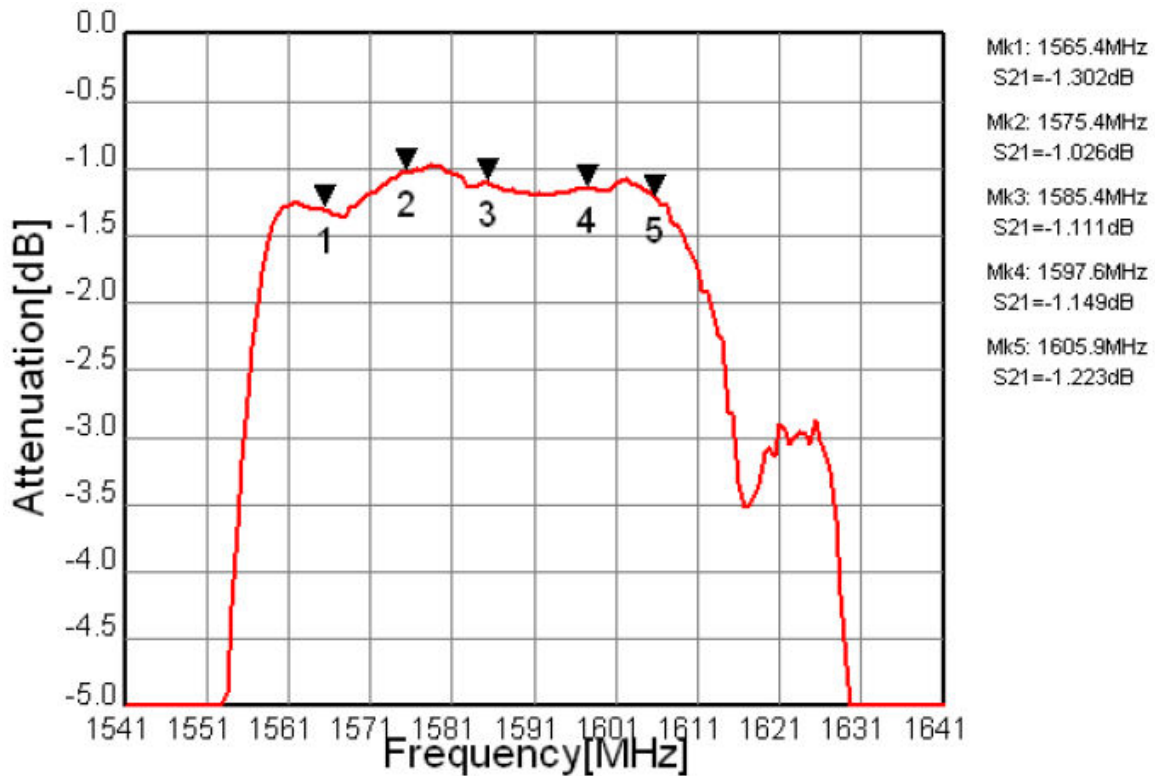


Fig.3 In-band Characteristics

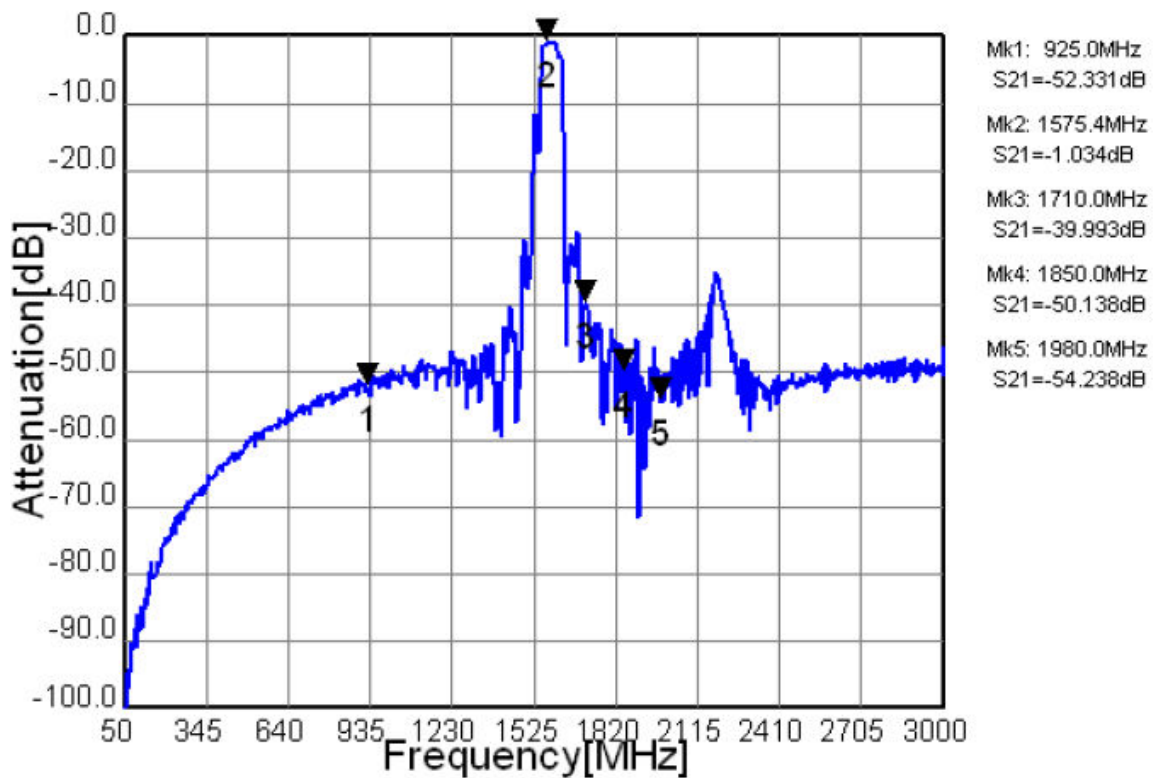


Fig.4 Wide-band Characteristics



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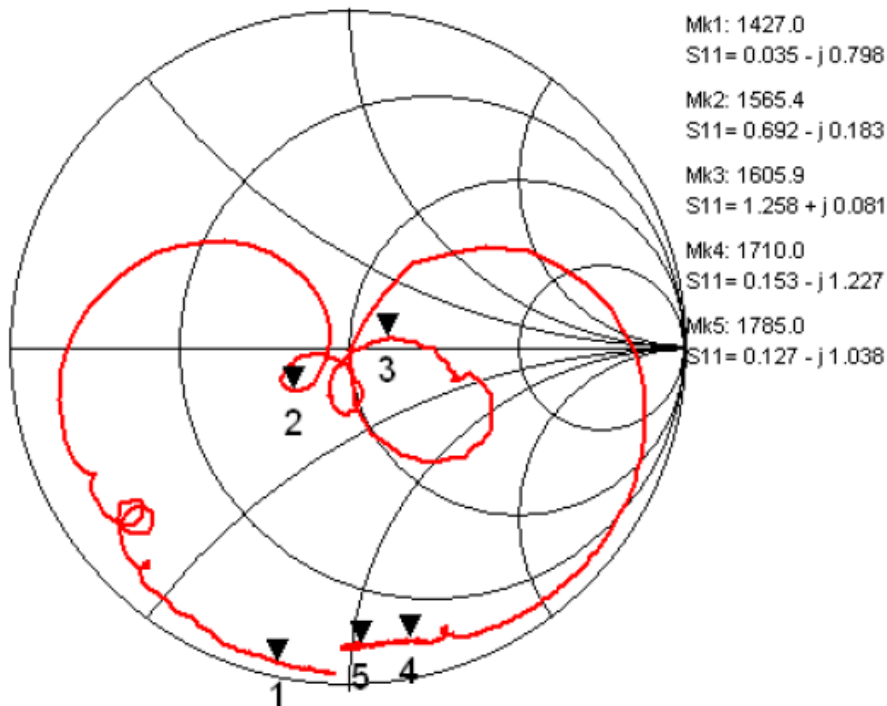


Fig.5 Input Impedance

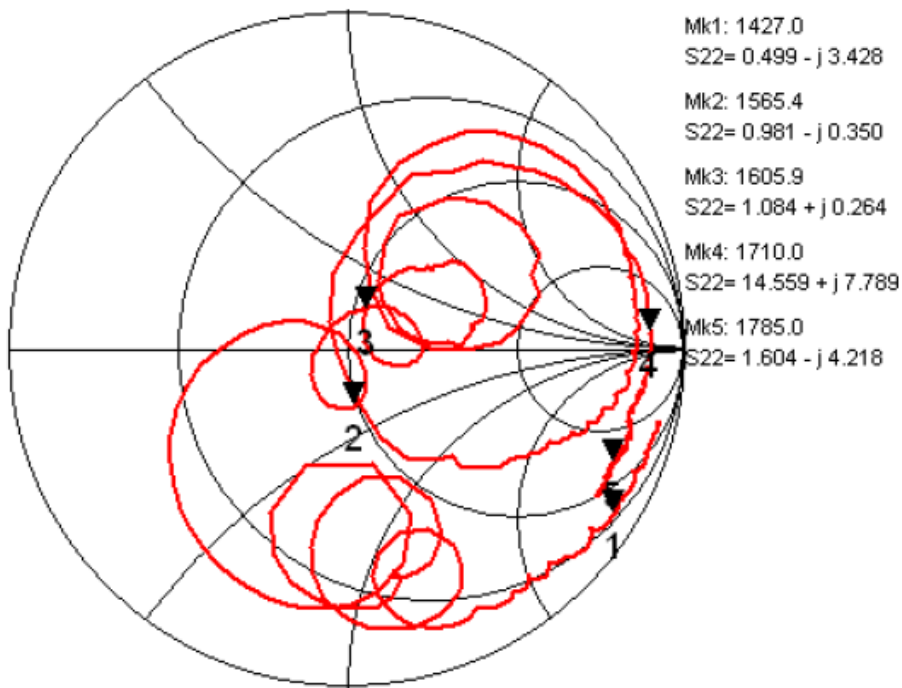


Fig.6 Output Impedance



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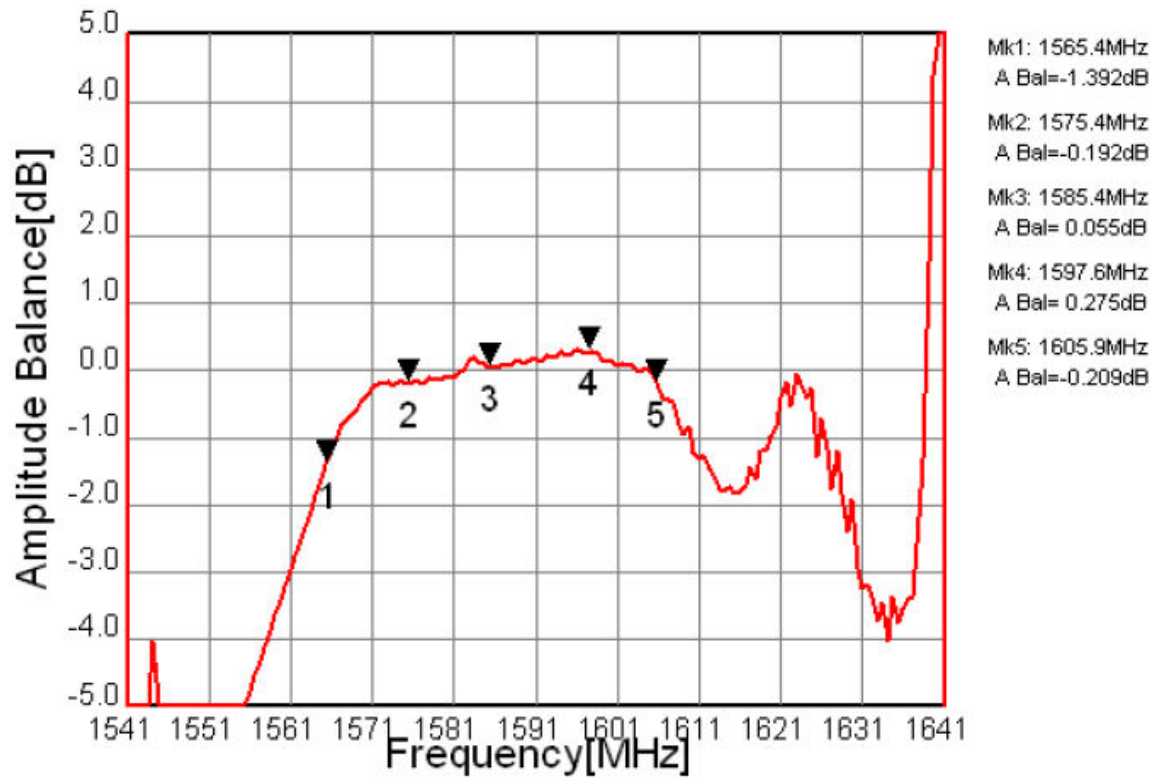


Fig.7 Amplitude Balance

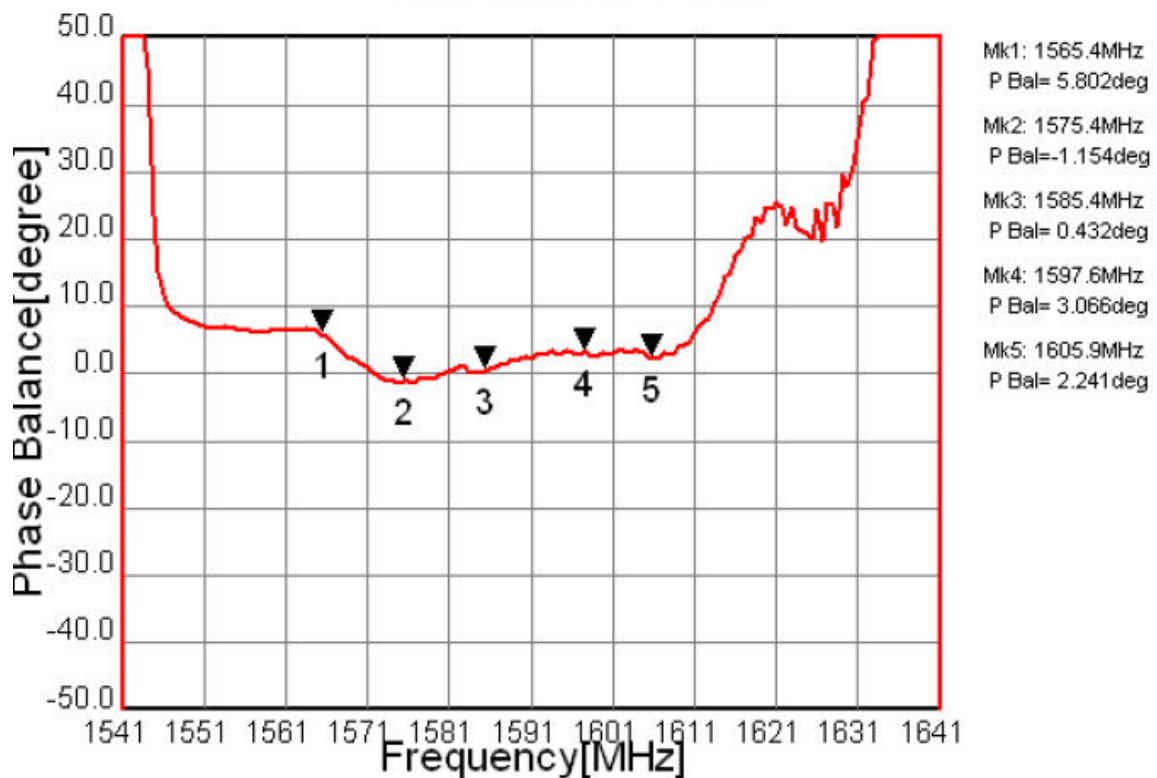


Fig.8 Phase Balance