



ILIM1	ILIM0	CURRENT LIMIT
0	0	100mA (1X)
0	1	1A (10X)
1	0	0.5mA (SUSP)
1	1	500mA (5X)

MODE	BUCKS	BUCK-BOOST	BOOST
0	Pulse Skip	PWM	Pulse Skip
1	Burst	Burst	Pulse Skip

Unless noted:
Resistors: Ohms, 0402, 1%, 1/16W
Capacitors: 0402, 10%, 6.3V

CUSTOMER NOTICE		CONTRACT NO.	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS DRAWN: J.Drew	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		CHECKED: J.Drew	
		ENGINEER: J.Drew	
		DESIGNER: J.Drew	
		TITLE: SCHEMATIC	LTC3588EUF
		APPROVED: J.Drew	High Efficiency USB Power Manager + Dual Buck + Boost + Buck - Boost DC / DC
		DATE: Monday, July 21, 2008	
		SIZE: B	DWG NO. DC1306A
			REV: A
			SHEET 1 OF 1