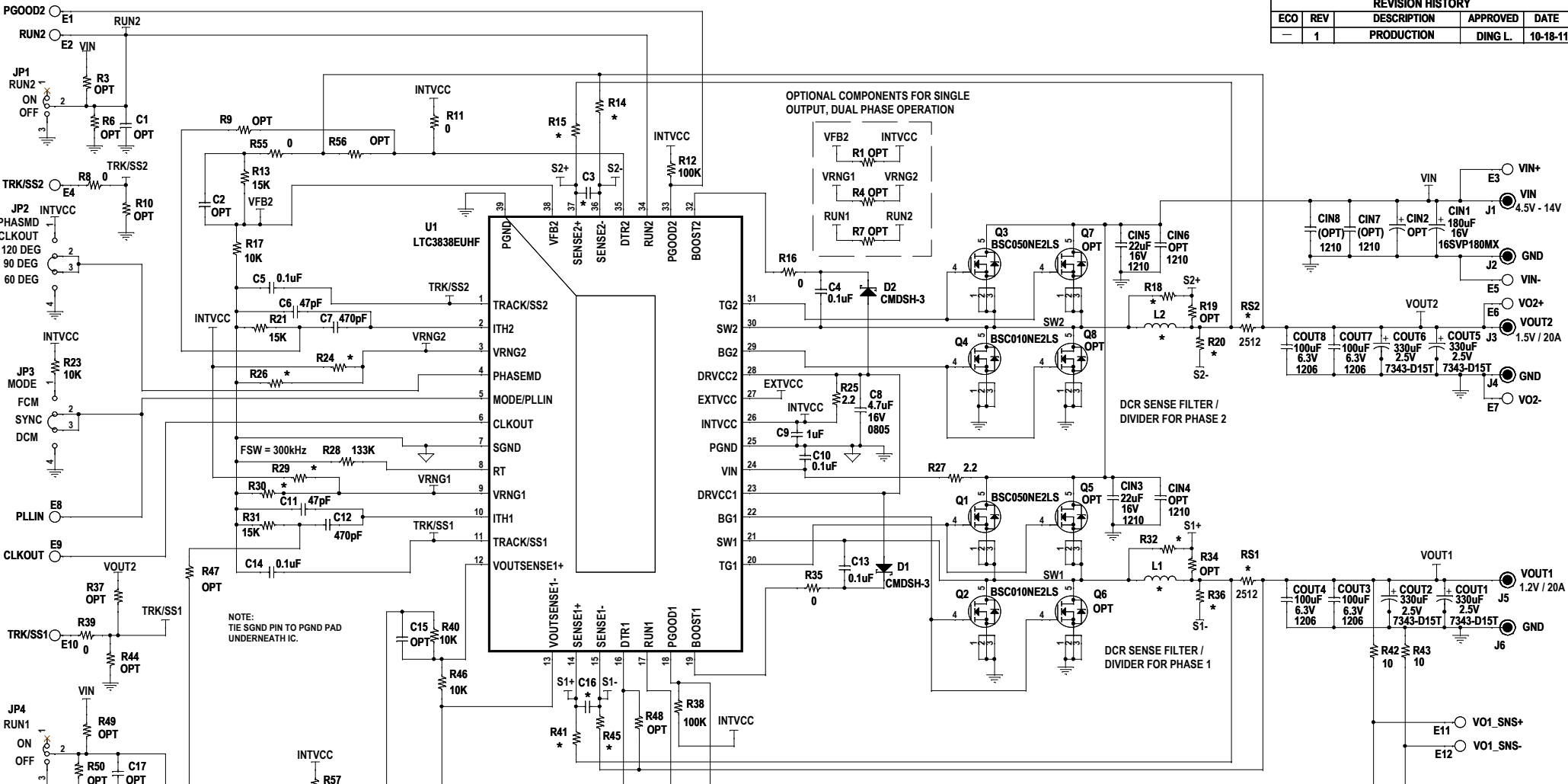


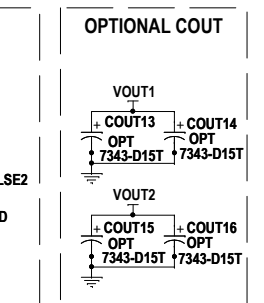
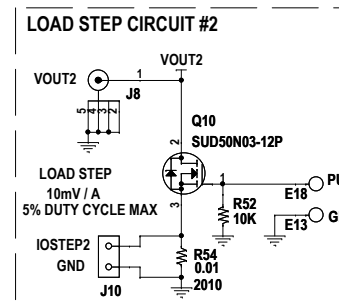
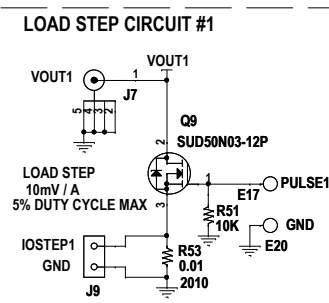
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION	DING L.	10-18-11



NOTE: TIE SGND PIN TO PGND PAD UNDERNEATH IC.

- NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE 0603.
 2. ALL CAPACITORS ARE 0603.
 3. INSTALL SHUNTS ON JUMPERS AS SHOWN.
 4. TIE DTR1, DTR2 TOGETHER FOR 2-PH SINGLE VOUT CASE.

ASSY	L1,L2	C3, C16	R18, R32	R20, R36	R14,R15, R41,R45	R24, R29	R26, R30	RS1, RS2
-A (DCR SENSE)	WURTH 744355047 (0.47uH)	0.22uF	2.15K	0	OPT	OPT	0	0.000 Ohm 2512 TEPRO RN5326
-B (RSENSE)	COILTRONICS FP1308R3-R44-R (0.44uH)	1000pF	OPT	OPT	10	0	OPT	0.002 Ohm 2512 VISHAY WSL2512L000FEA



CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	JW
APP ENG.	DING L.
SCALE	NONE

LINEAR TECHNOLOGY

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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
HIGH EFFICIENCY DUAL OUTPUT STEP-DOWN DC-DC CONVERTER

SIZE: N/A IC NO. **LTC3838EUHF** REV. 1
DEMO CIRCUIT 1801A

DATE: 03-10-11 SHEET 1 OF 1