



2206211281 MGM13P and BGM13P Datasheet Release v1.3

PCN Issue Date: Jun 21, 2022

Effective Date: Sep 27, 2022

PCN Type: Datasheet

Description of Change

Silicon Labs is pleased to announce the release of datasheet version 1.3 for MGM13P and BGM13P devices.

Notable changes from previous revisions are:

- Added timing specifications for RESETh low time in Table 4.22 General-Purpose I/O (GPIO) on page 32.
- Removed BIASPROG = 1, FULLBIAS = 0 specifications from Table 4.25 Analog Comparator (ACMP) on page 36.
- Clarified the dimensions in Figure 9.4 MGM13P Recommended PCB Land Pattern on page 94. (MGM13P only)
- Removed all references to RFSENSE.
- Removed all references to BOOT_TX and BOOT_RX. (MGM13P only)

Reason for Change

Improve our production capability

Impact on Form, Fit, Function, Quality, Reliability

There is no change in form, fit, quality and reliability. Functionality is impacted as listed in change description.

Product Identification

Existing Part #
BGM13P22F512GA-V2
BGM13P22F512GA-V2R
BGM13P22F512GE-V2
BGM13P22F512GE-V2R
BGM13P32F512GA-V2
BGM13P32F512GA-V2R
BGM13P32F512GE-V2
BGM13P32F512GE-V2R
MGM13P02F512GA-V2
MGM13P02F512GA-V2R
MGM13P02F512GE-V2
MGM13P02F512GE-V2R
MGM13P12F512GA-V2
MGM13P12F512GA-V2R
MGM13P12F512GE-V2
MGM13P12F512GE-V2R

Last Date of Unchanged Product: Sep 27, 2022

Qualification Samples

NA

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

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Qualification Data

NA



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Silicon Laboratories Inc.
400 West Cesar Chavez
Austin, TX 78701

<http://www.silabs.com>