

REVISION HISTORY		
06/23/2011	1	INITIAL RELEASE
09/26/2011	1.1	REMOVE LCD_FETES; Q4 & Q5, SIMPLIFY TP_SIGNAL_NAMES REMOVE I2C_SIGNALS OR ALS_SENSOR
04/09/2012	2	CHANGE WFI TO_GAINSPAN, EXPAND ALT_FWR/DRM, SEPARATE RESETOUT
08/20/2012	2.1	CHANGE D10 TO BE LOADED

U11	Pin	Signal	Notes
[4] P0_0_RD1_TXD3_TXD0	94	TXD0	
[4] P0_1_TXD1_RXD3_RXD0	95	TXD0	
[4] P0_2_TXD0	96	TXD0	
[4] P0_3_RXD0	97	TXD0	
[4] P0_4_028RX_CLK_RXD0_CAPD1	98	TXD0	
[4] P0_5_028RX_WS_T02_CAP2.1	99	TXD0	
[4] P0_6_28RX_SDA_SSEL2_MAT2.1	100	TXD0	
[4] P0_7_28TX_CLK_SCK1_MAT2.1	101	TXD0	
[4] P0_8_28TX_WS_MISO1_MAT2.2	102	TXD0	
[4] P0_9_28TX_SDA_SSEL1_MAT2.1	103	TXD0	
[4] P0_10_TXD2_SDA2_MAT3.0	104	TXD0	
[4] P0_11_RXD2_SCL2_MAT3.1	105	TXD0	
[3,4] P0_12_USB1PWR2_ADD6	106	TXD0	
[3,4] P0_13_USB2_ULPD_ADD7	107	TXD0	
[3] USB2_C0N	108	TXD0	
[4,5] P0_15_TXD1	109	TXD0	
[4,5] P0_16_RXD1	110	TXD0	
[4] P0_17_CTS1	111	TXD0	
[4] P0_18_VDD1	112	TXD0	
[4] P0_19_DSR1_SDA1_MCICLK	113	TXD0	
[4] P0_20_DIR1_SCK1_MCICMD	114	TXD0	
[4] P0_21_RAMP_MODE	115	TXD0	
[4] P0_22_MCICMD_RT151	116	TXD0	
[4,5] TP_X1_P0_23_ADD0	117	TXD0	
[4,5] TP_Y2_P0_24_ADD1	118	TXD0	
[4] P0_25_ADD2_TXD3	119	TXD0	
[4] P0_26_ADD3_AOUT_R0D3	120	TXD0	
[3] USB2_OVC	121	TXD0	
[4] P0_28_USB1_DP_ENT0	122	TXD0	
[4] P0_30_USB1_DM_ENT1	123	TXD0	
[3] USB2_DP	124	TXD0	
[3] USB2_DM	125	TXD0	
[4] P1_0_ENET_TXD0	126	TXD0	
[4] P1_1_ENET_TXD1	127	TXD0	
[3] BL_PWM	128	TXD0	
[4,5] P1_3_MCICMD_PWM0.2	129	TXD0	
[4,5] P1_4_ENET_TXEN	130	TXD0	
[4,5] P1_5_MCPWR_PWM0	131	TXD0	
[4,5] P1_6_MCIDAT0_PWM0	132	TXD0	
[4,5] P1_7_MCIDAT1_PWM0	133	TXD0	
[4] P1_8_ENET_CRSD0	134	TXD0	
[4] P1_9_ENET_RXD0	135	TXD0	
[4] P1_10_ENET_RXD1	136	TXD0	
[4,5] P1_11_MCIDAT3_PWM0.6	137	TXD0	
[4] P1_12_MCIDAT3_PCAP0.6	138	TXD0	
[4] P1_14_ENET_RX_ER	139	TXD0	
[4] P1_15_ENET_REFCLK	140	TXD0	
[4] P1_16_ENET_MDC	141	TXD0	
[4] P1_17_ENET_MDIO	142	TXD0	
[3] TP_Y1	143	TXD0	
[4] USB1_PPWR	144	TXD0	
[3] LCDV010	145	TXD0	
[3] LCDV011	146	TXD0	
[3] LCDV012	147	TXD0	
[3] LCDV013	148	TXD0	
[3] LCDV014	149	TXD0	
[3] LCDV015	150	TXD0	
[3] LCDV016	151	TXD0	
[3] LCDV017	152	TXD0	
[3] LCDV018	153	TXD0	
[3] LCDV019	154	TXD0	
[3] LOCAL_SDA	155	TXD0	
[3,5] LOCAL_SCL	156	TXD0	
[3] CAS	157	TXD0	
[2] RAS	158	TXD0	
[2] CLK	159	TXD0	
[5] TP_X2	160	TXD0	
[2] SDRAM_CS#	161	TXD0	
[5] SCK0	162	TXD0	
[5] WFI_CS	163	TXD0	
[2] CAS	164	TXD0	
[3] V3_ER	165	TXD0	
[5] MISO0	166	TXD0	
[5] MISO1	167	TXD0	
[2] DMG0	168	TXD0	
[2] DMG1	169	TXD0	
[2] DMG2	170	TXD0	
[2] DMG3	171	TXD0	

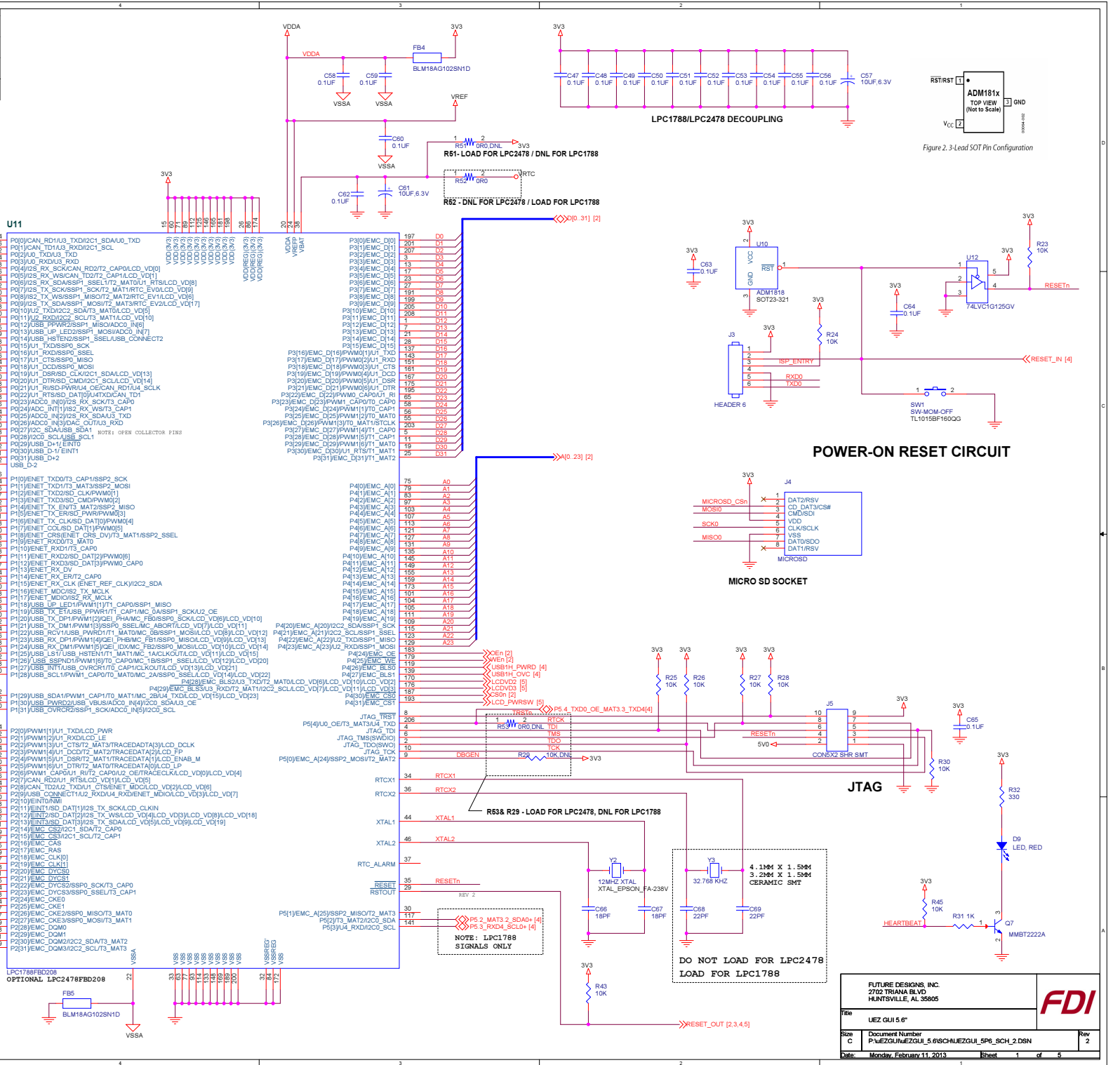


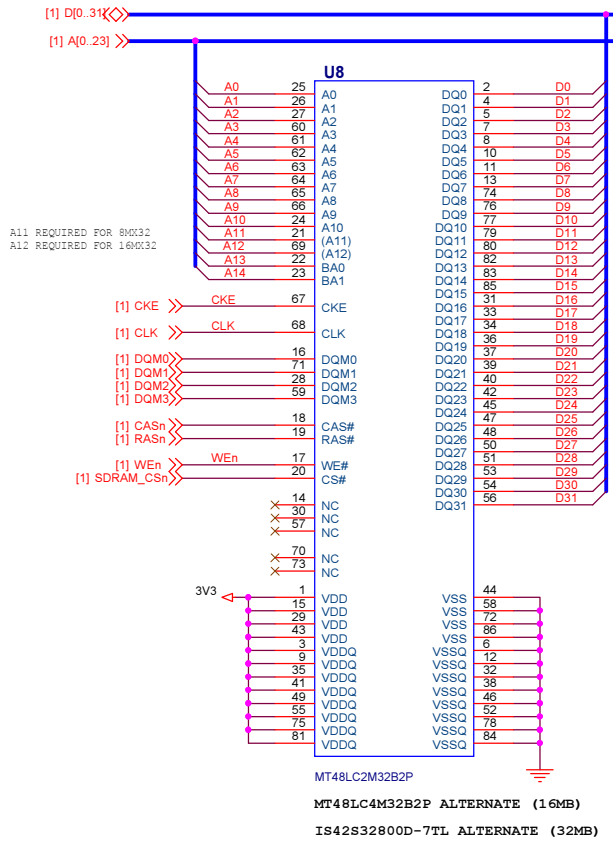
Figure 2. 3-Lead SOT Pin Configuration

POWER-ON RESET CIRCUIT

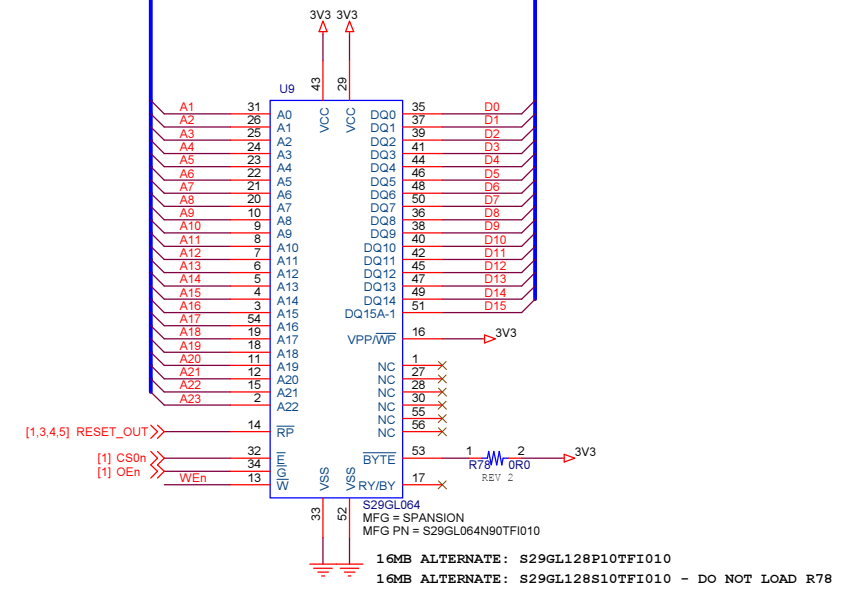
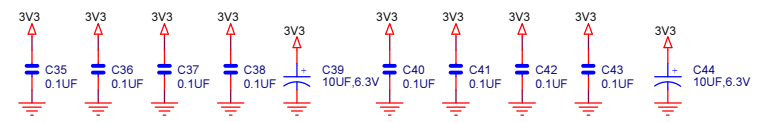
MICRO SD SOCKET

JTAG

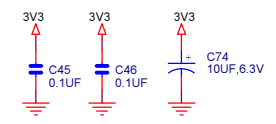
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File LEZ GUI 5 6"	Document Number P-UEZ01UEZGUI_5_6SCHUEZGUI_5P6_SCH_2.DSN	
Date Monday, February 11, 2013	Sheet 1 of 5	Rev 2



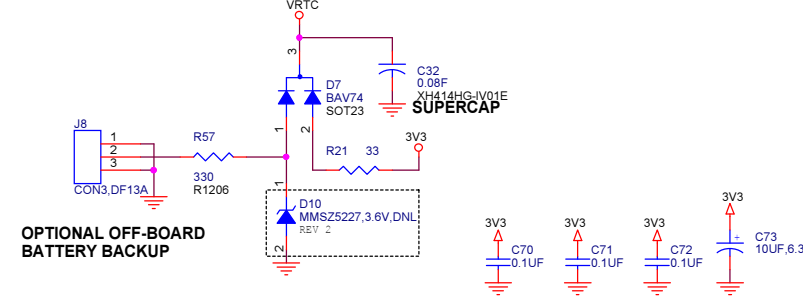
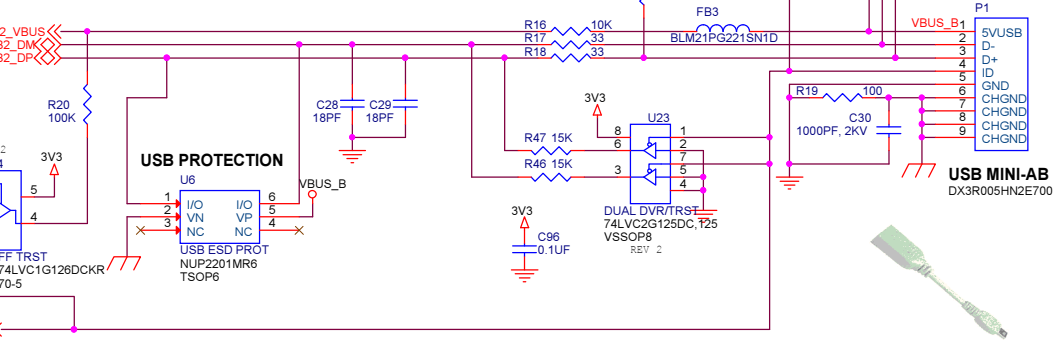
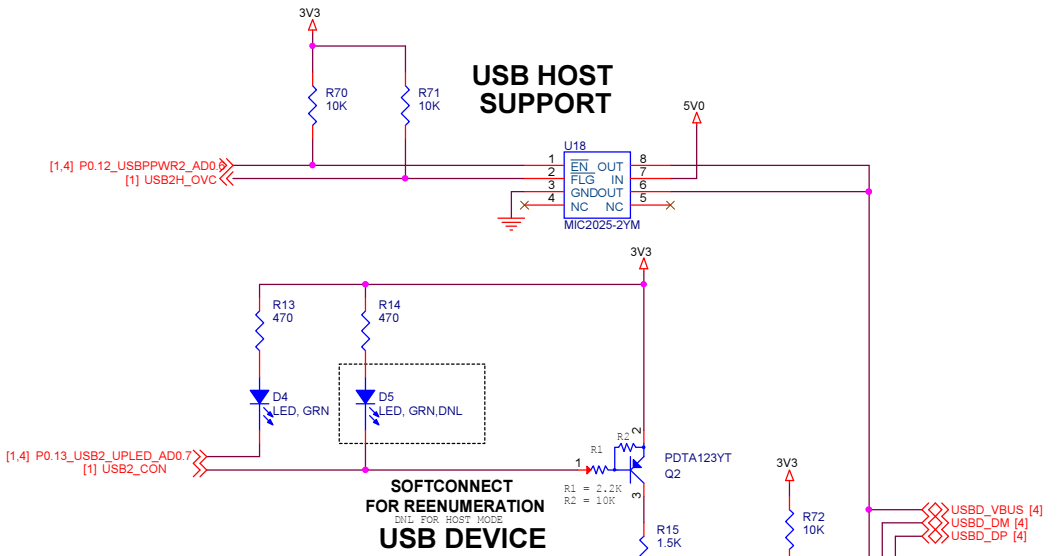
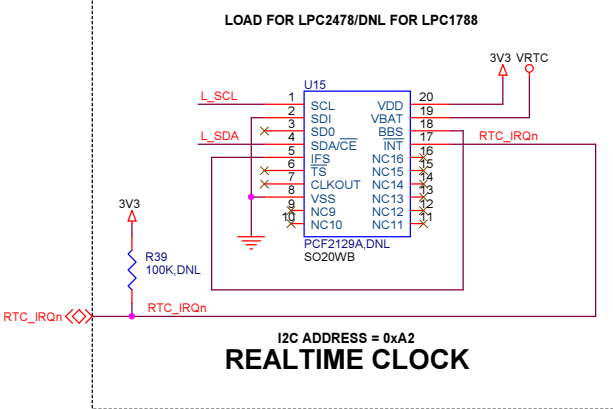
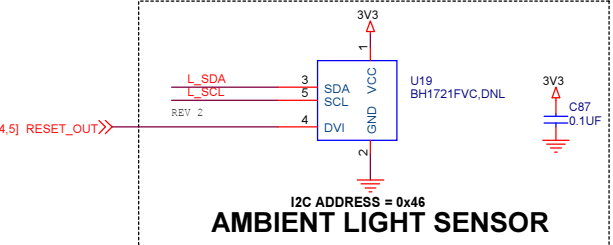
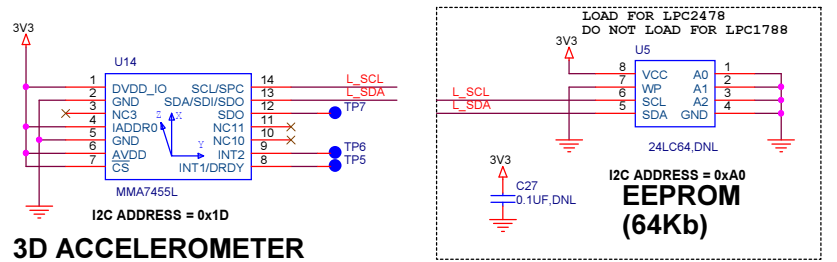
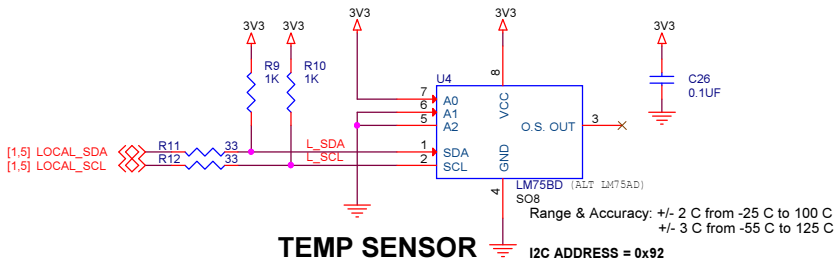
SDRAM (8MB TO 32MB)



NOR FLASH (8MB TO 16MB)



FUTURE DESIGNS, INC. 2702 TRIANA BLVD HUNTSVILLE, AL 35805		
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Date: Tuesday, June 17, 2014	Sheet 2 of 5	



TO CONVERT FROM USB MINI-AB TO USB A UTILIZE CABLE SHOWN BELOW, AVAILABLE FROM DIGI-KEY, PN: 10-00003-ND

FUTURE DESIGNS, INC. 996 A CLEANER WAY HUNTSVILLE, AL 35805		
Title: UEZ GUI 5.6"		
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Date: Monday, April 08, 2013	Sheet: 3 of 5	

**POWER INPUT ONLY
USB MINI-B
(NO USB FUNCTION)**

**ALTERNATE POWER &
COMM INTERFACE**
HIROSE DF13

3.3V REGULATOR, 0.8A

WALL 5V INPUT MONITOR VIA ADC


SPEAKER AMP

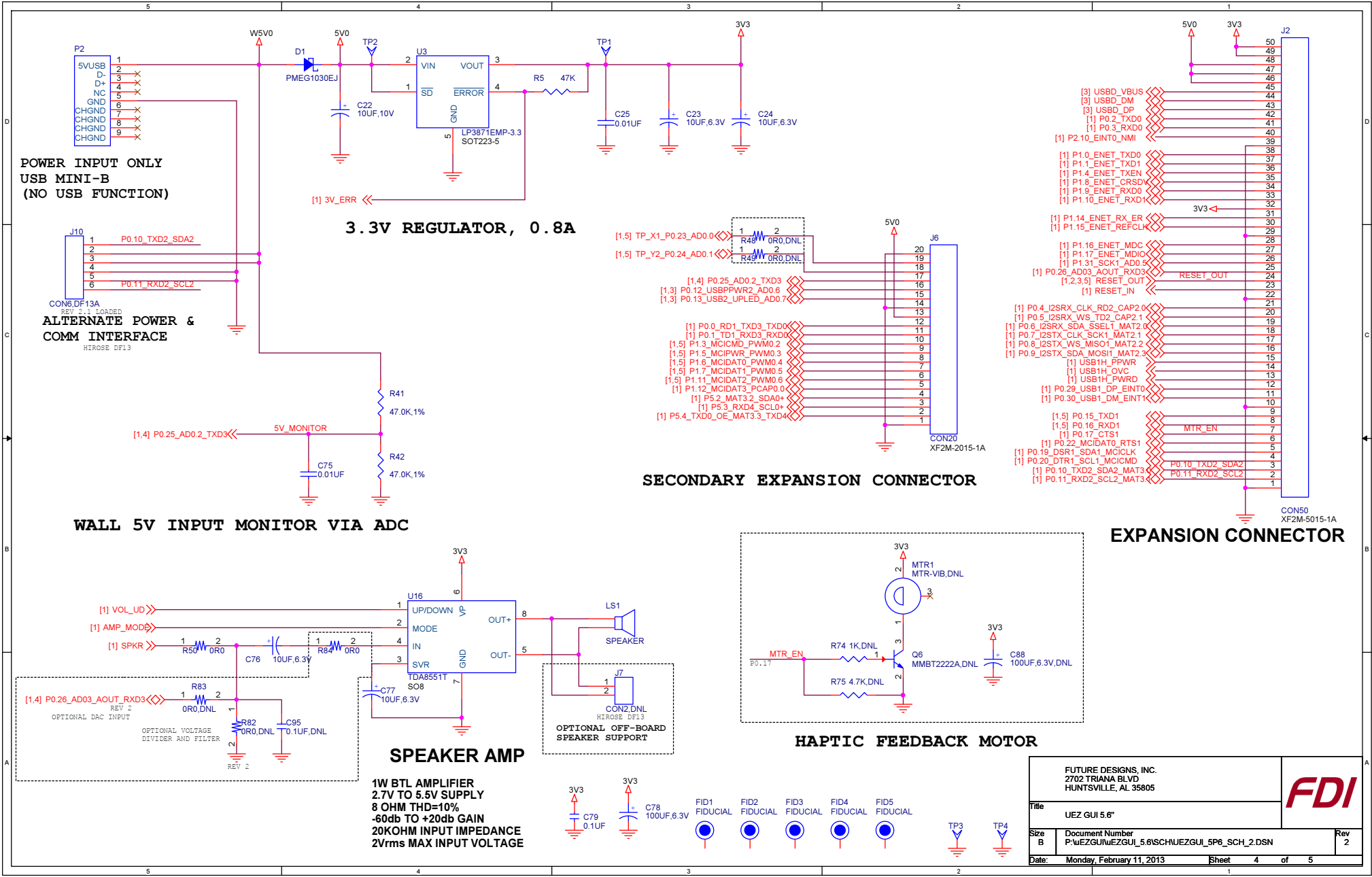
**1W BTL AMPLIFIER
2.7V TO 5.5V SUPPLY
8 OHM THD=10%
-60db TO +20db GAIN
20KOHM INPUT IMPEDANCE
2Vrms MAX INPUT VOLTAGE**

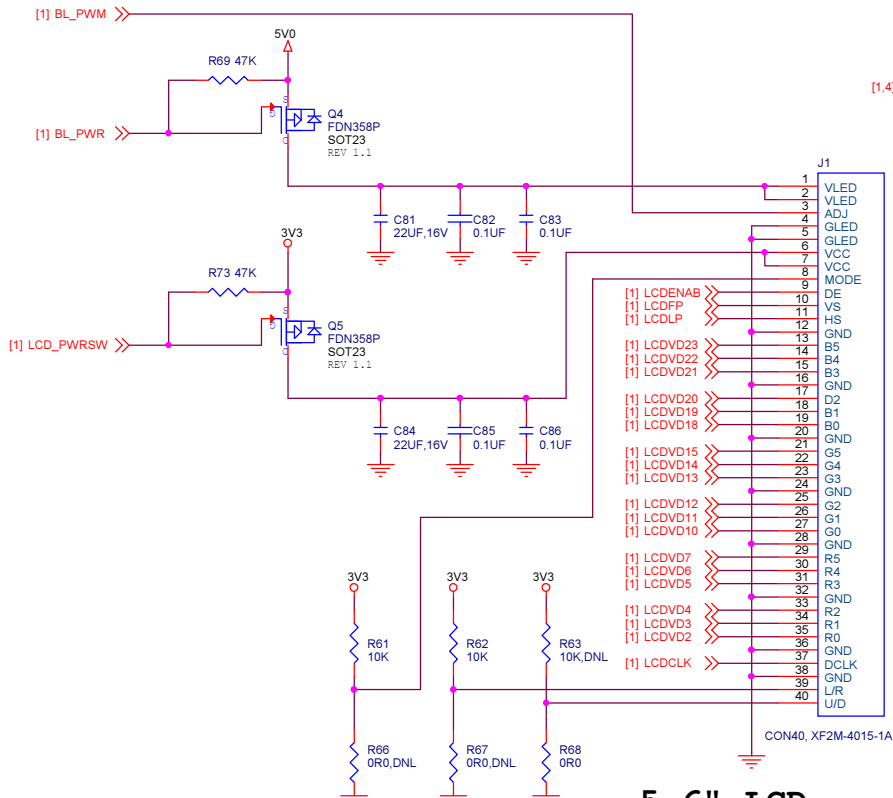
SECONDARY EXPANSION CONNECTOR

HAPTIC FEEDBACK MOTOR

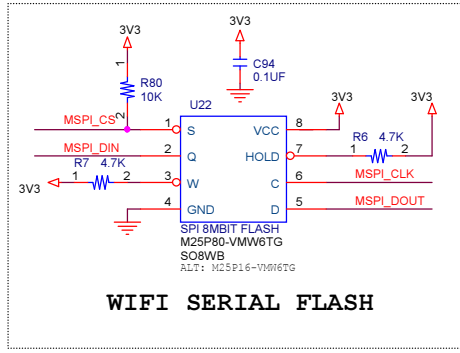
EXPANSION CONNECTOR

FUTURE DESIGNS, INC. 2702 TRIANA BLVD HUNTSVILLE, AL 35805		
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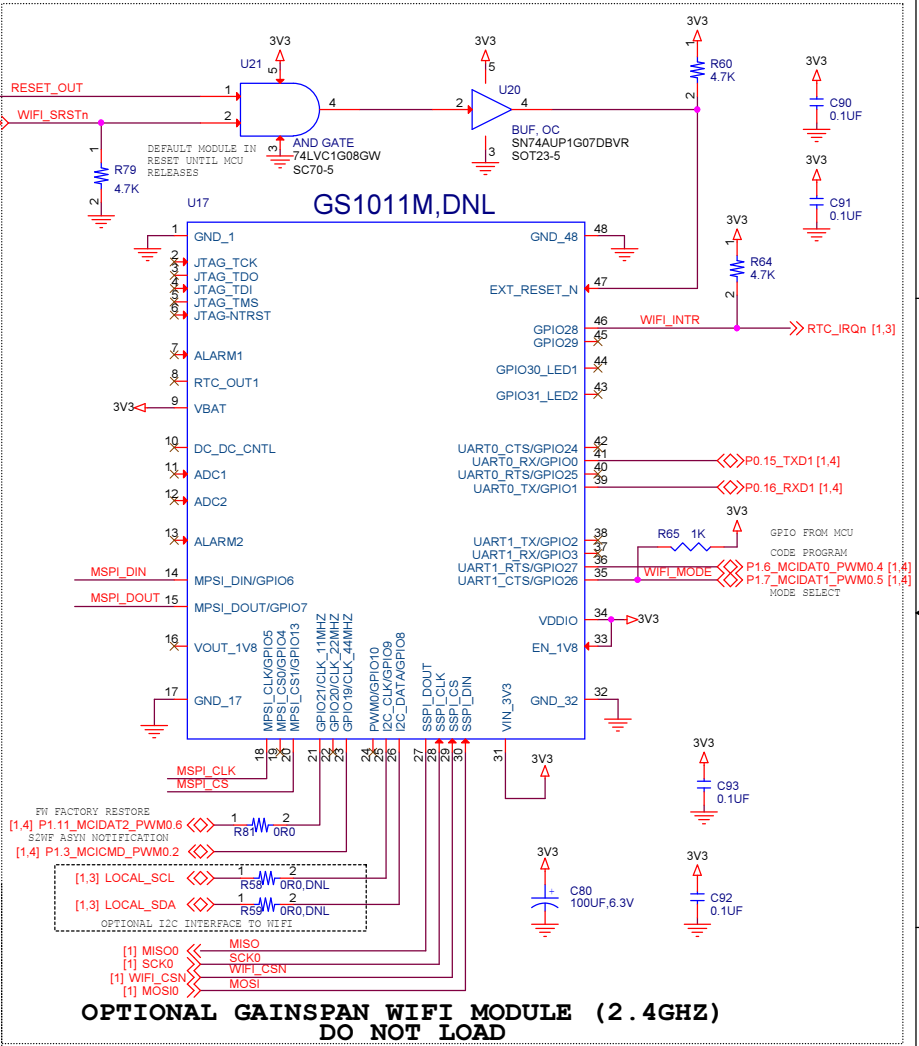


5.6" LCD



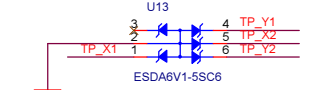
WIFI SERIAL FLASH

[1,2,3,4] RESET_OUT
[1,4] P1.5_MCIPWR_PWM0.3
WIFI SOFTWARE RESET

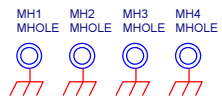
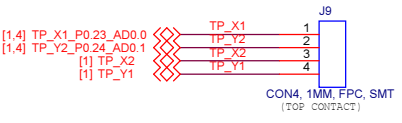


**OPTIONAL GAINSPAN WIFI MODULE (2.4GHZ)
DO NOT LOAD**

- GAINSPAN WIFI IMPLEMENTATION:
- 1) PRIMARY INTERFACE IS SPI
 - 2) SUPPORT OPTIONAL UART MODE VIA P1.7
 - 3) SUPPORT FW UPDATE VIA P1.6
 - 4) SUPPORT SW RESET VIA P1.5 (LOW TRUE)
 - 5) OPTIONAL I2C INTERFACE
 - 6) ALWAYS POWERED ON



TP ESD PROTECTION



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