U2	Assy	R15	R16
LTM9003-AA	LTM9003-AA	DNI	0 ohm
LTM9003-AB	LTM9003-AB	0 ohm	DNI

C42:
For 2, 2.3, 2.5 GHz: DNI
For 3.5 GHz: install 1.5pF

C43:
For 2, 2.3, 2.5 GHz: install 1.0pF
For 3.5 GHz: DNI

L1:
For 2 GHz: install 3.9nH
For 2.3 GHz: install 2.7nH
For 2.5 GHz: install 2.2nH
For 3.5 GHz: install 0 ohm

L5:
For 2 GHz: install 2.7nH
For 2.3, 2.5, 3.5 GHz: install 1.0nH

C39:
For 2 GHz: install 0.5pF
For 2.3, 2.5, 3.5 GHz: install 0.5pF

ON BACK OF BOARD
ENCODE CLK
MABA-007159-000000
LT1763CDE
LTM9003IV-xx
FIN1108
ALTERA_HSME_DE_CARD
24LC02ST
JMPR-2X1-PS-2MM
SOLDER_JMPR-1
2802S-06G2

REVISION HISTORY

ECO	REV	DESCRIPTION	DATE	APPROVED
	A2	INITIAL RELEASE	24NOV09	

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMERS RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS

CONTRACT NO.

APPROVALS

DRAWN	E.TRELEWICZ
CHECKED	
APPROVED	
ENGINEER	E.TRELEWICZ
DESIGNER	

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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
PRE-DISTORTION RECEIVER [LVDS OUTPUT]

SIZE	DWG NO.	REV:
B	DC1642	A

FILENAME: 1642A

DATE: 6/8/2011 8:21:25 AM

SHEET 1 OF 1