

## C1206C475K8PACTU

Aliases (C1206C475K8PAC7800)

SMD Comm X5R, Ceramic, 4.7 uF, 10%, 10 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1206



Click [here](#) for the 3D model.

Dimensions	
Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
T	1.6mm +/-0.20mm
B	0.5mm +/-0.25mm

  

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2000

General Information	
Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	41 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	4.7 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	10 VDC
Dielectric Withstanding Voltage	25 VDC
Temperature Range	-55/+85°C
Temperature Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 48 Hours
Insulation Resistance	21.3 MOhms

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