User's Guide

TPS62830xDRLEVM Evaluation Module

ABSTRACT

This user's guide describes the characteristics, operation, and use of TI's TPS62830xDRL evaluation modules (EVM). These EVMs are designed to help the user to easily evaluate and test the operation and functionality of the TPS628303A and TPS628303B buck converters in their SOT583 package. The EVMs convert a 2.25-V to 5.5-V input voltage to a regulated 1.8-V output voltage that delivers up to 3-A maximum. This user's guide includes setup instructions for the following:

- Hardware
- A printed-circuit board (PCB) layout
- Schematic diagram
- Bill of materials (BOM)
- Test results of the EVM

Throughout this document, the TPS62830xDRLEVM is used as an abbreviation representing the TPS628303ADRLEVM and also TPS628303BDRLEVM.

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Trademarks

1 Warning and Caution

Caution

Caution Hot surface.
Contact may cause burns.
Do not touch!

2 Introduction

The TPS628303A and TPS628303B are synchronous step-down buck DC-DC converters with integrated noise filtering capacitors, optimized for excellect EMI performance. Based on the DCS-Control topology, they provide a fast transient response with small output capacitance. The TPS628303A and TPS628303B delivers an output current up to 3A. The TPS628303B device option has a latch-off protection for short circuit as well as over voltage incidents. The TPS628303A and TPS628303B are available in a 1.6-mm × 2.1-mm SOT583 package as well as a 1.0-mm × 2.0-mm QFN package. The TPS62830xDRLEVM uses the SOT583 package.

2.1 Performance Specification

Table 1-1 provides a summary of the TPS628303A and TPS628303B performance specifications.

Table 2-1. Performance Specification Summary

Specification		Test Conditions	MIN	TYP	MAX	Unit
Input voltage			2.25		5.5	V
Output voltage setpoint				1.8		V
Output current	TPS628303ADRLEVM		0		3	A
	TPS628303BDRLEVM		0		3	A

2.2 Dual Package Layout

Because both the QFN and SOT583 packages have the same pinout, there is also the possibility for board designers to overlap both package footprints like in Figure 2-1. This overlap gives more flexibility to switch between packages when there is shortage in supply of one. The TPS62830xDRLEVM does not have this overlap and is only designed for the SOT583 package.

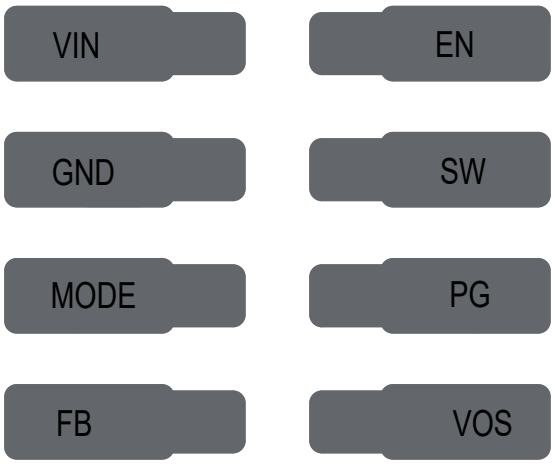

Figure 2-1. Overlapped QFN and SOT583 Footprints

2.3 Modifications

The printed-circuit board (PCB) for this EVM is designed to accommodate the different output current versions of this integrated circuit (IC). On the EVM, additional input and output capacitors can be added, and the default output voltage can be changed as well. Finally, the loop response of the IC can be measured.

2.3.1 Input and Output Capacitors

C2 and C3 are provided for additional input capacitors. These capacitors are not required for proper operation but can be used to reduce the input voltage ripple.

C9 is provided for an additional output capacitor. This capacitor is not required for proper operation but can be used to reduce the output voltage ripple and to improve the load transient response. The output capacitance must remain within the recommended range in the device data sheet for proper operation.

2.3.2 Loop Response Measurement

The loop response of the TPS62830xDRLEVM can be measured by cutting the trace parallel to R3 and assembling a 50-Ω resistor as R3 to inject the measurement signal across.

3 Setup

This section describes how to properly use the TPS62830xDRLEVM.

3.1 Connector Descriptions

J1, Pin 1 and 2 – VIN	Positive input voltage connection from the input supply for the EVM
J1, Pin 3 and 4 – S+/S–	Input voltage sense connections, measure the input voltage at this point
J1, Pin 5 and 6 – GND	Input return connection from the input supply for the EVM
J2, Pin 1 and 2 – VOUT	Positive output voltage connection
J2, Pin 3 and 4 – S+/S–	Output voltage sense connections, measure the output voltage at this point
J2, Pin 5 and 6 – GND	Output return connection
J3 – PG/GND	The PG output appears on pin 1 of this header with a convenient ground on pin 2.
JP1 – EN	EN pin jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.
JP2 – MODE	MODE pin jumper. Place the supplied jumper across VIN and MODE to force the device in fixed frequency PWM operation at all load currents. Place the jumper across MODE and GND to enable power save mode.

3.2 Hardware Setup

To operate the EVM, set jumpers JP1 to the desired positions per *Connector Descriptions*. Connect the input supply to J1, and connect the load to J2.

4 TPS62830xDRLEVM Test Results

The TPS62830xDRLEVM was used to take the data in the TPS62830x data sheet for the SOT583 package. See the device data sheet for the performance of this EVM.

5 Board Layout

This section provides the TPS62830xDRLEVM board layout and illustrations in Figure 5-1 through Figure 5-6.

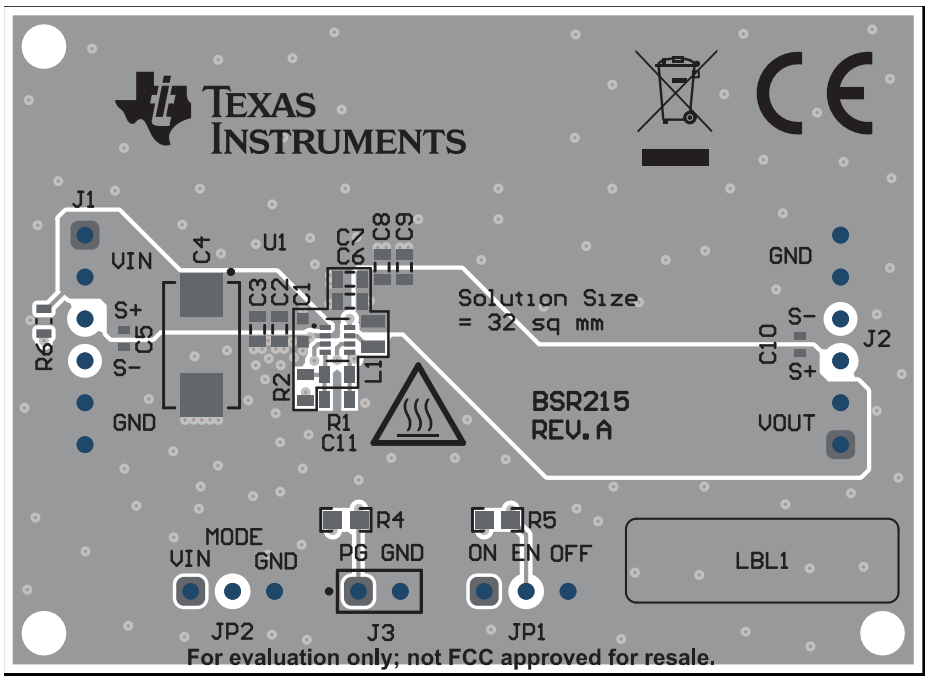

Figure 5-1. Top Assembly

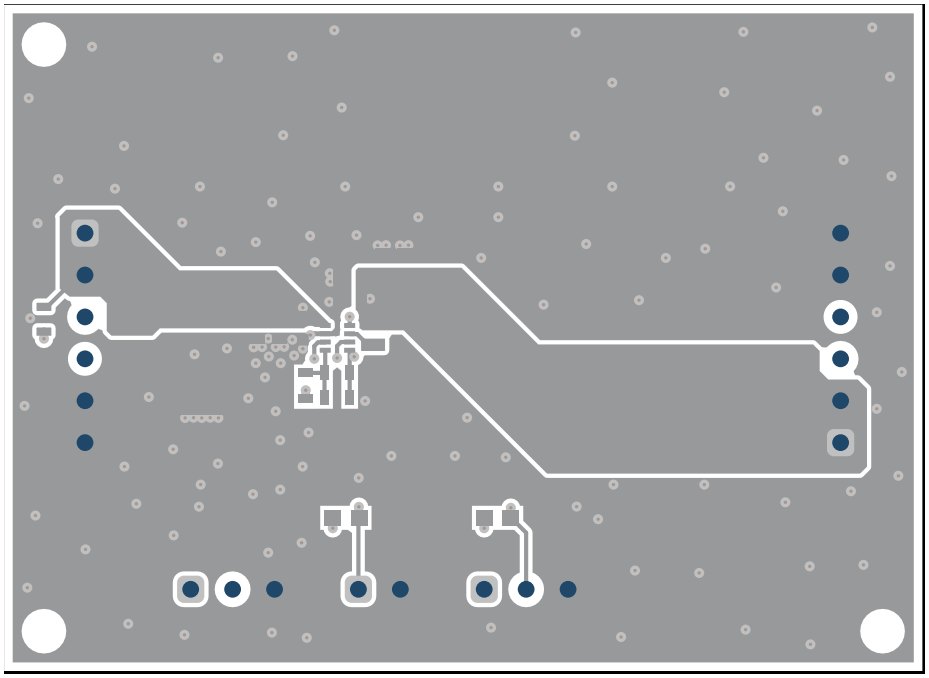

Figure 5-2. Top Layer

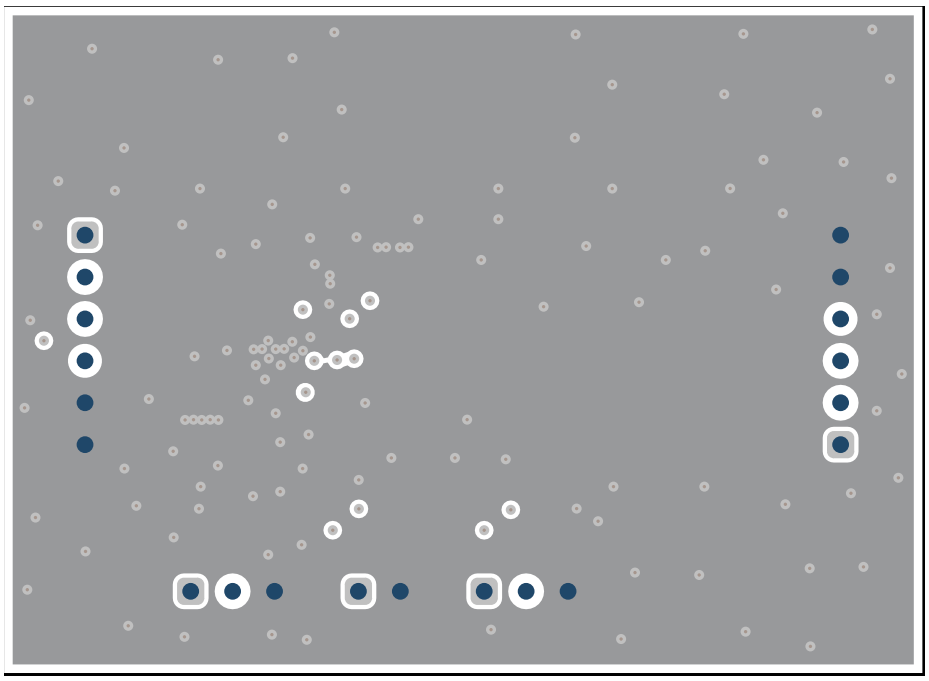

Figure 5-3. Signal Layer 1

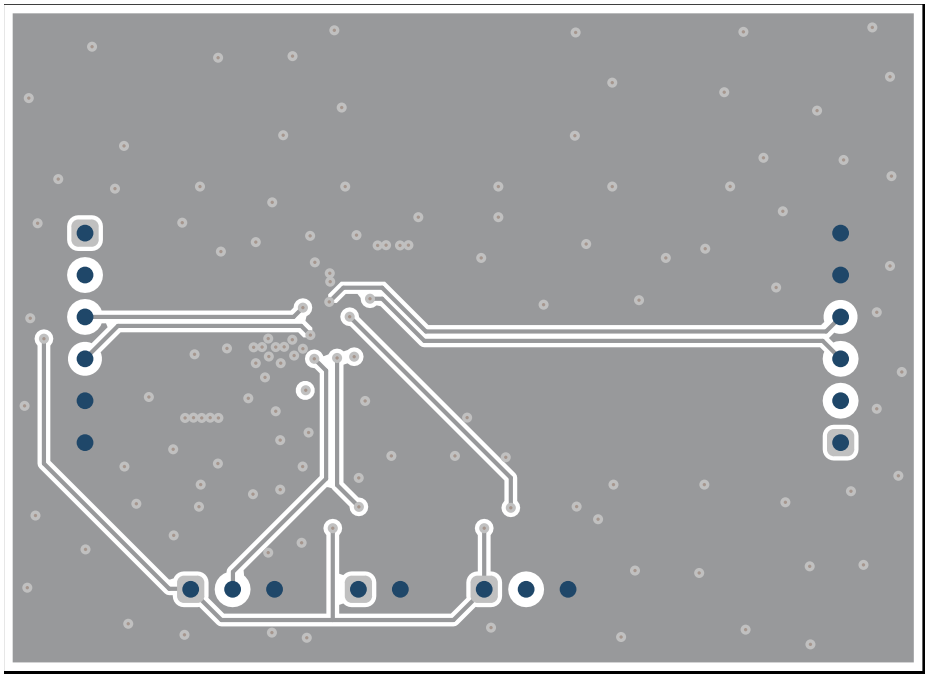

Figure 5-4. Signal Layer 2

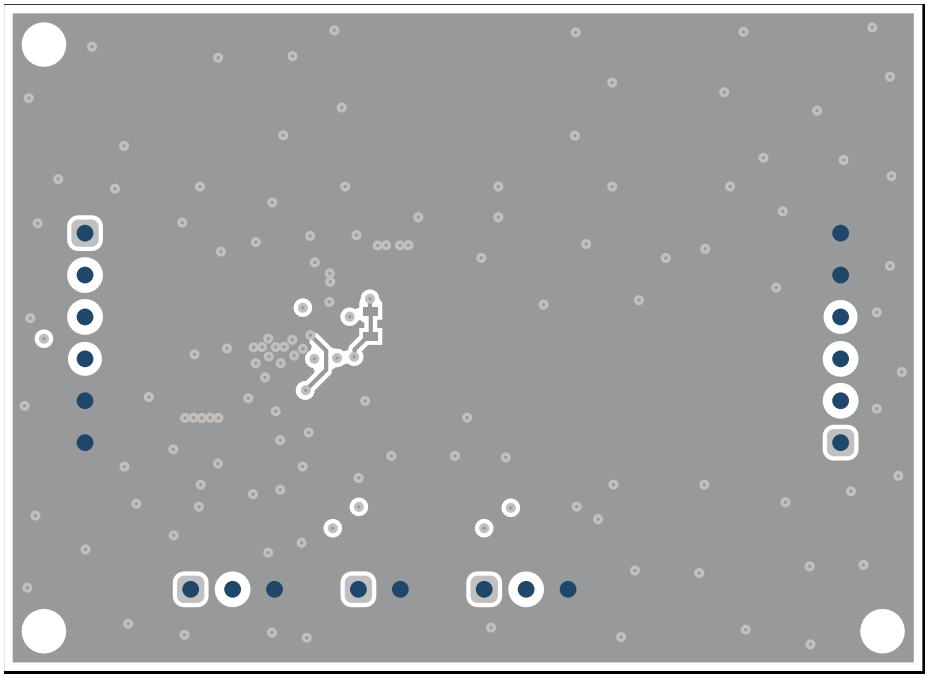

Figure 5-5. Bottom Layer

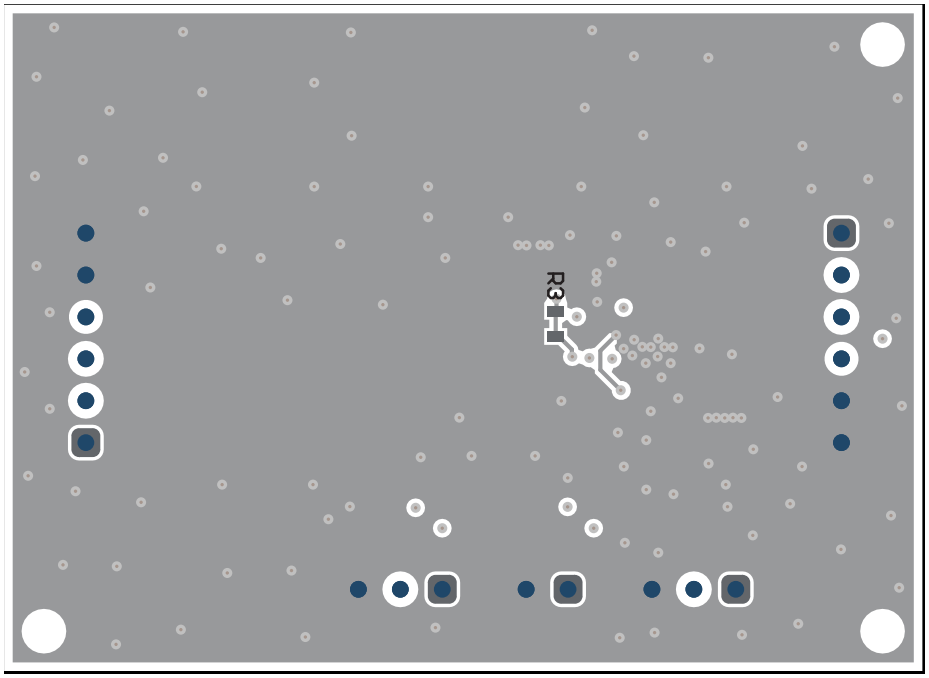

Figure 5-6. Bottom Assembly

6 Schematic and Bill of Materials

This section provides the TPS62830xDRLEVM schematic and bill of materials.

6.1 Schematic

Figure 6-1 illustrates the EVM schematic of TPS628303ADRLEVM which is valid for the other variants as well

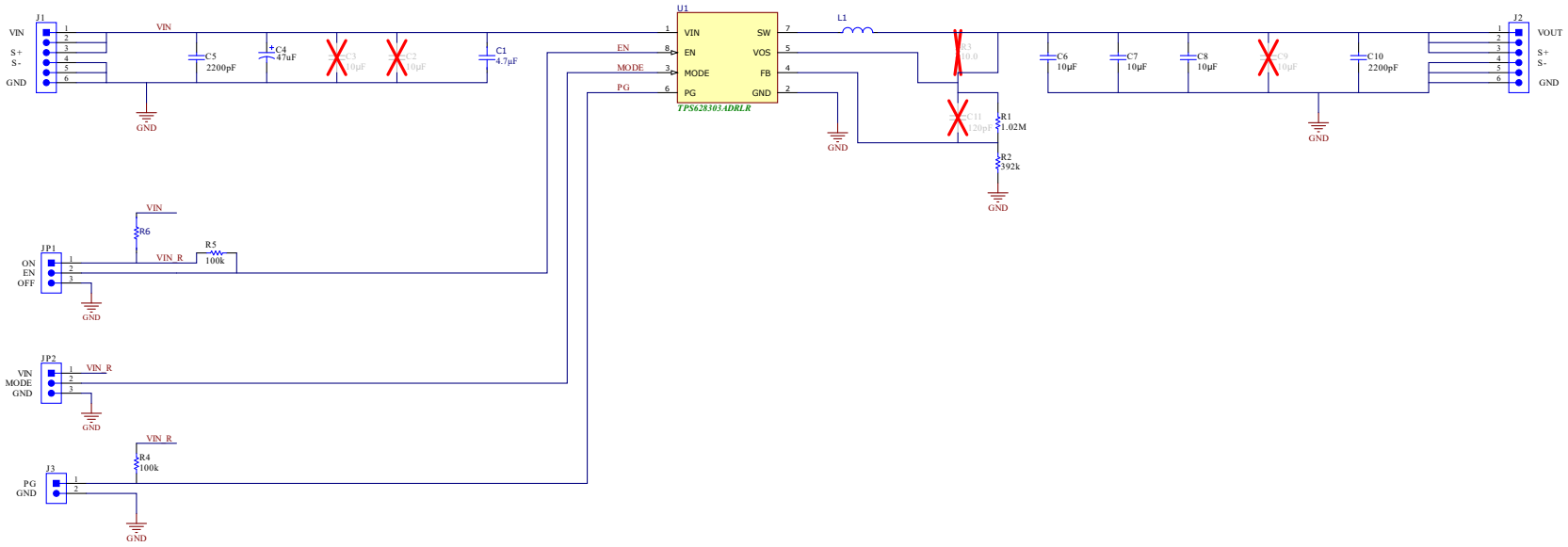

Figure 6-1. TPS628303ADRLEVM Schematic

6.2 Bill of Materials

The following table lists the BOM for this EVM.

Table 6-1. TPS62830xDRLEVM Bill of Materials

QUANTITY		REF DES	VALUE	DESCRIPTION	SIZE	PART NUMBER	MFR
TPS628303ADRLEVM	TPS628303BDRLEVM						
1	1	C1	4.7µF	CAP, CERM, 4.7 µF, 6.3 V, ±10%, X7R, 0603	0603	JMK107BB7475KA-T	Taiyo Yuden
1	1	C4	47 µF	CAP, TA, 47 uF, 35 V, ±10%, 0.3 Ohm, 2917	2917	T495X476K035ATE300	Kemet
2	2	C5, C10	2200 pF	CAP, CERM, 2200 pF, 50 V, +/- 10%, X7R, 0402	0402	GRM155R71H222KA01D	MuRata
3	3	C6, C7, C8	10 µF	CAP, CERM, 10 µF, 10 V, ±10%, X7R, 0603	0603	GRM188Z71A106KA73D	MuRata
1	1	L1	0.47 µH	Inductor, 4.8A, 0.47µH, 0.032 Ω	0805	LSCNE2012HKTR47MD	Taiyo Yuden
1	1	R1	1.02Meg	Resistor, Chip, 0.1 W, 1%	0603	Std	Std
1	1	R2	392k	Resistor, Chip, 0.1 W, 1%	0603	Std	Std
2	2	R4, R5	100k	Resistor, Chip, 0.1 W, 1%	0603	Std	Std
1	1	R6	1k	Resistor, Chip, 0.1 W, 1%	0603	Std	Std
1	0	U1		IC, 5.5-V, 3-A Step-Down Converter	1.6 × 2.1 mm	TPS628303ADRLR	Texas Instruments
0	1	U1		IC, 5.5-V, 3-A Step-Down Converter with latch-off protection for OCP and OVP	1.6 × 2.1 mm	TPS628303BDRLR	Texas Instruments

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User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

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3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

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4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

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4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

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