

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
40V	11.5mΩ @ V <sub>GS</sub> = 10V	10.8A
	17.6mΩ @ V <sub>GS</sub> = 4.5V	8.7A

## Description and Applications

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), maintain superior switching performance, making it ideal for high efficiency power management applications.

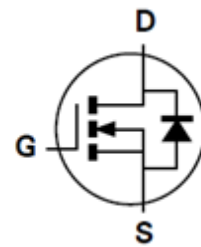
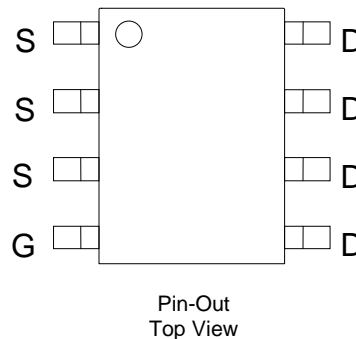
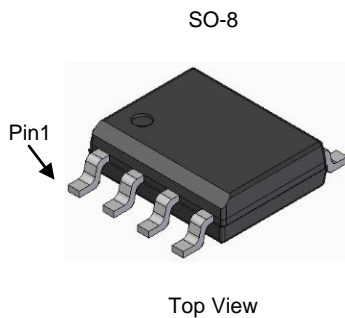
- DC-DC Converters
- Synchronous Rectification
- Power Supplies

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.076 grams (Approximate)

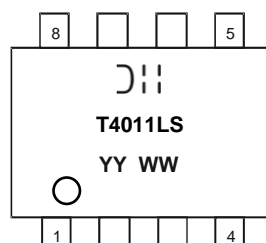


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT4011LSS-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



⌋⌋ = Manufacturer's Marking  
 T4011LS = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 19 = 2019)  
 WW or WW = Week (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	40	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	I <sub>D</sub>	T <sub>A</sub> = +25°C	10.8
		T <sub>A</sub> = +70°C	8.67
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	I <sub>D</sub>	T <sub>A</sub> = +25°C	8.7
		T <sub>A</sub> = +70°C	6.9
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	70	A
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	2.4	A
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	19.9	A
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	19.8	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.31	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	Steady State	95.4
Total Power Dissipation (Note 6)		P <sub>D</sub>	2.02
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	Steady State	61.7
Thermal Resistance, Junction to Case		R <sub>θJC</sub>	9.3
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 32V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	+100 -100	nA	V <sub>GS</sub> = +20V, V <sub>DS</sub> = 0V V <sub>GS</sub> = -16V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.1	1.7	2.4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	8.5	11.5	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A
		—	12.8	17.6		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A
Diode Forward Voltage	V <sub>SD</sub>	—	0.84	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	829	—	pF	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	248	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	24.6	—		
Gate Resistance	R <sub>G</sub>	—	2.0	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>G</sub>	—	14.3	—	nC	V <sub>DS</sub> = 20V, I <sub>D</sub> = 20A
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>	—	7.2	—		
Gate-Source Charge	Q <sub>GS</sub>	—	1.7	—		
Gate-Drain Charge	Q <sub>GD</sub>	—	3.0	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	4.4	—	ns	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 20V, R <sub>G</sub> = 1.6Ω, I <sub>D</sub> = 20A
Turn-On Rise Time	t <sub>R</sub>	—	3.7	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	13.5	—		
Turn-Off Fall Time	t <sub>F</sub>	—	3.3	—		
Reverse Recovery Time	t <sub>RR</sub>	—	11.9	—	ns	I <sub>F</sub> = 15A, di/dt = 400A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	—	9.4	—	nC	

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.

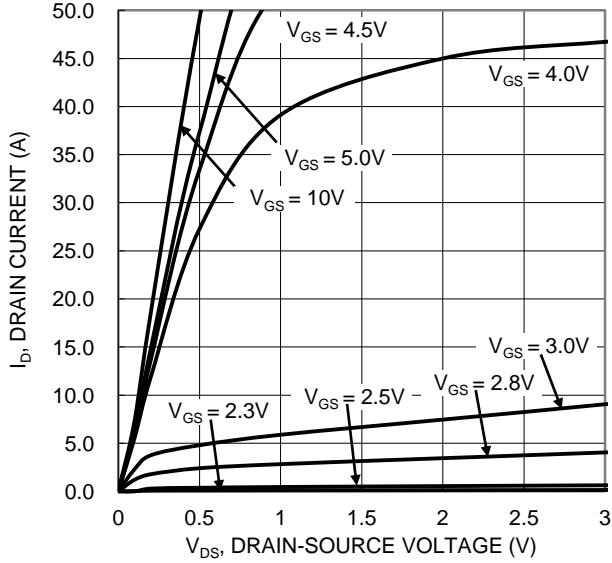


Figure 1. Typical Output Characteristic

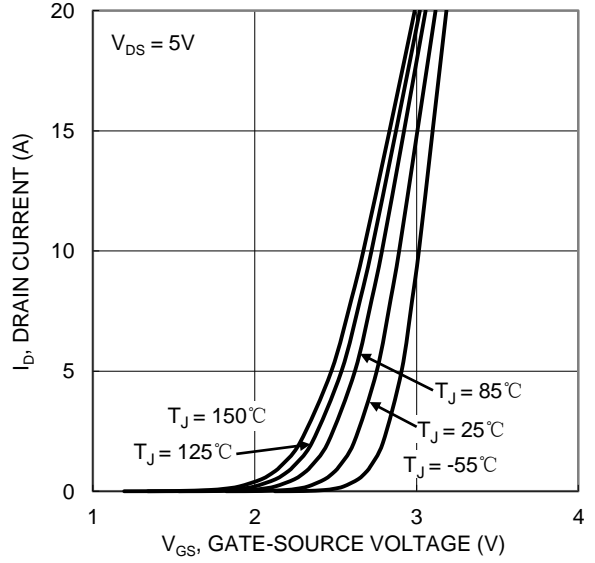


Figure 2. Typical Transfer Characteristic

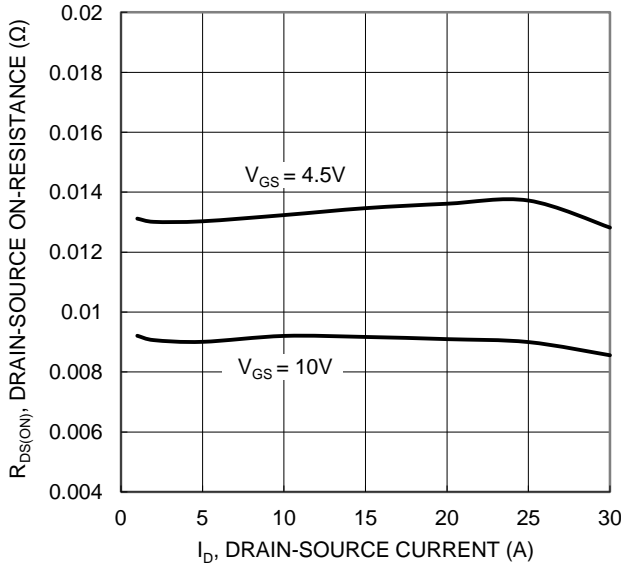


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

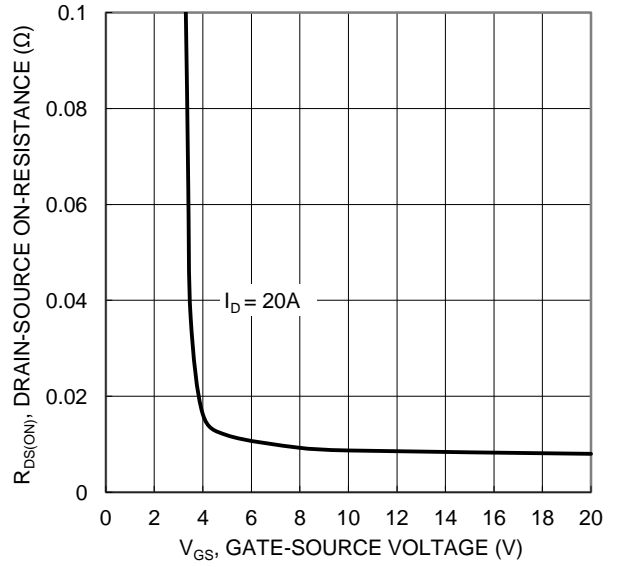


Figure 4. Typical Transfer Characteristic

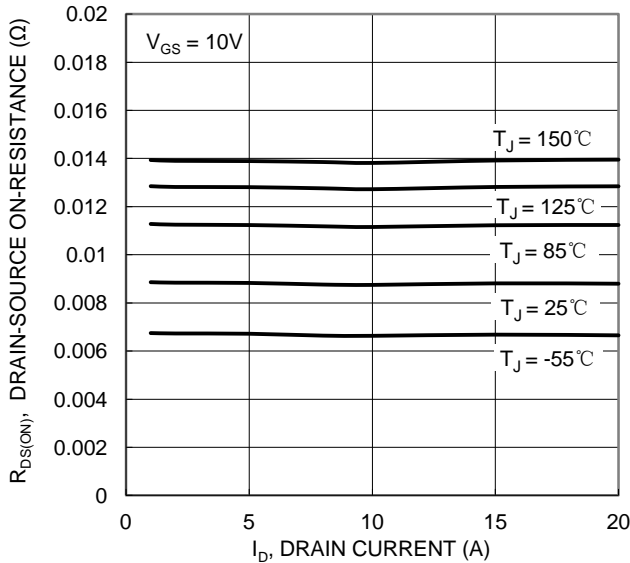


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

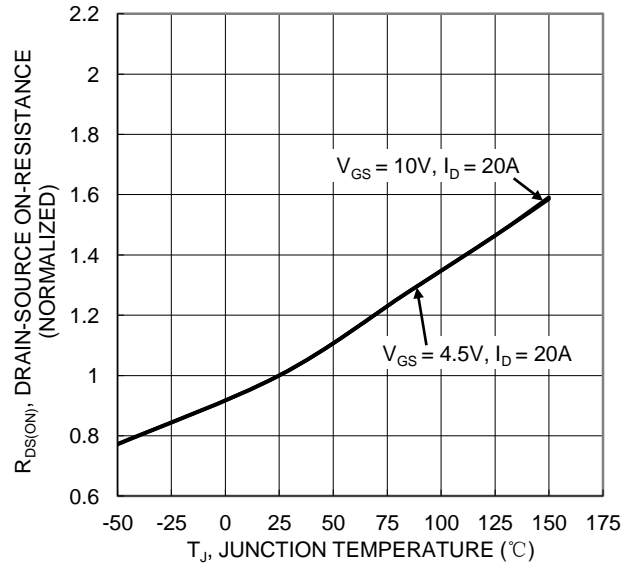


Figure 6. On-Resistance Variation with Junction Temperature

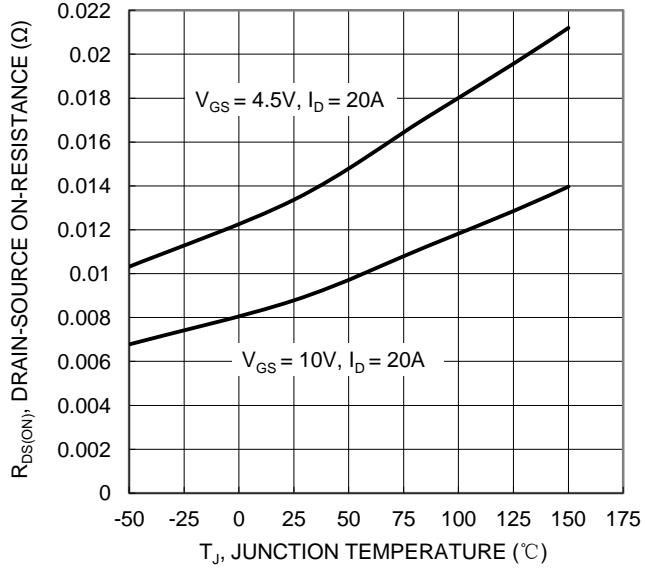


Figure 7. On-Resistance Variation with Junction Temperature

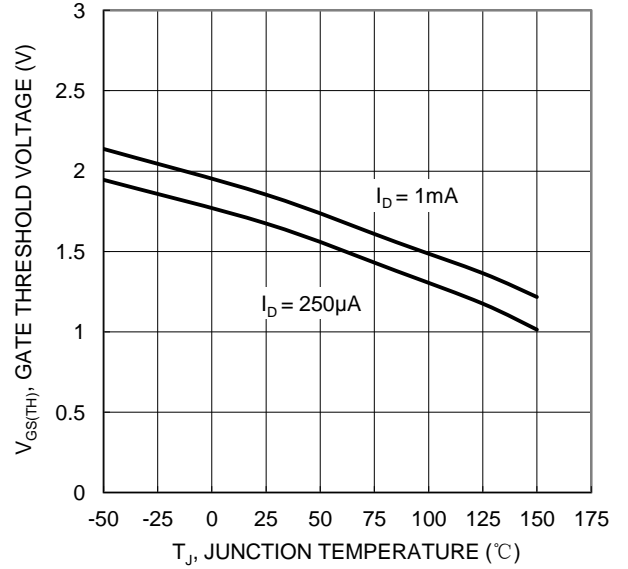


Figure 8. Gate Threshold Variation vs. Junction Temperature

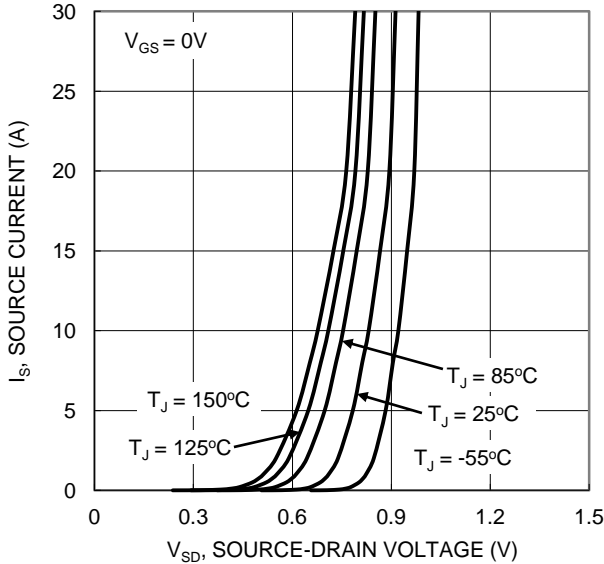


Figure 9. Diode Forward Voltage vs. Current

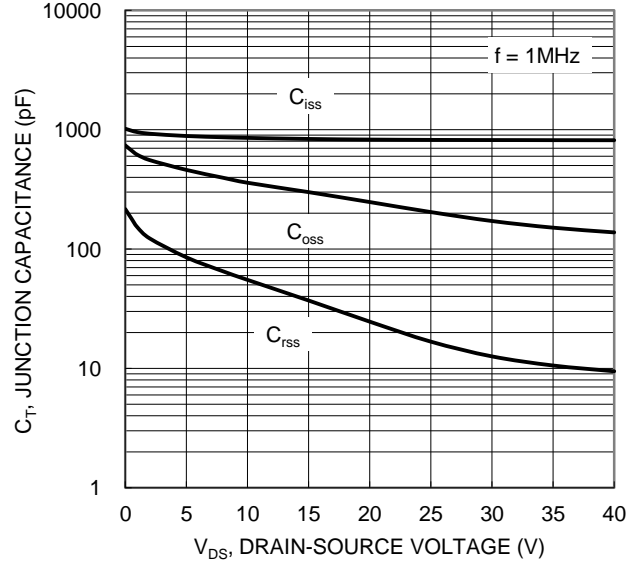


Figure 10. Typical Junction Capacitance

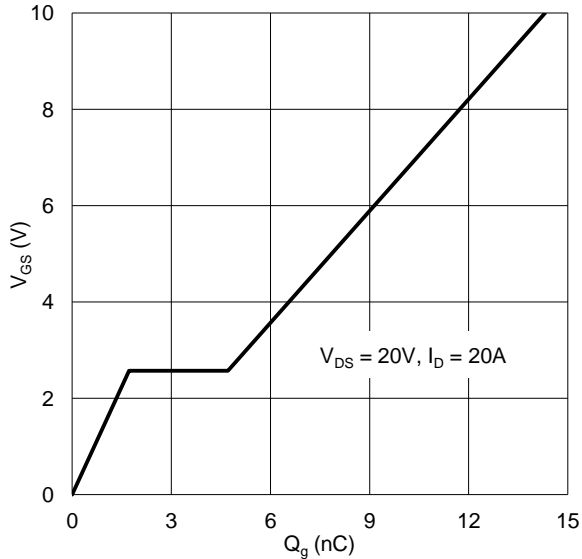


Figure 11. Gate Charge

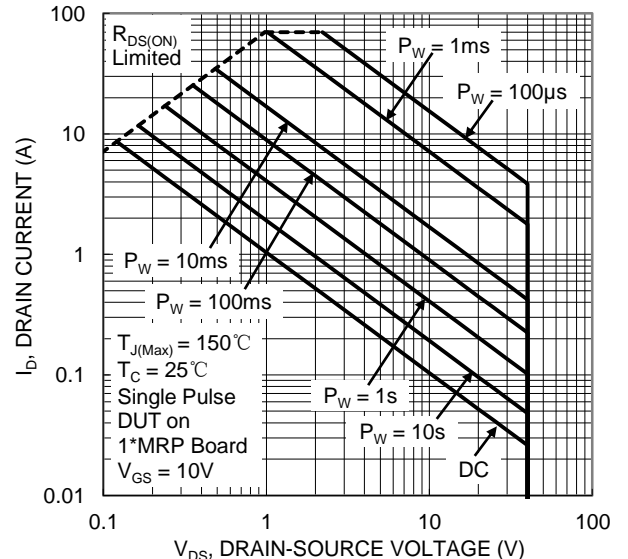


Figure 12. SOA, Safe Operation Area

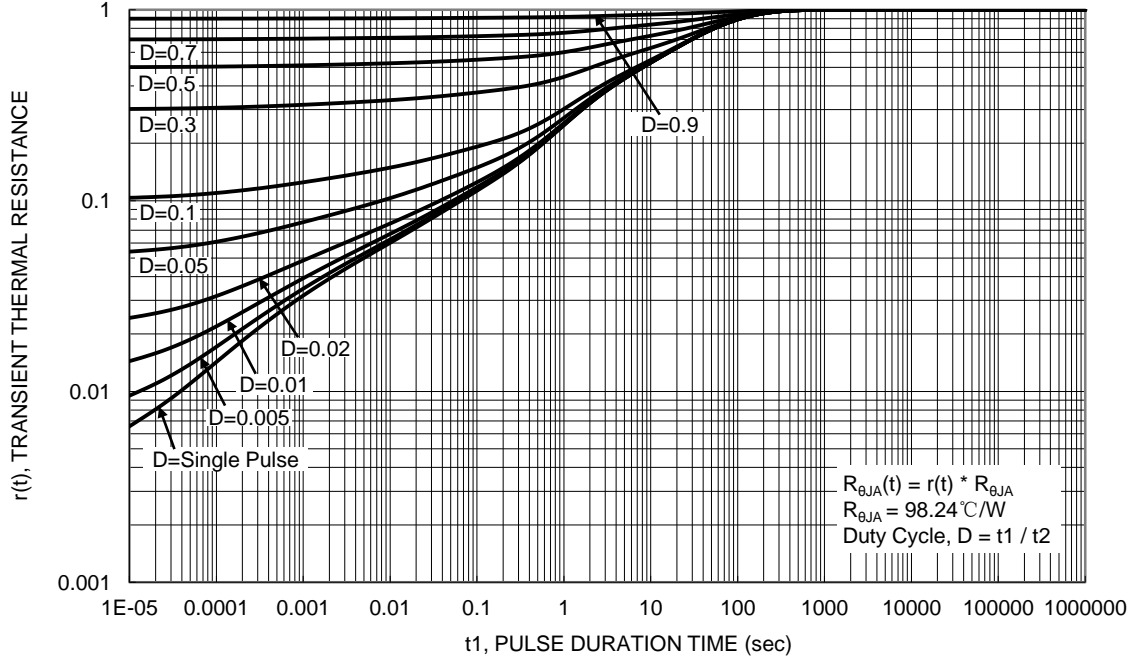
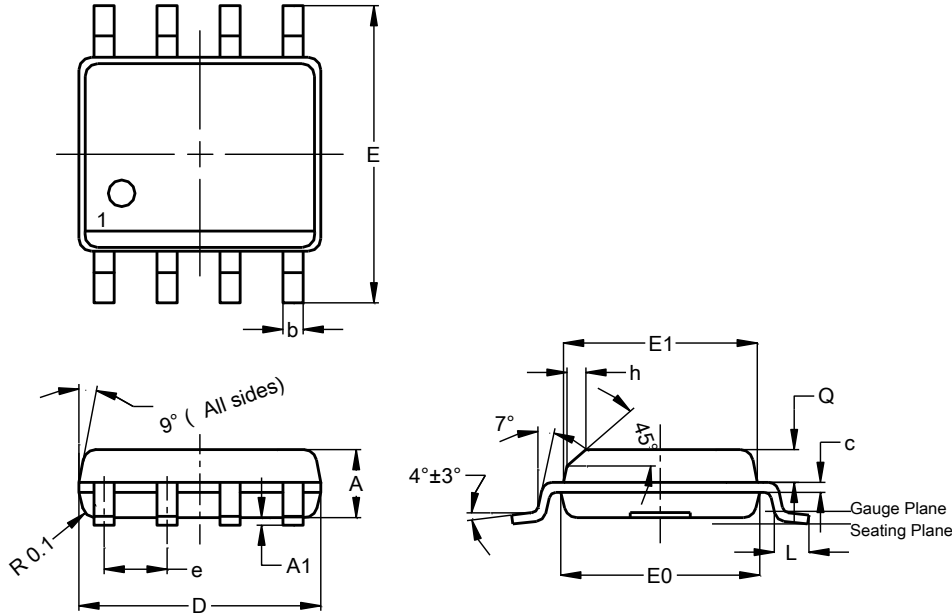


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**

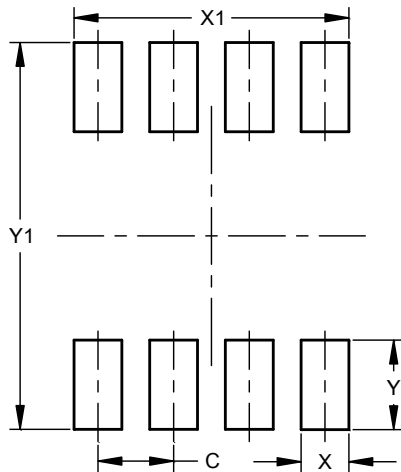


SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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