



191125671 ZGM130 New Revision Parts Available

PRCN Issue Date: 11/25/2019

Effective Date: 3/2/2020

PCN Type: Product Revision

Description of Change

Silicon Labs is pleased to announce a new revision of ZGM130S module with updates to the embedded SoC revision, substrate plating and wirebonding.

Silicon Labs is announcing the following changes in assembly package materials:

1. Substrate to ENEPIG plating due to plating line End of Life (EOL)
2. Wire to Gold for better wire bondability.

The ZGM130S datasheet was updated to version 1.1 with the following changes:

- In the front page block diagram, updated the lowest energy mode for LETIMER.
- Updated 3.6.4 Low Energy Timer (LETIMER) lowest energy mode.
- In 2. Ordering Information, updated ordering part numbers.

Additionally the ZGM130S Errata was updated to version v0.2 with following changes:

- Updated to product revision V2.
- Added LES_E201, TIMER_E202 USART_E205, and WTIMER_E201.
- Resolved TRNG_E202 and USART_E203.
- Migrated to new errata document format.

Reason for Change

Update to new revision SoC and substrate plating line EOL

Impact on Form, Fit, Function, Quality, Reliability

No impact on Form, Fit, Function, Quality or Reliability

Product Identification

Existing Part #	Replacement Part #	DropInCompInd.
ZGM130S037HGN1	ZGM130S037HGN2	Yes
ZGM130S037HGN1R	ZGM130S037HGN2R	Yes

Last Date of Unchanged Product: 3/2/2020

Qualification Samples

Available upon request.

Customer Response

Lack of acknowledgment of the PCN within 30 days constitutes acceptance of the change, Ref. JEDEC-J-STD-046.

To request further data or inquire about this notification, please contact your Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at <http://www.silabs.com>.

Customers may approve early PCN acceptance by emailing approval, along with PCN # to PCNEarlyAcceptance@silabs.com

User Registration

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Qualification Data

Available upon request.



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