

U\_FPGA\_IO  
FPGA\_IO.SchDoc



U\_FPGA\_MGT  
FPGA\_MGT.SchDoc



U\_FPGA\_MISC  
FPGA\_MISC.SchDoc



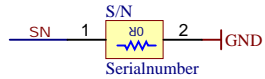
U\_B2B\_Connector  
B2B\_Connector.SchDoc



U\_POWER  
POWER.SchDoc



U\_Revision  
Revision\_Changes.SchDoc

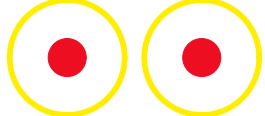


LOGO1

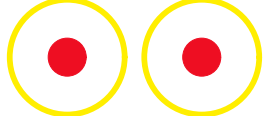


LOGO PRINT

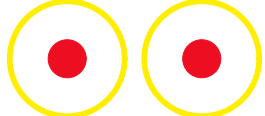
FIDU-DOT - small FIDU-DOT - small



PM1 PM2  
FIDU-DOT - small FIDU-DOT - small



PM4 PM5  
FIDU-DOT - small FIDU-DOT - small

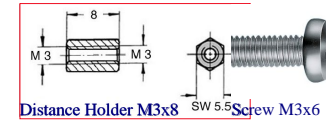


PM6 PM3

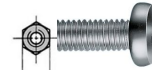
Top of Board



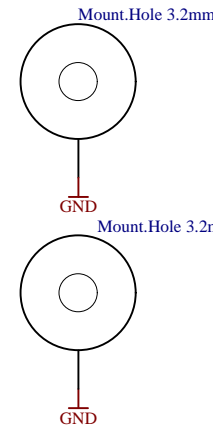
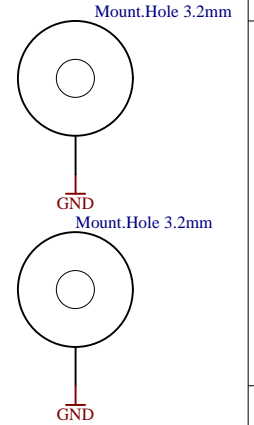
Screw M3x4



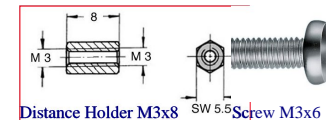
Distance Holder M3x8



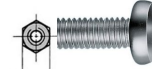
Screw M3x6



Screw M3x4



Distance Holder M3x8



Screw M3x6



Title: <b>TE0714</b>		
A4	Number: <b>1</b> <b>50-21</b>	Rev. <b>02</b>
Date: <b>2016-08-04</b>	Copyright: <b>Trenz Electronic GmbH / TT</b>	Page <b>1</b> of <b>7</b>
Filename: <b>TE0714.SchDoc</b>		

A

B

C

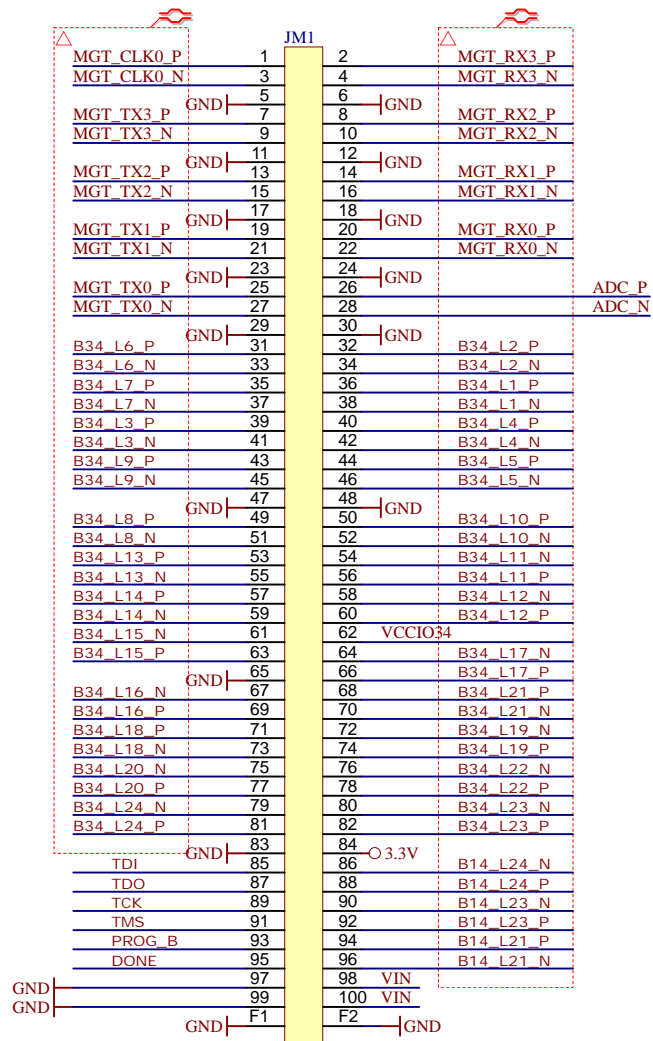
D

A

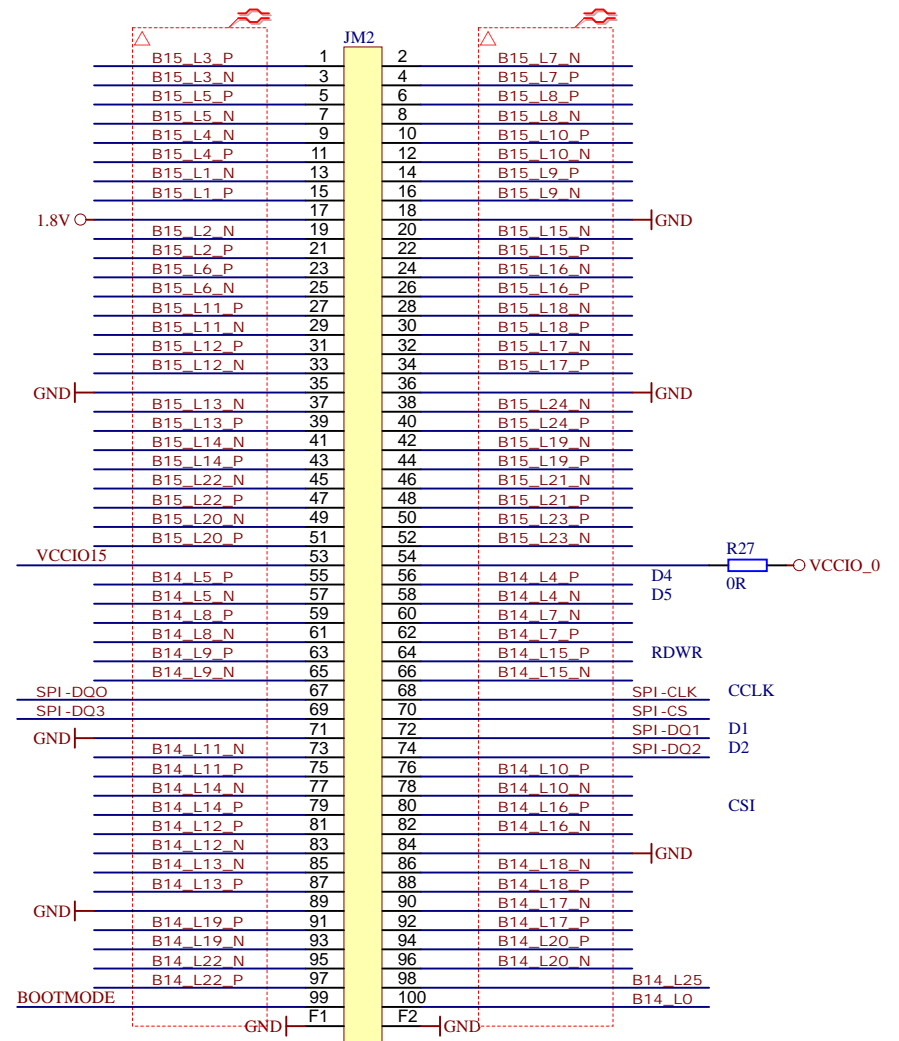
B

C

D



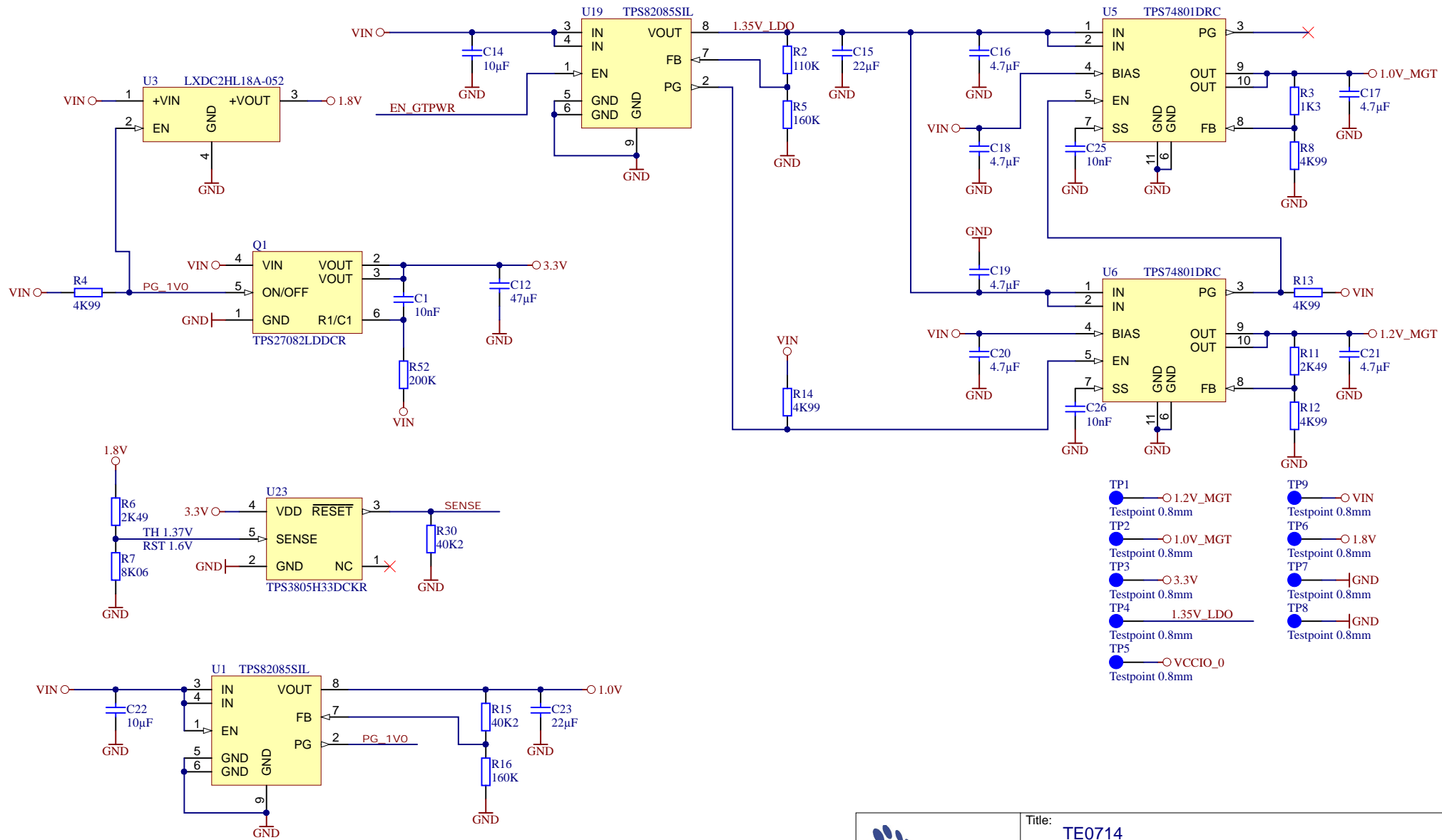
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LSHM-150-04.0-L-DV-A-S-K-TR



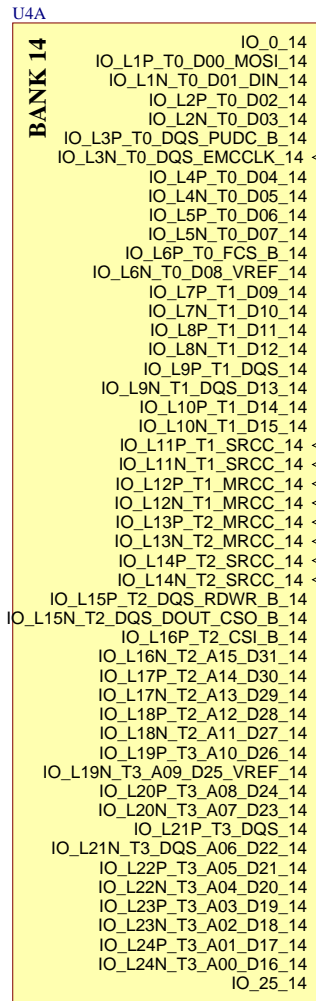
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A4	Number: 2 50-21	Rev. 02
Date: 2016-08-04	Copyright: Trenz Electronic GmbH / TT	Page2 of 7
Filename: B2B_Connector.SchDoc		



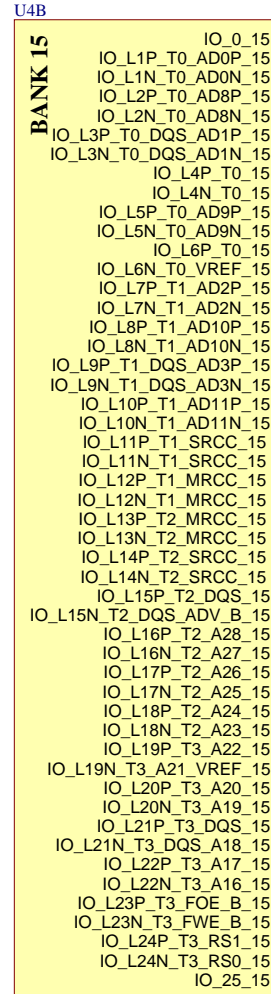
- TP1 — 1.2V\_MGT Testpoint 0.8mm
- TP2 — 1.0V\_MGT Testpoint 0.8mm
- TP3 — 3.3V Testpoint 0.8mm
- TP4 — 1.35V\_LDO Testpoint 0.8mm
- TP5 — VCCIO\_0 Testpoint 0.8mm
- TP6 — 1.8V Testpoint 0.8mm
- TP7 — GND Testpoint 0.8mm
- TP8 — GND Testpoint 0.8mm
- TP9 — VIN Testpoint 0.8mm



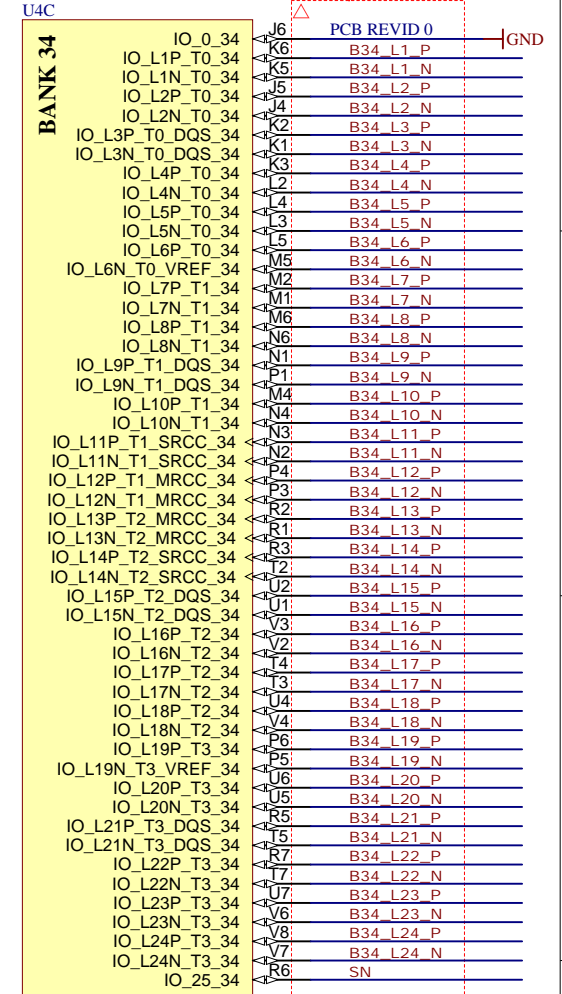
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A4	Number: <b>3</b> <b>50-21</b>	Rev. <b>02</b>
Date: 2016-08-04	Copyright: Trenz Electronic GmbH	Page3 of 7
Filename: <b>POWER.SchDoc</b>		



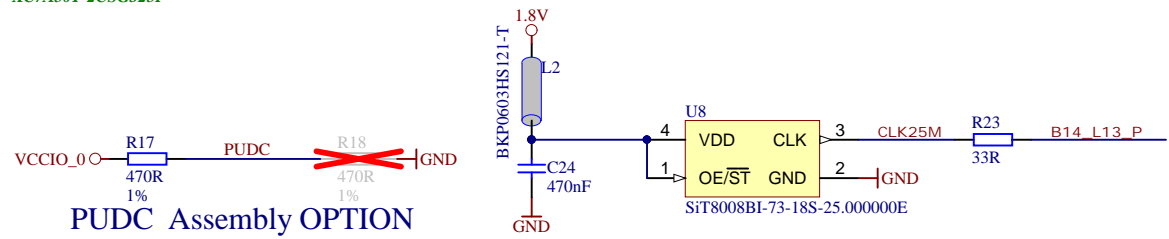
XC7A50T-2CSG3251



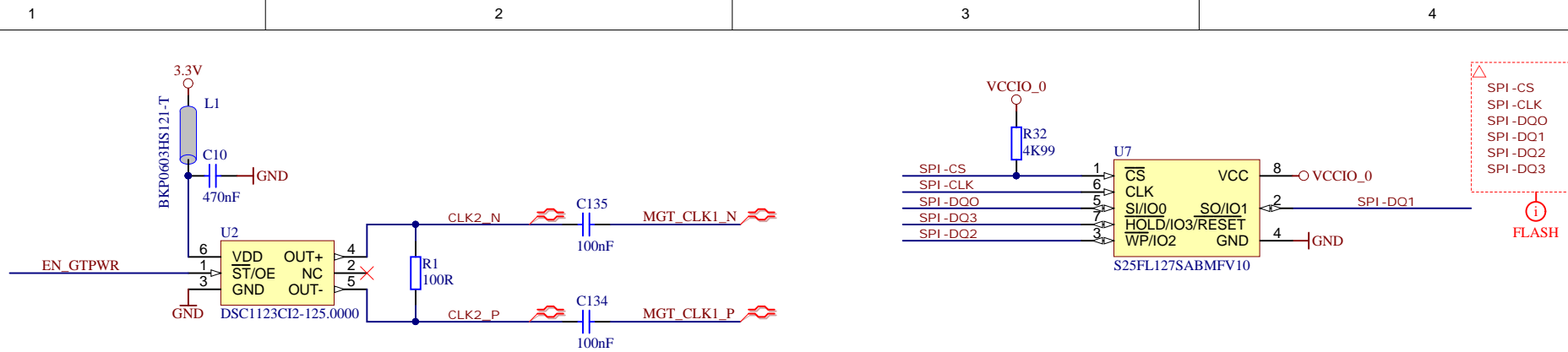
XC7A50T-2CSG3251



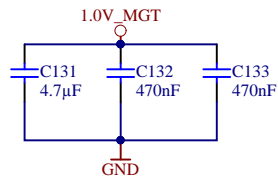
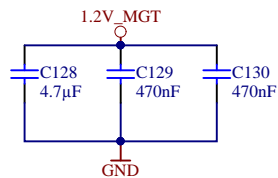
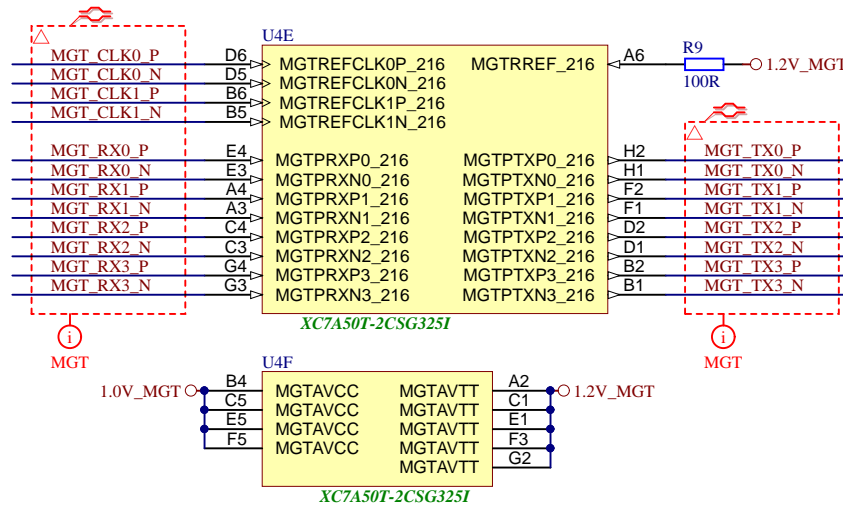
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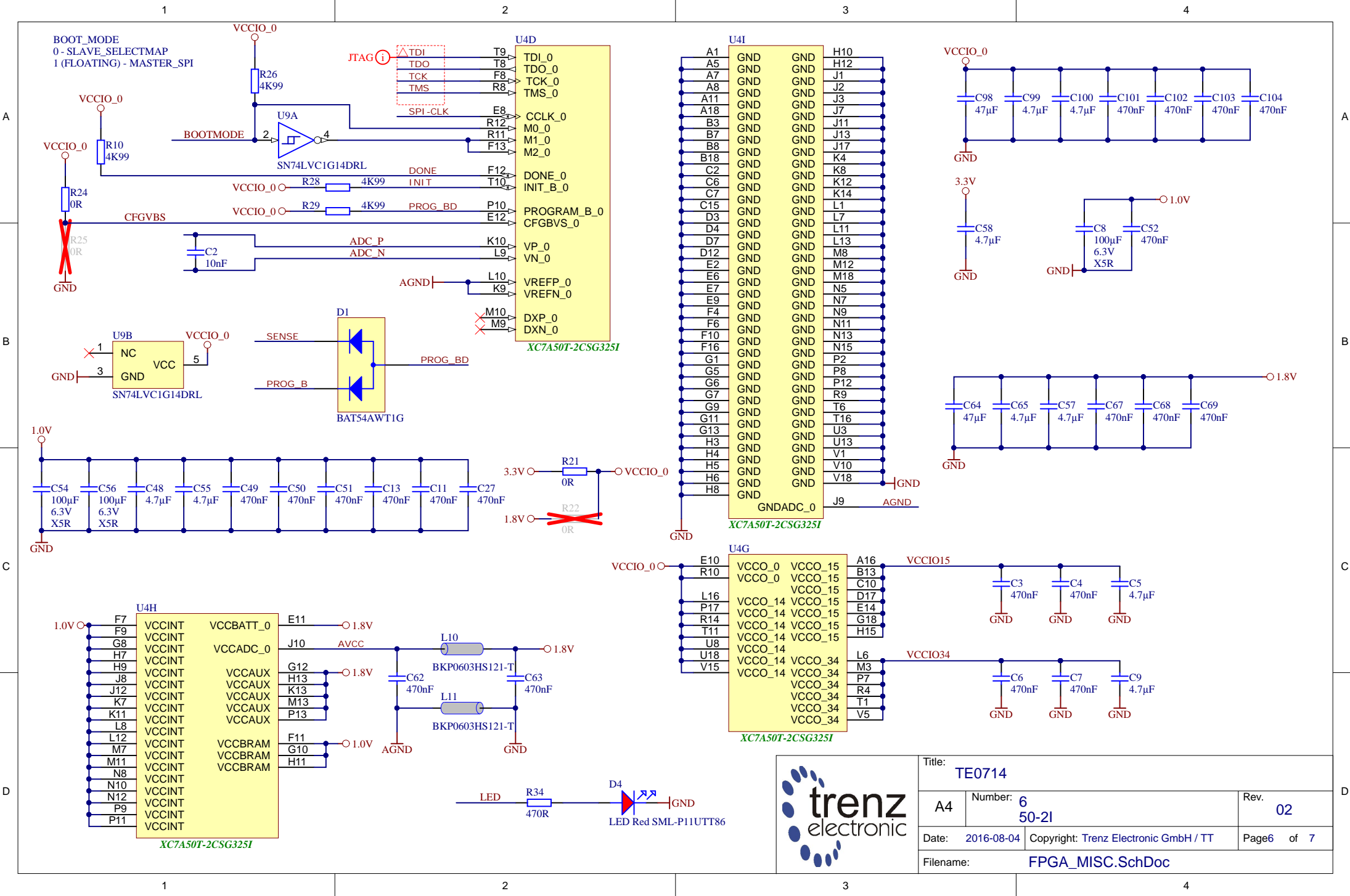
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A4	Number: 4	Rev. 02	
Date: 2016-08-04		Copyright: Trenz Electronic GmbH / TT	
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Filename: FPGA_IO.SchDoc			



OSC FREQ is Assembly OPTION = 125MHZ



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	Filename: <b>FPGA_MGT.SchDoc</b>		



Title: TE0714		
A4	Number: 6 50-21	Rev. 02
Date: 2016-08-04	Copyright: Trenz Electronic GmbH / TT	Page6 of 7
Filename: FPGA_MISC.SchDoc		

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Changes REV 02:

- 1. Added 0 ohm strap option to supply VCCIO0 on B2B connector
- 2. Added PCB Revision sense support. PCB Revision readout possible from HDL Design
- 3. Updated FPGA pin PROG\_B connection. TPS3805H33 push-pull output RESET\_N not affected on baseboard circuit, connected to PROG\_B.
- 4. C8, C54, C56 updated to 100uF for variant 50-2I
- 5. Added testpoints

Changes REV 02A (12.2018):

- 1. New FLASH memory U7 S25FL127SABMFV10

A

A

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B

C

C

D

D

		Title: <b>TE0714</b>	
		A4	Number: <b>7</b> <b>50-2I</b>
Date: <b>2016-08-04</b>		Copyright: <b>Trenz Electronic GmbH / TT</b>	
Date: <b>2016-08-04</b>		Page <b>7</b> of <b>7</b>	
Filename: <b>Revision Changes.SchDoc</b>			

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