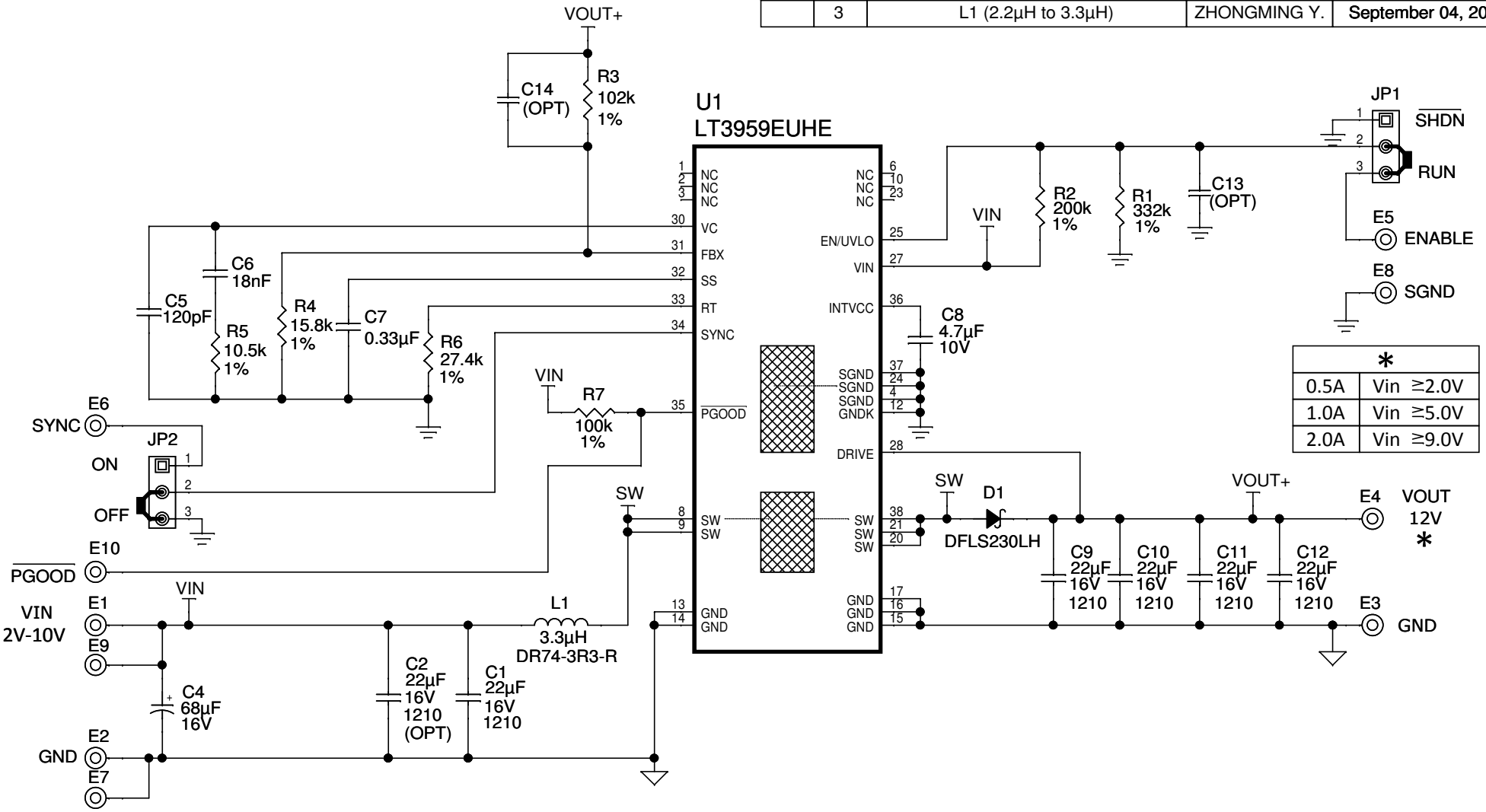
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	1st PROTOTYPE	ZHONGMING Y.	Aug. 12, 2011
	2	PRODUCTION	ZHONGMING Y.	June 08, 2012
	3	L1 (2.2µH to 3.3µH)	ZHONGMING Y.	September 04, 2012



NOTE: UNLESS OTHERWISE SPECIFIED.

1. ALL RESISTORS 0603.
2. ALL CAPACITORS 0603.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

C:\PADS PROJECTS\1853A\SCH\1853A_REV3.DSN

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	RB
APP ENG.	ZHONGMING Y.

SCALE = NONE

LINEAR TECHNOLOGY

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TITLE: SCHEMATIC

WIDE INPUT VOLTAGE RANGE BOOST CONVERTER

SIZE	IC NO.	REV.
N/A	LT3959EUHE DEMO CIRCUIT 1853A	3

MODIFY DATE: *September 04, 2012*