

JP6 settings

MODE	BUCK	BUCK-BOOST
1	BURST	BURST
0	PULSE SKIP	PWM


Unless noted:
 Resistors: Ohms, 0402, 1%, 1/16W
 Capacitors: uF, 0402, 10%, 50V

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.
 APPROVALS
 DRAWN: G. Barbehenn
 CHECKED:
 APPROVED:
 ENGINEER: G. Barbehenn
 DESIGNER:



1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900
 Fax: (408)434-0507
 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
LTC3558EUD: Linear USB Battery Charger with Buck and Buck-Boost Regulators

SIZE **A** DWG NO. **DC1276B** REV **B**

DATE: Friday, February 08, 2008 SHEET 1 OF 1