






SPECIFICATIONS

CUSTOMER : _____
MODEL NO. : **GFT043FA480272Y**
VERSION : **F**
DATE : **2023.03.09**
CERTIFICATION : **ROHS**

Customer Sign	Approved By	Prepared By	Prepared By
			

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Revision Record

Data(y/m/d)	Ver.	Description	page
2010.05.11	A	New	
2011.02.23	B	Modify touch panel information	16-17
2012.08.15	C	修改背光輝度跟模組輝度	6、8
2017.07.27	D	修改公司抬頭、格式統一	
2018.10.11	E	加註背光Lifetime 50000Hr(LED有專利)	
2023.03.09	F	更新公司抬頭認證圖示	



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Note : For detailed information please refer to IC data sheet:

Primacy (TFT LCD): Himax: HX8257(or Compatible IC)

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1. SPECIFICATIONS

1.1 Features

Main LCD panel

Item	Standard Value
Display Type	480(R, G, B)* 272 Dots
LCD Type	a-Si TFT, Normally white, Transmissive type
Screen size (inch)	4.3 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Backlight	LED B/L
Interface	Digital 24-bits RGB
Built in	Touch Panel
Other (controller/driver IC)	HX8257 (Or Compatible IC)

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W)*67.2(L)*4.2(H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	95.04(W)*53.856(L)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VCC	GND=0	-0.3	5.0	V
	VGH-VGL	GND=0	-0.3	45.0	
Input Voltage*1	VI	-	-0.3	VCC+0.3	V
Operating Temperature	Top	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C

1.4 DC Electrical Characteristics

(Unless otherwise specified, Voltage Referenced to DVSS, VDDIO = 2.2V, T_A = 25°C)

Symbol	Parameter	Test condition	Spec.			Unit
			Min.	Typ.	Max.	
V _{DDIO}	Power supply pin of IO pins	Recommend Operating Voltage Possible Operating Voltage	1.8	-	3.6	V
V _{CI}	Booster Reference Supply Voltage Range	Recommend Operating Voltage Possible Operating Voltage	3 or VDDIO	-	3.6	V
I _{sleep}	Sleep mode current	-	-	50	-	µA
I _{dp}	Operating mode current	V _{CI} =3.3V	-	13	15	mA
V _{CL}	Negative V _{CI} Output Voltage	No panel loading	- V _{CI}	-	- V _{CI} +0.7	V
V _{CIX2}	V _{CIX2} primary booster efficiency ⁽¹⁾	No panel loading, ITO for V _{CIX2} , V _{CI} and V _{CHS} = 10 Ohm	5.2	5.4	5.6	V
V _{DC}	V _{DC} Output Voltage	V _{DC} [3:0]=1011	4.9	5	5.1	V
V _{GH}	Gate driver High Output Voltage Booster efficiency ⁽²⁾	No panel loading; 3x booster	84	89.5	-	%
		No panel loading; 4x booster	80	88.5	-	%
V _{GL}	Gate driver Low Output Voltage	V _{GL} = -2 x V _{DC}	-10	-10	-9	V
V _{COMH}	V _{COM} High Output Voltage ⁽³⁾	-	-3%	COMC+COMPP	3%	V
V _{COML}	V _{COM} Low Output Voltage ⁽³⁾	-	-3%	COMC-COMPP	3%	V
V _{LCD}	V _{LCD} Output Voltage	V _{RH} [5:0]=100100	4.41	4.51	4.61	V
V _{OH1}	Logic High Output Voltage	I _{out} = -100µA	0.9*V _{DDIO}	-	V _{DD}	V
V _{VD}	Source Output Voltage Deviation	-	-	±20	±30	mV
V _{OS}	Source Output Voltage Offset	-	-	-	±30	mV
V _{OL1}	Logic Low Output Voltage	I _{out} = 100µA	0	-	0.1*V _{DDIO}	V
V _{IH1}	Logic High Input voltage	-	0.8*V _{DDIO}	-	V _{DDIO}	V
V _{IL1}	Logic Low Input voltage	-	0	-	0.2*V _{DDIO}	V
I _{OH}	Logic High Output Current Source	V _{out} = V _{DD} - 0.4V	50	-	-	µA
I _{OL}	Logic Low Output Current Drain	V _{out} = 0.4V	-	-	-50	µA
I _{oz}	Logic Output Tri-state Current Drain Source	-	-1	-	1	µA
I _{I/OH}	Logic Input Current	-	-1	-	1	µA

Note : (1) V_{CIX2} efficiency = V_{CIX2} / (2 x V_{CI}) x 100%

(2) V_{GH} efficiency = V_{GH} / (V_{DC} x n) x 100% (where n = booster factor)

(3) V_{COML} < 0V, V_{COMH} < V_{CIX2J}



1.5 Optical Characteristics

TFT LCD Module

VCC=3.3V Ta=25°C

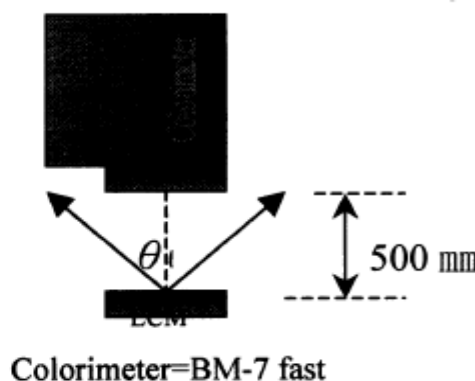
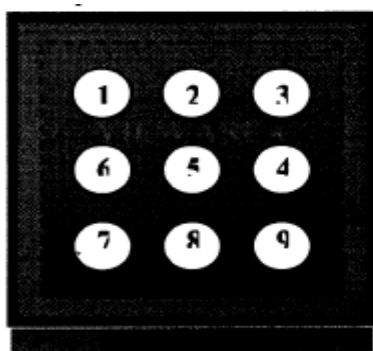
Item	Symbol	Condition	Min.	Typ	Max	Unit		
Response time	Rise	Ta=25°C θx,θy=0°	-	10	20	ms	Note2	
	Fall		-	20	30			
Viewing angle	Top	CR ≥ 10	-	50	-	Deg.	Note4	
	Bottom		-	55	-			
	Left		-	60	-			
	Right		-	60	-			
Contrast ratio		CR	200	250	-	-	Note3	
Color of CIE Coordinate (With B/L)	White	X	Ta=25°C θx,θy=0°	0.281	0.278	0.284	-	NoteA
		Y		0.282	0.276	0.288		
Average Brightness Pattern=white display (With B/L)		IV	IF=20mA	110	130	-	cd/m ²	NoteA
Uniformity (With B/L)*1		ΔB	IF=20mA	80	-	-	%	

Note A:

*1: $\Delta B = B(\min) / (\max)$

*2: Measurement Condition for Optical Characteristics:

- Environment 25°C ± 5°C / 60 ± 20R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
- Measurement Distance 500 ± 50mm (θ=0°)
- Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
- The uncertainty of the C.I.E coordinate measurement ± 0.01, Average Brightness ± 4%



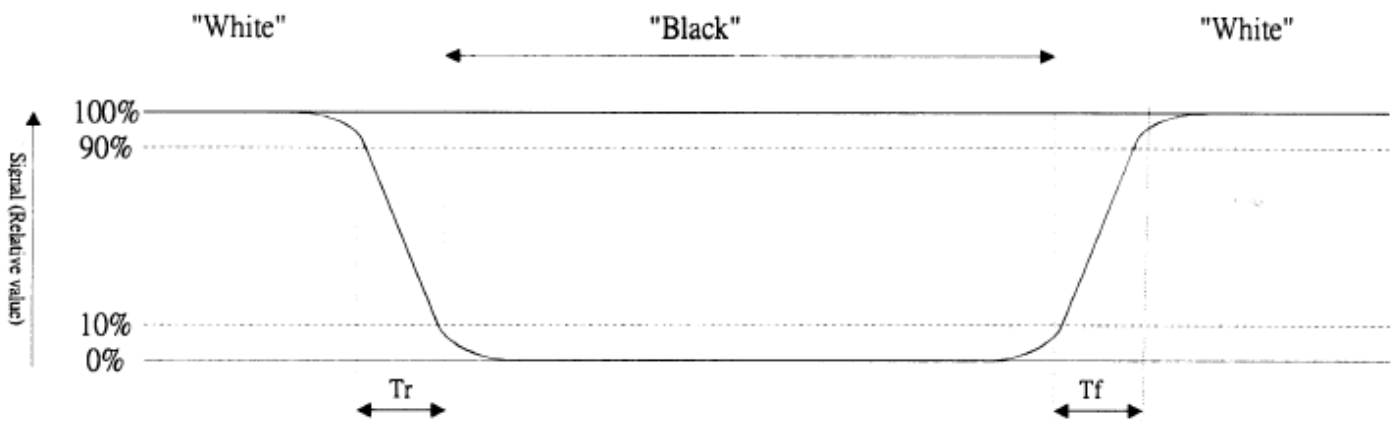


Note1: To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white” (falling time) and from “white” to “black” (rising time). respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

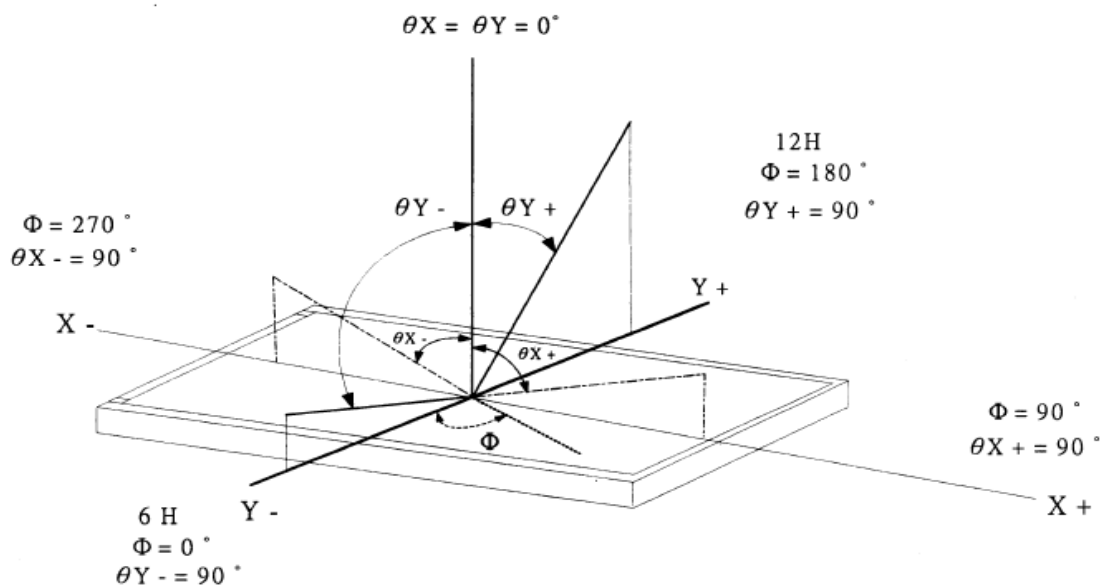
Photo detector output when LCD is at “White” state

Contrast ratio (CR) =

Photo detector output when LCD is at “Black” state

Note4: Definition of viewing angle:

Refer to figure as below:





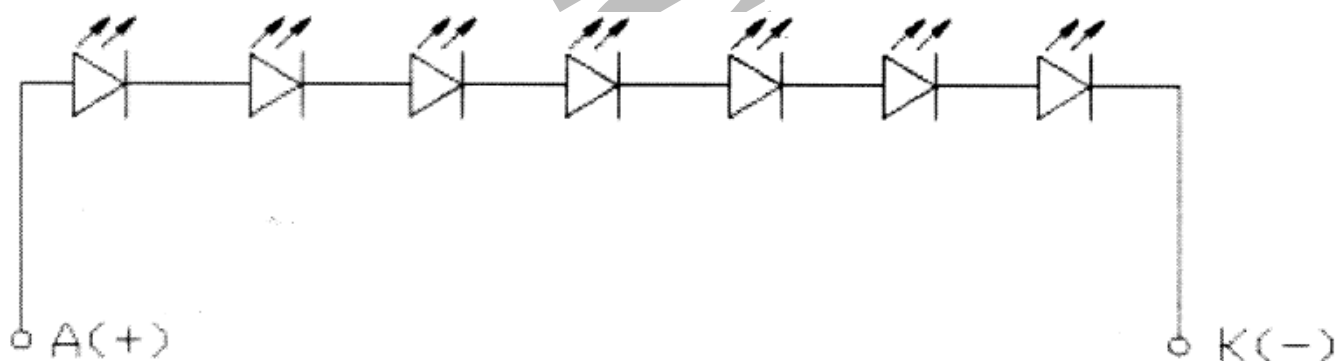
1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max	Unit
LED Forward Current	IF	Ta=25°C	-	20	mA
LED Reverse Voltage	VR	Ta=25°C	-	5	V

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF=20mA	21.1	23.1	25.2	V
Average Brightness (Without LCD)	IV		1200		-	cd/m ²
CIE Color Coordinate (Without LCD)	X		0.265	0.29	0.315	-
	Y		0.265	0.29	0.315	
Color	White					
LED Life time	(LEDs are patented)		-	50000	-	Hr





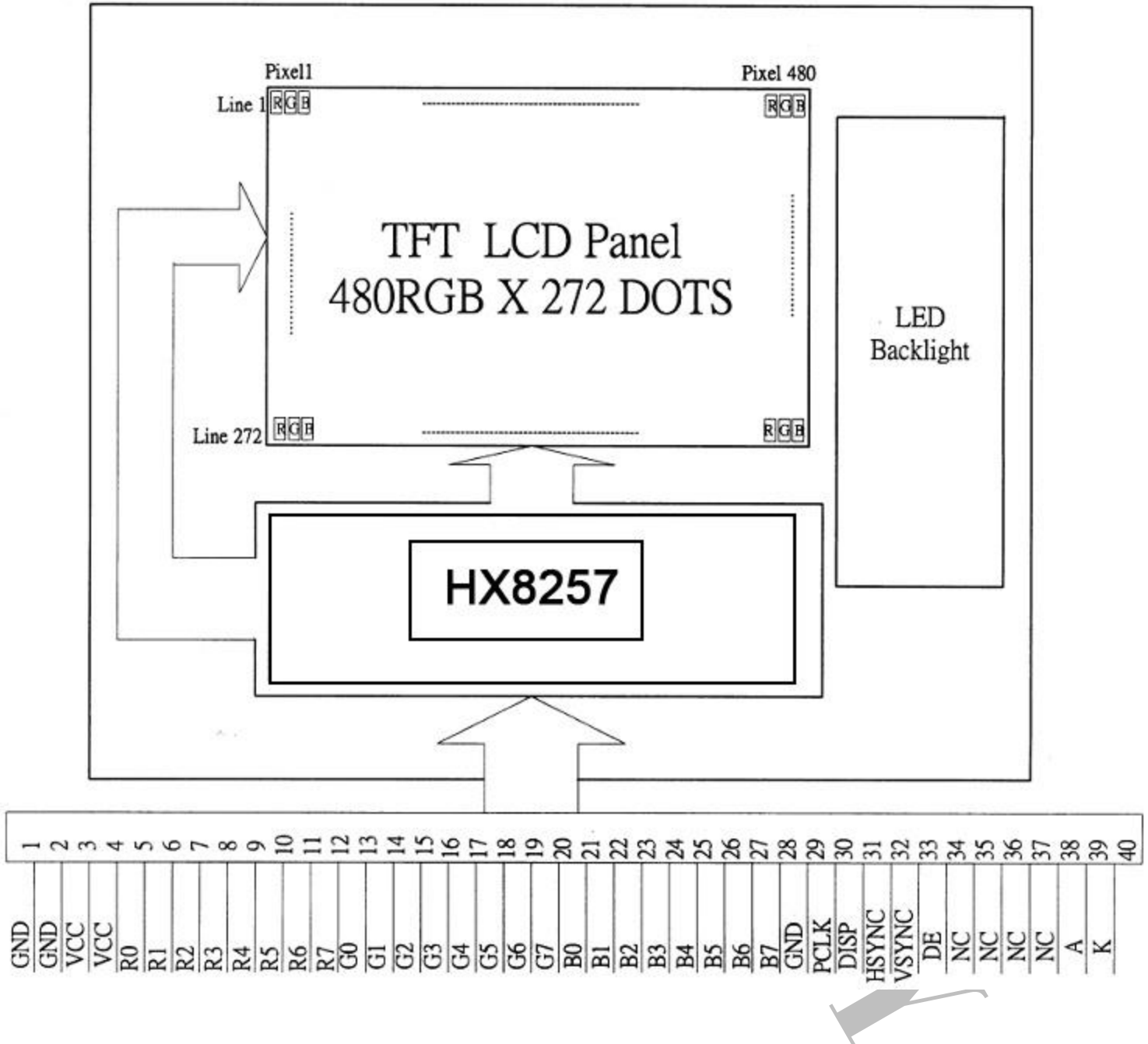
2. MODULE STRUTURE

2.1 Counter Drawing

LCM Mechanical Diagram

- See Appendix

2.1.2 Block diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	GND	Ground
2	GND	Ground
3	VCC	Digital power
4	VCC	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7
21	B0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	PCLK	Dot data clock
31	DISP	Display on / off mode control.
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data enable control
35	NC	No connection
36	NC	No connection
37	NC	No connection
38	NC	No connection
39	A	Power supply for LED Backlight anode input
40	K	Power supply for LED Backlight cathode input



2.3 Timing Characteristics

Timing Parameters 1

(480RGBx272, TA=25°C, VCC=3.3to 3.6V, GND=0V)

PARAMETER	Symbol	Min.	Typ.	Max.	Unit
Clock cycle	f	-	9	15	MHz
Hsync cycle	1/th	-	17.14	-	KHz
Vsync cycle	1/th	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th	-	525	-	CLK
Horizontal display period	thd	-	480	-	CLK
Horizontal front porch	thf	2	2	82	CLK
Horizontal pulse width	thp	2	41	41	CLK
Horizontal back porch	thb	2	2	41	CLK
Vertical Signal					
Vertical cycle	tv	285	286	399	H
Vertical display period	tvd	-	272	-	H
Vertical front porch	tvf	1	2	227	H
Vertical pulse width	tvp	1	10	11	H
Vertical back porch	tvb	1	2	11	H

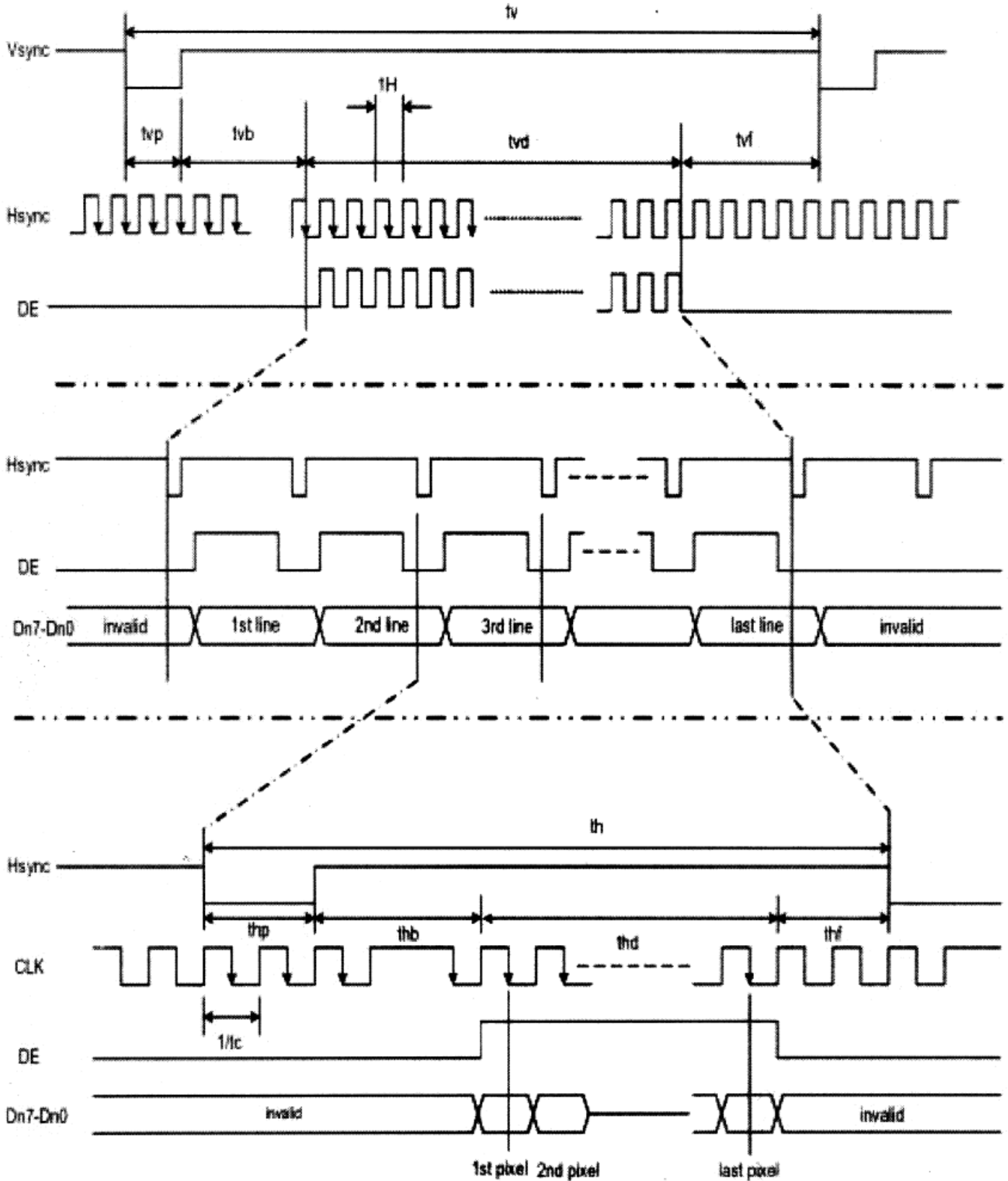
Note: (1) The table is parallel interface. Clock frequency and horizontal signal parameters are tripled in serial interface.

The maximum clock frequency of serial interface is 33MHz

(2) thd= 480CLK, thf=2CLK, thp=41CLK, thb=2CLK, thf+thp+thb>44CLK
(CLK=1/fCLK, H=th)



Timing Chart 1





Timing Parameters 2

($T_A=25^{\circ}\text{C}$, $V_{CC}=3.3\text{V}$ to 3.6V , $GND=0\text{V}$, $t_r^{(2)}=t_f^{(2)}=2\text{ns}$)

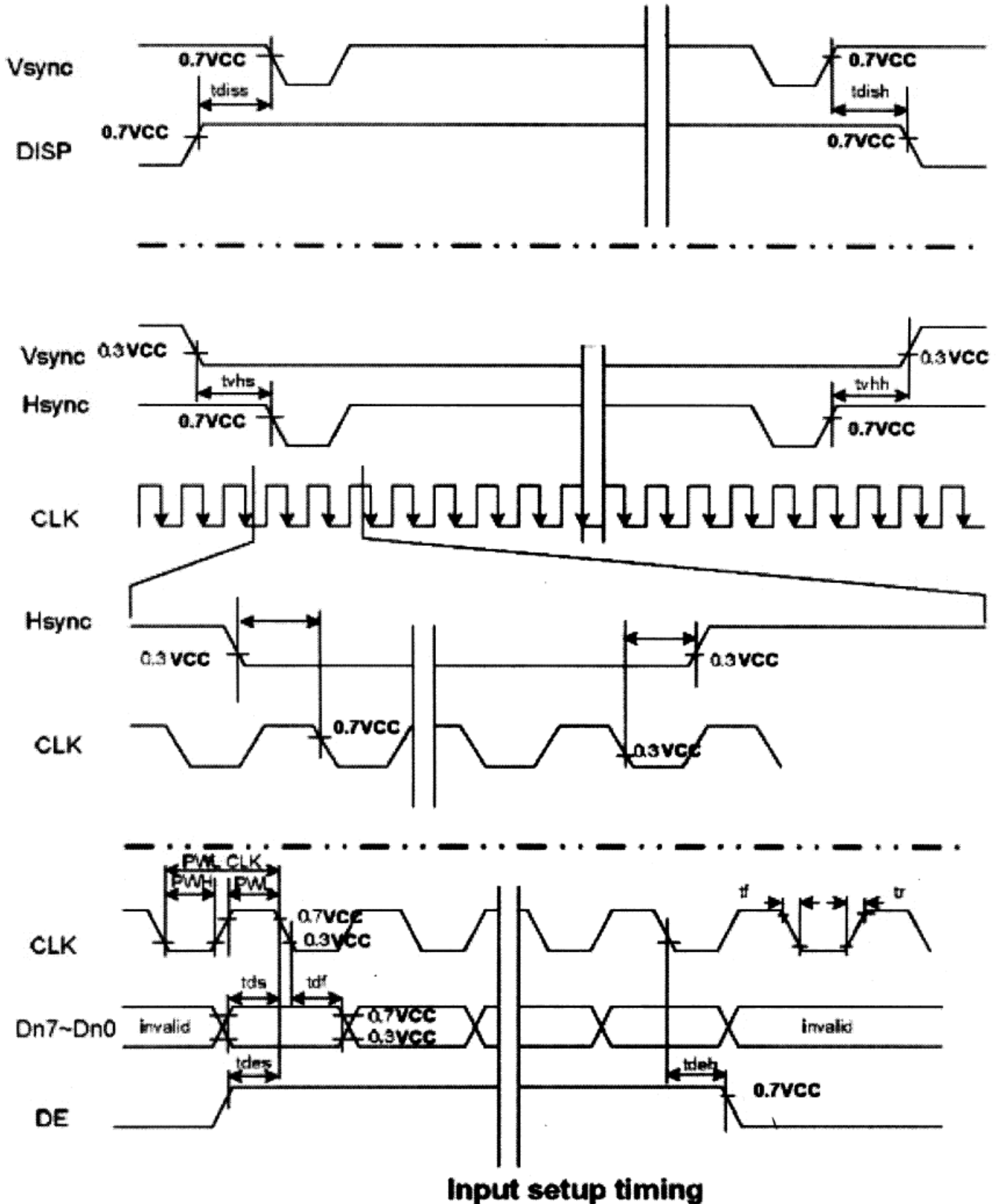
PARAMETER	Symbol	Min.	Typ.	Max	Unit
DISP setup time	t_{diss}	10	-	-	ns
DISP hold time	t_{dish}	10	-	-	ns
Clock period	$PW_{CLK}^{(1)}$	66.7	-	-	ns
Clock pulse high period	$PWH^{(1)}$	26.7	-	-	ns
Clock pulse low period	$PWL^{(1)}$	26.7	-	-	ns
Hsync setup time	t_{hs}	10	-	-	ns
Hsync hold time	t_{hh}	10	-	-	ns
Data setup time	t_{ds}	10	-	-	ns
Data hold time	t_{dh}	10	-	-	ns
DE setup time	t_{des}	10	-	-	ns
DE hold time	t_{deh}	10	-	-	ns
Vsync setup time	t_{vhs}	10	-	-	ns
Vsync hold time	t_{vhh}	10	-	-	ns

Note: (1) For parallel interface, maximum clock frequency is 15MHz.

(2) t_r , t_f is defined 10% to 90% of signal amplitude.



Timing Chart 2



Input setup timing



Color Data Assignment

COLOR	INPUT DATA	R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
BLUE	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) Definition of gray scale Color (n) : n means level of gray scale

Larger n means brighter level

(2) Data: 1-High, 0-Low



3. Touch panel

3.1 Product type

- 1) Resistance Type : Analogy Type
- 2) Film/Glass Type

3.2 Criteria of Materials

1) Uper Electrode

- Base material: ITO FILM
- Type: anti-glare and anti-newton ring
- Thickness: $188\pm 10\mu\text{m}$
- Resistance: $400\pm 100\Omega/\text{sq}$

2) Lower Electrode

- Base material: ITO GLASS
- Thickness: $1.1\pm 0.10\text{mm}$
- Resistance : $400\pm 100\Omega/\text{sq}$

3) Connector Tail

- type : FPC (double side)

3.3 Characteristics

1) Mechanical characteristics

- Outside dimension : $105.70\pm 0.2\text{ mm}\times 67.40\pm 0.2\text{ mm}$
- View area : $99.00\text{ mm}\times 58.00\text{mm}$
- Thickness : $1.55\pm 0.10\text{mm}$
- Input method : Pen
- Operating force : 10-100g

Shape of pen end: $\varnothing 0.3\text{mm}\sim \varnothing 0.5\text{mm}$

- Hardness of surface :
Hard surface : $> 3\text{H}$ [JIS K 5400]
- Heat seal intensity: X $>2.0\text{kgf}$ Y $>500\text{gf}$ Z $>200\text{gf}$

2) Electrical characteristics

- Operating Voltage : DC5V
- Loop resistance: X : $550\text{-}950\Omega$ Y : $150\text{-}350\Omega$
- Linearity : $\leq \pm 1.5\%$
- Insulation resistance: $> 10\text{M}\Omega$ At DC 25V.
- Insulation ability: $\geq 60\text{sec.}$ At DC 25V.
- Chatting times: $< 10\text{ms}$



3) Optical characteristics

- Total Transmittance: >78% [JISK7105]

3.4 Processing Environment :

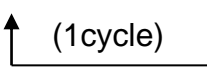
- Operating Temperature: -20°C ~ +70°C
- Operating Humidity: ≤90%RH
- Storage Temperature: -30°C ~ +80°C
- Storage Humidity: < 90%RH

3.5 Environmental test

- High temperature : +70°C , 120 hr.
- Low temperature : -20°C , 120 hr.
- High temp./high humidity test: 70°C & 90%, 120hr.
- High Low temperature test: -30°C 30min/+80°C 30min
- Notes life ≥ 1×10⁵ words min
- Input life ≥ 10⁶ times min



4. RELIABILITY TEST

NO.	ITEM	CONDITION		STANDARD	NOTE
1	High Temp. Storage	80°C	120 hrs	Appearance Without defect	
2	Low Temp. Storage	-30°C	120 hrs	Appearance Without defect	
3	High Temp. & High Humi. Storage	40°C 90% RH	120 hrs	Appearance Without defect	
4	High Temp. Operating Display	70°C	120 hrs	Appearance Without defect	
5	Low Temp. Operating Display	-20°C	120 hrs	Appearance Without defect	
6	Thermal Shock	-20°C, 30min. → 70°C, 30min. 		Appearance Without defect	10 cycles

** Dissipation current, contrast and display functions

** Polarizing filter deterioration, other appearance defects

** The function test shall be conducted after 4hours storage at the normal temperature and humidity after remove from the test chamber.



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI-When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

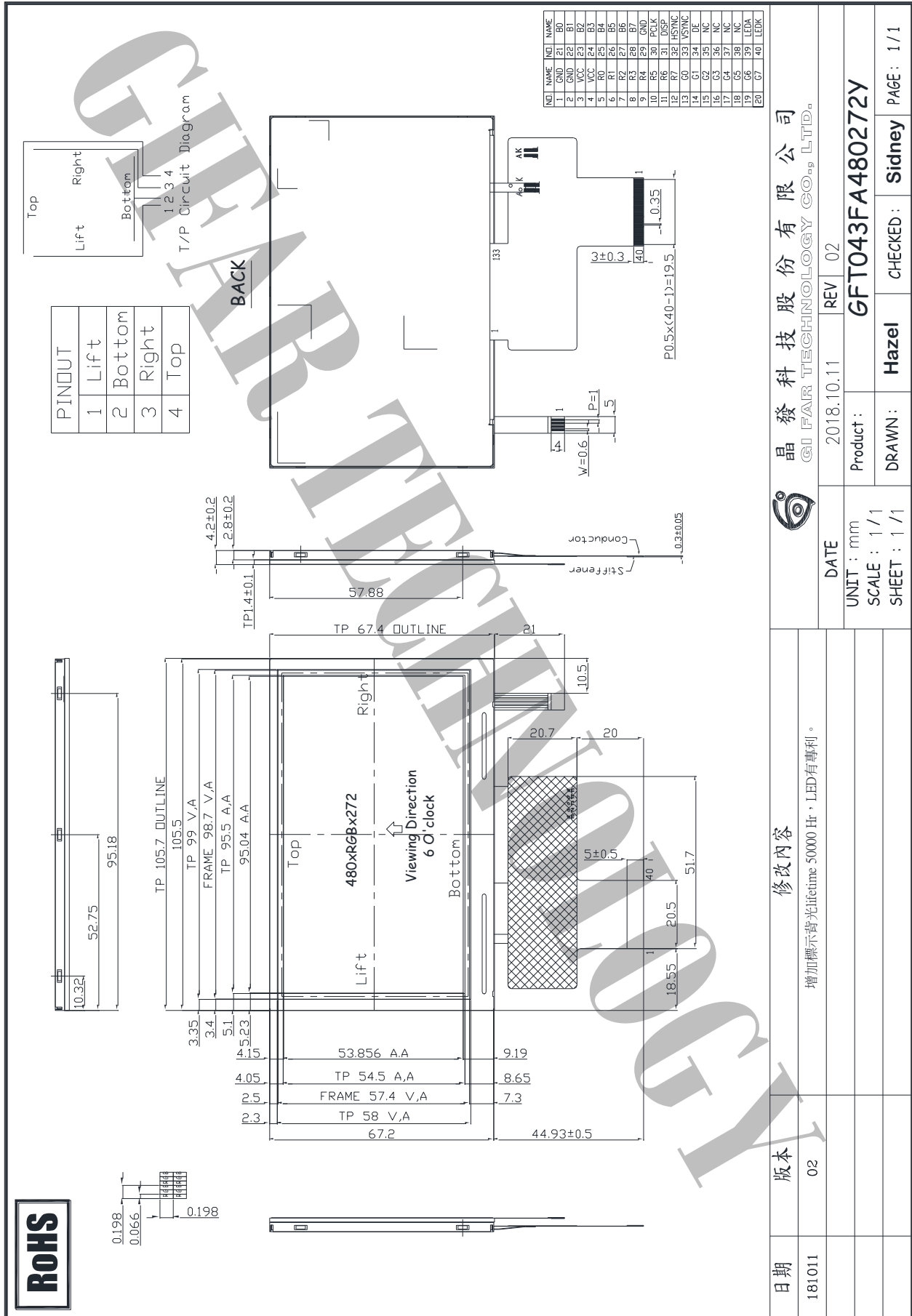
The period is within 12 months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



6. OUTLINE DRAWING:



出貨檢驗標準書
Shipping inspection standard

核准 Approved by	審核 Checked by	作成 Made by
ANDY	JACKY	RUBY

1.目的 Purpose :

規範出貨產品之檢驗項目及判斷標準，確保產品出貨能滿足客戶要求。

Standardize the inspection items and judgment standards to ensure the products that shipped out can meet customer's requirements.

2.範圍 Area :

適用於出廠之所有產品。

Applicable to all products shipped from the factory.

3.名詞解釋 Explanation of terms :

3-1 主要缺陷：亦會造成功能缺失或嚴重外觀缺陷。

Major Defects: It also causes loss of function or serious appearance defects.

3-2 次要缺陷：稍有缺陷但不影響客戶使用。

Minor defect: Slightly defective but does not affect customer use.

4.檢驗體制 Inspection system :

4-1 抽樣計劃：依 ANSI/ASQ Z1.4 一般檢驗水準 II 之 正常檢驗一次抽驗方案。

Sampling plan: According to ANSI/ASQ Z1.4 general inspection level II the normal inspection one-time sampling plan.

4-2 允收水準 Acceptable Level : (AQL)

主要缺陷 Major defect : 0.4 %

次要缺陷 Minor defect : 0.65 %

5.檢驗條件 Inspection conditions :

5-1 使用相關之檢測儀器及測試、量測工具。

Use relevant testing instrument, testing and measuring tools .

5-2 環境要求：其條件需控制在常溫下 $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ 及溼度 70%RH 以下。

Environmental requirements: The conditions should be controlled at room temperature $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ and humidity below 70%RH.

5-3 外觀檢驗：須在 $380\pm 20\%$ LUX 的白色日光燈下，其目視距離需於產品離 30 ± 5 cm 檢驗。

Appearance inspection: Under the white fluorescent lamp of $380\pm 20\%$ LUX , the visual distance shall be checked above the product 30 ± 5 cm.

5-4 電性測試 Electrical Testing :

5-4-1 有背光之產品需關燈並在 $5\sim 300\text{Lux}\pm 3\%$ 下檢驗。

The products with backlight should be tested at $5\sim 300\pm 3\%$ Lux.

5-4-2 無背光之產品需開燈並在 $60\sim 300\text{Lux}\pm 3\%$ 白色日光燈下檢驗。

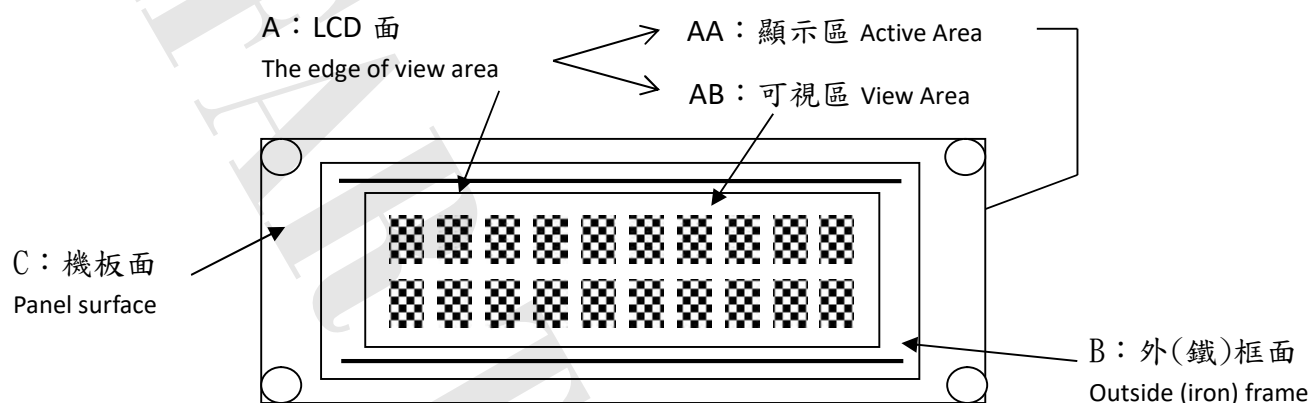
Products without backlight need to be turned on and tested under $60\sim 300 \pm 3\%$ LUX white fluorescent lamps .

5-5 檢查視角依產品視角方向。

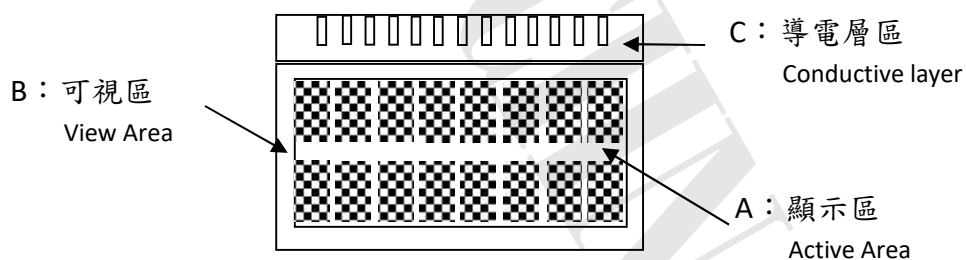
Check the viewing angle according to the product viewing angle.

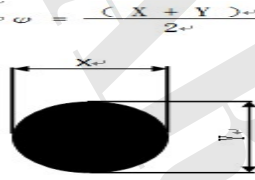
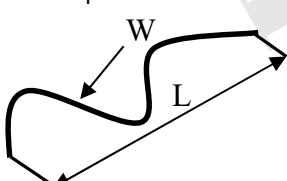
5-6 其不良現象檢視區域 Bad phenomenon View area

5-6-1 適用種類 Applicable category : COB、TFT



5-6-2 適用種類 Applicable category : COG、TAB、TN



種類 Category		TFT																
編號 No.	檢驗項目 Item	檢驗內容及判定標準 Inspection Content & Standard	區域 Zone	類別 Category	缺陷等級 Level													
1	點類(一) Dot (1)	氣泡...圓狀 Bubble ...round shape 	兩點距離須超過 5 mm Two points have to be ≥ 5 mm <table border="1"> <tr> <th>ϕ (mm)</th> <th>允收數 Acceptance Qty</th> </tr> <tr> <td>$\phi \leq 0.25$</td> <td>無視 Ignore</td> </tr> <tr> <td>$0.25 < \phi \leq 0.5$</td> <td>3</td> </tr> <tr> <td>$\phi > 0.5$</td> <td>0</td> </tr> </table>	ϕ (mm)	允收數 Acceptance Qty	$\phi \leq 0.25$	無視 Ignore	$0.25 < \phi \leq 0.5$	3	$\phi > 0.5$	0	A	外觀 Appearance	次要 Minor AQL0.65%				
ϕ (mm)	允收數 Acceptance Qty																	
$\phi \leq 0.25$	無視 Ignore																	
$0.25 < \phi \leq 0.5$	3																	
$\phi > 0.5$	0																	
2	線類 Line	刮傷、毛屑...等線狀 Scratch、Fiber.. and other linear shape. 	<table border="1"> <tr> <th>L (mm)</th> <th>W (mm)</th> <th>允收數 Acceptance Qty</th> </tr> <tr> <td>--</td> <td>$W \leq 0.01$</td> <td>無視 Ignore</td> </tr> <tr> <td>$L \leq 3$</td> <td>$0.01 < W \leq 0.05$</td> <td>3</td> </tr> <tr> <td>$L > 3$</td> <td>$W > 0.05$</td> <td>0</td> </tr> </table>	L (mm)	W (mm)	允收數 Acceptance Qty	--	$W \leq 0.01$	無視 Ignore	$L \leq 3$	$0.01 < W \leq 0.05$	3	$L > 3$	$W > 0.05$	0	A	外觀 Appearance	次要 Minor AQL0.65%
L (mm)	W (mm)	允收數 Acceptance Qty																
--	$W \leq 0.01$	無視 Ignore																
$L \leq 3$	$0.01 < W \leq 0.05$	3																
$L > 3$	$W > 0.05$	0																
3	FPC 外觀 FPC Appearance	※ FPC 上刺傷導致線路無法導通 拒收 Stabbing on the FPC causes the line to fail to conduct Reject ※ FPC 上髒污或是殘留異物以致線路無法導通 拒收 Dirty or residual foreign matter on the FPC makes the circuit unable to conduct Reject ※ FPC 直角折痕、斷裂 拒收 FPC right-angle crease and fracture Reject	C	外觀 Appearance	次要 Minor AQL0.65%													
4	點類(二) Dot (2)	<table border="1"> <tr> <th>類型 Type</th> <th>允收數 Acceptance Qty</th> </tr> <tr> <td>亮點 Highlights</td> <td>$N \leq 2$</td> </tr> <tr> <td>暗點 dark spot</td> <td>$N \leq 3$</td> </tr> </table> ※ 缺陷點面積暫全點 1/2 則為一個缺陷點 Temporarily full area of defect point 1/2 is a defect point ※ 亮點：於黑畫面中使用 2% ND Filter 遮蓋須不可見 Highlights: Use 2% ND Filter in a black screen to cover up invisible ※ 暗點：在純紅、綠、藍模式下判定 Dark spot: judged in pure red, green and blue mode	類型 Type	允收數 Acceptance Qty	亮點 Highlights	$N \leq 2$	暗點 dark spot	$N \leq 3$	AA	電訊 Electronics	次要 Minor AQL0.65%							
類型 Type	允收數 Acceptance Qty																	
亮點 Highlights	$N \leq 2$																	
暗點 dark spot	$N \leq 3$																	

5	無動作 No reaction	顯示畫面一直處於起始畫面而無法進行切換 拒收 The display (view area) always shows in the initial screen and can't be switched to others. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
6	無畫面 No display	通電後，完全無任何畫面顯示 拒收 After connecting to the power, there is no display. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
7	斷線 Broken line	顯示畫面中少直、橫線 拒收 There is a lack of vertical or horizontal lines in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
8	I CON	顯示畫面缺少部份顯示圖案 拒收 Lack of partial ICON in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
9	深淺不一 Color difference	顯示畫面的對比，比其他顯示深或淺並依電氣規格(VOP)值判定 The contrast of display is obviously lighter or darker than others and according to the VOP value in the electronics specification. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	AA	電訊 Electronics	次要 Minor AQL0.65%
10	畫面異常 Abnormal screen	通電後畫面出現未定義之電訊不良現象 拒收 After connecting to the power, there is a undefined electronics appearance showing in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
11	牛頓環 Newton ring	點亮後目視有環、圓或曲線狀 拒收 There are rings, circles or curves visually after lighting Reject	A	電訊 Electronics	次要 Minor AQL0.65%
12	背光色不均 Uneven color of backlight	※ 點亮後 LED 有明暗不均現象依其均勻度判定 拒收 After lighting LEDs have brightness and darkness uneven the determined according to its uniformity. Reject ※ 點亮後 LED 色澤不一致 拒收 LED color is inconsistent after lighting Reject	A	電訊 Electronics	次要 Minor AQL0.65%
13	亮度不足 Lack of brightness	波長、色座標、輝度與圖面標示定義不符 拒收 Wave length, chromatic coordinates, brightness don't correspond to the definition of the drawing. Reject	A	電訊 Electronics	主要 Major AQL 0.4%

TFT

14	觸控 Touch	測試時無法點觸或劃，其靈敏度判定則依 SPEC 上定義判定 拒收 It cannot be touched or swiped during the test. Its sensitivity is judged according to the definition on SPEC Reject	A	電訊 Electronics	主要 Major AQL 0.4%
15	尺寸量測 Size Measurement	未依圖面上標示 拒收 No correspond to the indication on the drawing. Reject	ALL	外觀 Appearance	主要 Major AQL 0.4%
16	其他 Other	如發現有上述未定義之不良則與客端簽訂限度樣 If there is another undefined defective situation. It will be listed as others. The inspection standard is according to the golden sample.	ALL	電訊 Electronics 外觀 Appearance	次要 Minor AQL0.65%