

74AUP1T00

Low-power 2-input NAND gate with voltage-level translator

Rev. 3 — 25 January 2022

Product data sheet

1. General description

The 74AUP1T00 provides the single 2-input NAND function. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 2.3 V to 3.6 V.

The 74AUP1T00 is designed for logic-level translation applications with input switching levels that accept 1.8 V low-voltage CMOS signals, while operating from either a single 2.5 V or 3.3 V supply voltage.

The wide supply voltage range ensures normal operation as battery voltage drops from 3.6 V to 2.3 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Schmitt trigger inputs make the circuit tolerant to slower input rise and fall times across the entire V_{CC} range.

2. Features and benefits

- Wide supply voltage range from 2.3 V to 3.6 V
- High noise immunity
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; $I_{CC} = 1.5 \mu\text{A}$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial power-down mode operation
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

| Type number | Package | | | Version |
|-------------|-------------------|--------|--|-----------|
| | Temperature range | Name | Description | |
| 74AUP1T00GW | -40 °C to +125 °C | TSSOP5 | plastic thin shrink small outline package; 5 leads; body width 1.25 mm | SOT353-1 |
| 74AUP1T00GX | -40 °C to +125 °C | X2SON5 | plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm | SOT1226-3 |

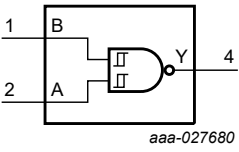
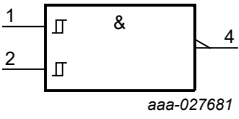
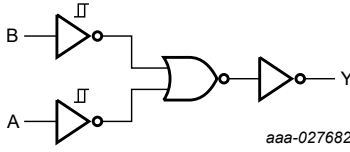
4. Marking

Table 2. Marking

| Type number | Marking code [1] |
|-------------|------------------|
| 74AUP1T00GW | 5a |
| 74AUP1T00GX | 5a |

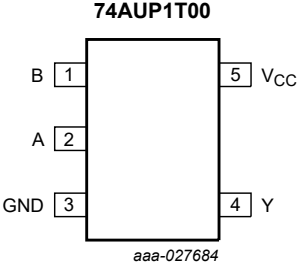
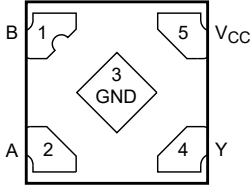
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

| | | |
|--|--|---|
|  <p>Fig. 1. Logic symbol</p> |  <p>Fig. 2. IEC logic symbol</p> |  <p>Fig. 3. Logic diagram</p> |
|--|--|---|

6. Pinning information

6.1. Pinning

| | |
|---|--|
|  <p>Fig. 4. Pin configuration SOT353-1 (TSSOP5)</p> |  <p>Fig. 5. Pin configuration SOT1226-3 (X2SON5)</p> |
|---|--|

6.2. Pin description

Table 3. Pin description

| Symbol | Pin | Description |
|-----------------|-----|----------------|
| B | 1 | data input |
| A | 2 | data input |
| GND | 3 | ground (0 V) |
| Y | 4 | data output |
| V _{CC} | 5 | supply voltage |

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

| Input | | Output |
|-------|---|--------|
| A | B | Y |
| L | L | H |
| L | H | H |
| H | L | H |
| H | H | L |

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|-------------------------|---------------------------------|----------|------|------|
| V_{CC} | supply voltage | | -0.5 | +4.6 | V |
| I_{IK} | input clamping current | $V_I < 0$ V | -50 | - | mA |
| V_I | input voltage | | [1] -0.5 | +4.6 | V |
| I_{OK} | output clamping current | $V_O < 0$ V | -50 | - | mA |
| V_O | output voltage | Active mode and Power-down mode | [1] -0.5 | +4.6 | V |
| I_O | output current | $V_O = 0$ V to V_{CC} | - | ±20 | mA |
| I_{CC} | supply current | | - | 50 | mA |
| I_{GND} | ground current | | -50 | - | mA |
| T_{stg} | storage temperature | | -65 | +150 | °C |
| P_{tot} | total power dissipation | $T_{amb} = -40$ °C to +125 °C | [2] - | 250 | mW |

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.
For SOT1226-3 (X2SON5) package: P_{tot} derates linearly with 3.0 mW/K above 67 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

| Symbol | Parameter | Conditions | Min | Max | Unit |
|-----------|---------------------|---------------------------------|-----|----------|------|
| V_{CC} | supply voltage | | 2.3 | 3.6 | V |
| V_I | input voltage | | 0 | 3.6 | V |
| V_O | output voltage | Active mode | 0 | V_{CC} | V |
| | | Power-down mode; $V_{CC} = 0$ V | 0 | 3.6 | V |
| T_{amb} | ambient temperature | | -40 | +125 | °C |

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--------------------------------|--------------------------------------|---|-----------------------|-----|------|------|
| T_{amb} = 25 °C | | | | | | |
| V _{T+} | positive-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.60 | - | 1.10 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.75 | - | 1.16 | V |
| V _{T-} | negative-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.35 | - | 0.60 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.50 | - | 0.85 | V |
| V _H | hysteresis voltage | (V _H = V _{T+} - V _{T-}) | | | | |
| | | V _{CC} = 2.3 V to 2.7 V | 0.23 | - | 0.60 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.25 | - | 0.56 | V |
| V _{OH} | HIGH-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = -20 μA; V _{CC} = 2.3 V to 3.6 V | V _{CC} - 0.1 | - | - | V |
| | | I _O = -2.3 mA; V _{CC} = 2.3 V | 2.05 | - | - | V |
| | | I _O = -3.1 mA; V _{CC} = 2.3 V | 1.9 | - | - | V |
| | | I _O = -2.7 mA; V _{CC} = 3.0 V | 2.72 | - | - | V |
| | | I _O = -4.0 mA; V _{CC} = 3.0 V | 2.6 | - | - | V |
| V _{OL} | LOW-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = 20 μA; V _{CC} = 2.3 V to 3.6 V | - | - | 0.10 | V |
| | | I _O = 2.3 mA; V _{CC} = 2.3 V | - | - | 0.31 | V |
| | | I _O = 3.1 mA; V _{CC} = 2.3 V | - | - | 0.44 | V |
| | | I _O = 2.7 mA; V _{CC} = 3.0 V | - | - | 0.31 | V |
| | | I _O = 4.0 mA; V _{CC} = 3.0 V | - | - | 0.44 | V |
| I _I | input leakage current | V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V | - | - | ±0.1 | μA |
| I _{OFF} | power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V | - | - | ±0.1 | μA |
| ΔI _{OFF} | additional power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V | - | - | ±0.1 | μA |
| I _{CC} | supply current | V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 2.3 V to 3.6 V | - | - | 1.2 | μA |
| C _I | input capacitance | V _{CC} = 0 V to 3.6 V; V _I = GND or V _{CC} | - | 0.8 | - | pF |
| C _O | output capacitance | V _O = GND; V _{CC} = 0 V | - | 1.7 | - | pF |

Low-power 2-input NAND gate with voltage-level translator

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|--------------------------------------|---|-----------------------|-----|------|------|
| T_{amb} = -40 °C to +85 °C | | | | | | |
| V _{T+} | positive-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.60 | - | 1.10 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.75 | - | 1.19 | V |
| V _{T-} | negative-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.35 | - | 0.60 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.50 | - | 0.85 | V |
| V _H | hysteresis voltage | (V _H = V _{T+} - V _{T-}) | | | | |
| | | V _{CC} = 2.3 V to 2.7 V | 0.10 | - | 0.60 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.15 | - | 0.56 | V |
| V _{OH} | HIGH-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = -20 µA; V _{CC} = 2.3 V to 3.6 V | V _{CC} - 0.1 | - | - | V |
| | | I _O = -2.3 mA; V _{CC} = 2.3 V | 1.97 | - | - | V |
| | | I _O = -3.1 mA; V _{CC} = 2.3 V | 1.85 | - | - | V |
| | | I _O = -2.7 mA; V _{CC} = 3.0 V | 2.67 | - | - | V |
| | | I _O = -4.0 mA; V _{CC} = 3.0 V | 2.55 | - | - | V |
| V _{OL} | LOW-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = 20 µA; V _{CC} = 2.3 V to 3.6 V | - | - | 0.1 | V |
| | | I _O = 2.3 mA; V _{CC} = 2.3 V | - | - | 0.33 | V |
| | | I _O = 3.1 mA; V _{CC} = 2.3 V | - | - | 0.45 | V |
| | | I _O = 2.7 mA; V _{CC} = 3.0 V | - | - | 0.33 | V |
| | | I _O = 4.0 mA; V _{CC} = 3.0 V | - | - | 0.45 | V |
| I _I | input leakage current | V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V | - | - | ±0.5 | µA |
| I _{OFF} | power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V | - | - | ±0.5 | µA |
| ΔI _{OFF} | additional power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V | - | - | ±0.5 | µA |
| I _{CC} | supply current | V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 2.3 V to 3.6 V | - | - | 1.5 | µA |
| ΔI _{CC} | additional supply current | V _{CC} = 2.3 V to 2.7 V; I _O = 0 A [1] | - | - | 0.6 | µA |
| | | V _{CC} = 3.0 V to 3.6 V; I _O = 0 A [2] | - | - | 10 | µA |

Low-power 2-input NAND gate with voltage-level translator

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|--------------------------------------|---|------------------------|-----|-------|------|
| T_{amb} = -40 °C to +125 °C | | | | | | |
| V _{T+} | positive-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.60 | - | 1.10 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.75 | - | 1.19 | V |
| V _{T-} | negative-going threshold voltage | V _{CC} = 2.3 V to 2.7 V | 0.33 | - | 0.64 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.46 | - | 0.85 | V |
| V _H | hysteresis voltage | (V _H = V _{T+} - V _{T-}) | | | | |
| | | V _{CC} = 2.3 V to 2.7 V | 0.10 | - | 0.60 | V |
| | | V _{CC} = 3.0 V to 3.6 V | 0.15 | - | 0.56 | V |
| V _{OH} | HIGH-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = -20 µA; V _{CC} = 2.3 V to 3.6 V | V _{CC} - 0.11 | - | - | V |
| | | I _O = -2.3 mA; V _{CC} = 2.3 V | 1.77 | - | - | V |
| | | I _O = -3.1 mA; V _{CC} = 2.3 V | 1.67 | - | - | V |
| | | I _O = -2.7 mA; V _{CC} = 3.0 V | 2.40 | - | - | V |
| | | I _O = -4.0 mA; V _{CC} = 3.0 V | 2.30 | - | - | V |
| V _{OL} | LOW-level output voltage | V _I = V _{T+} or V _{T-} | | | | |
| | | I _O = 20 µA; V _{CC} = 2.3 V to 3.6 V | - | - | 0.11 | V |
| | | I _O = 2.3 mA; V _{CC} = 2.3 V | - | - | 0.36 | V |
| | | I _O = 3.1 mA; V _{CC} = 2.3 V | - | - | 0.50 | V |
| | | I _O = 2.7 mA; V _{CC} = 3.0 V | - | - | 0.36 | V |
| | | I _O = 4.0 mA; V _{CC} = 3.0 V | - | - | 0.50 | V |
| I _I | input leakage current | V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V | - | - | ±0.75 | µA |
| I _{OFF} | power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V | - | - | ±0.75 | µA |
| ΔI _{OFF} | additional power-off leakage current | V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V | - | - | ±0.75 | µA |
| I _{CC} | supply current | V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 2.3 V to 3.6 V | - | - | 3.5 | µA |
| ΔI _{CC} | additional supply current | V _{CC} = 2.3 V to 2.7 V; I _O = 0 A [1] | - | - | 1.8 | µA |
| | | V _{CC} = 3.0 V to 3.6 V; I _O = 0 A [2] | - | - | 18 | µA |

[1] One input at 0.3 V or 1.1 V, other input at V_{CC} or GND.

[2] One input at 0.45 V or 1.2 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 7.

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|--|-------------------|---------------------------|-------|--------|-----|------------------|------|-------------------|------|------|
| | | | Min | Typ[1] | Max | Min | Max | Min | Max | |
| V_{CC} = 2.3 V to 2.7 V; V_I = 1.65 V to 1.95 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.9 | 3.4 | 5.3 | 0.5 | 6.8 | 0.5 | 7.5 | ns |
| | | C _L = 10 pF | 2.4 | 3.9 | 6.0 | 1.0 | 7.9 | 1.0 | 8.7 | ns |
| | | C _L = 15 pF | 2.8 | 4.4 | 6.6 | 1.0 | 8.7 | 1.0 | 9.6 | ns |
| | | C _L = 30 pF | 3.8 | 5.6 | 8.0 | 1.5 | 10.8 | 1.5 | 11.9 | ns |
| V_{CC} = 2.3 V to 2.7 V; V_I = 2.3 V to 2.7 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.4 | 3.2 | 5.3 | 0.5 | 6.0 | 0.5 | 6.6 | ns |
| | | C _L = 10 pF | 1.9 | 3.8 | 6.0 | 1.0 | 7.1 | 1.0 | 7.9 | ns |
| | | C _L = 15 pF | 2.3 | 4.3 | 6.6 | 1.0 | 7.9 | 1.0 | 8.7 | ns |
| | | C _L = 30 pF | 3.4 | 5.5 | 8.0 | 1.5 | 10.0 | 1.5 | 11.0 | ns |
| V_{CC} = 2.3 V to 2.7 V; V_I = 3.0 V to 3.6 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.2 | 3.0 | 4.8 | 0.5 | 5.5 | 0.5 | 6.1 | ns |
| | | C _L = 10 pF | 1.6 | 3.5 | 5.5 | 1.0 | 6.5 | 1.0 | 7.2 | ns |
| | | C _L = 15 pF | 2.1 | 4.0 | 6.1 | 1.0 | 7.4 | 1.0 | 8.2 | ns |
| | | C _L = 30 pF | 3.1 | 5.2 | 7.5 | 1.5 | 9.5 | 1.5 | 10.5 | ns |
| V_{CC} = 3.0 V to 3.6 V; V_I = 1.65 V to 1.95 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.9 | 2.8 | 3.9 | 0.5 | 8.0 | 0.5 | 9.0 | ns |
| | | C _L = 10 pF | 2.3 | 3.4 | 4.7 | 1.0 | 8.5 | 1.0 | 9.4 | ns |
| | | C _L = 15 pF | 2.6 | 3.8 | 5.3 | 1.0 | 9.1 | 1.0 | 10.1 | ns |
| | | C _L = 30 pF | 3.4 | 5.0 | 6.8 | 1.5 | 9.8 | 1.5 | 10.8 | ns |
| V_{CC} = 3.0 V to 3.6 V; V_I = 2.3 V to 2.7 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.4 | 2.7 | 4.2 | 0.5 | 5.3 | 0.5 | 5.9 | ns |
| | | C _L = 10 pF | 1.9 | 3.3 | 4.9 | 1.0 | 6.1 | 1.0 | 6.8 | ns |
| | | C _L = 15 pF | 2.3 | 3.7 | 5.5 | 1.0 | 6.8 | 1.0 | 7.5 | ns |
| | | C _L = 30 pF | 3.3 | 4.9 | 6.8 | 1.5 | 8.5 | 1.5 | 9.4 | ns |
| V_{CC} = 3.0 V to 3.6 V; V_I = 3.0 V to 3.6 V | | | | | | | | | | |
| t _{pd} | propagation delay | A, B to Y; see Fig. 6 [2] | | | | | | | | |
| | | C _L = 5 pF | 1.0 | 2.6 | 4.3 | 0.5 | 4.7 | 0.5 | 5.2 | ns |
| | | C _L = 10 pF | 1.6 | 3.2 | 5 | 1.0 | 5.7 | 1.0 | 6.3 | ns |
| | | C _L = 15 pF | 2.0 | 3.7 | 5.6 | 1.0 | 6.2 | 1.0 | 6.9 | ns |
| | | C _L = 30 pF | 3.0 | 4.8 | 6.9 | 1.5 | 7.8 | 1.5 | 8.6 | ns |

Low-power 2-input NAND gate with voltage-level translator

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|--------------------------------|-------------------------------|---|-------|--------|-----|------------------|-----|-------------------|-----|------|
| | | | Min | Typ[1] | Max | Min | Max | Min | Max | |
| T_{amb} = 25 °C | | | | | | | | | | |
| C _{PD} | power dissipation capacitance | f _i = 1 MHz; V _I = GND to V _{CC} [3] | | | | | | | | |
| | | V _{CC} = 2.3 V to 2.7 V | - | 4 | - | - | - | - | - | pF |
| | | V _{CC} = 3.0 V to 3.6 V | - | 5 | - | - | - | - | - | pF |

- [1] All typical values are measured at nominal V_{CC}.
- [2] t_{pd} is the same as t_{PLH} and t_{PHL}
- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).
 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:
 f_i = input frequency in MHz;
 f_o = output frequency in MHz;
 C_L = output load capacitance in pF;
 V_{CC} = supply voltage in V;
 N = number of inputs switching;
 Σ(C_L × V_{CC}² × f_o) = sum of the outputs.

11.1. Waveforms and test circuit

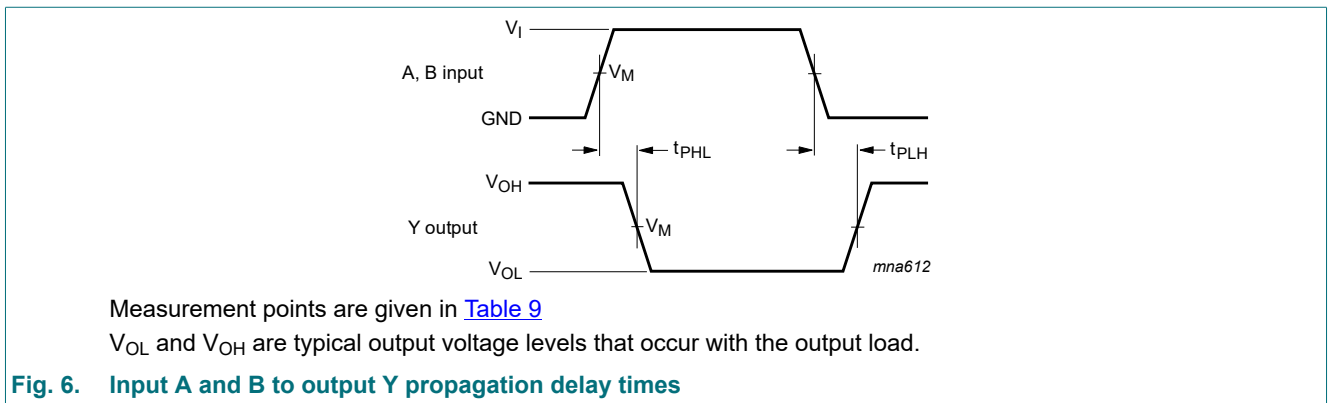
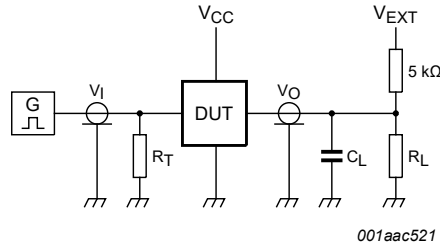


Table 9. Measurement points

| Supply voltage | Output | Input | | |
|-----------------|-----------------------|----------------------|-----------------|---------------------------------|
| V _{CC} | V _M | V _M | V _I | t _r = t _f |
| 2.3 V to 3.6 V | 0.5 × V _{CC} | 0.5 × V _I | 1.65 V to 3.6 V | ≤ 3.0 ns |

Low-power 2-input NAND gate with voltage-level translator



Test data is given in [Table 10](#).

Definitions for test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

V_{EXT} = External voltage for measuring switching times.

Fig. 7. Test circuit for measuring switching times

Table 10. Test data

| Supply voltage | Load | | V_{EXT} | | |
|----------------|------------------------------|--------------|-----------------------|-----------------------|-----------------------|
| V_{CC} | C_L | R_L [1] | t_{PLH} , t_{PHL} | t_{PZH} , t_{PHZ} | t_{PZL} , t_{PLZ} |
| 2.3 V to 3.6 V | 5 pF, 10 pF, 15 pF and 30 pF | 5 kΩ or 1 MΩ | open | GND | $2 \times V_{CC}$ |

[1] For measuring enable and disable times $R_L = 5 \text{ k}\Omega$.
 For measuring propagation delays, setup and hold times and pulse width $R_L = 1 \text{ M}\Omega$.

12. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



Fig. 8. Package outline SOT353-1 (TSSOP5)

X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.32 mm

SOT1226-3



Fig. 9. Package outline SOT1226-3 (X2SON5)

13. Abbreviations

Table 11. Abbreviations

| Acronym | Description |
|---------|---|
| CDM | Charged Device Model |
| CMOS | Complementary Metal-Oxide Semiconductor |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |

14. Revision history

Table 12. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|---|--------------------|---------------|---------------|
| 74AUP1T00 v.3 | 20220125 | Product data sheet | - | 74AUP1T00 v.2 |
| Modifications: | <ul style="list-style-type: none"> • Fig. 8: Package outline drawing for SOT353-1 has changed. | | | |
| 74AUP1T00 v.2 | 20210719 | Product data sheet | - | 74AUP1T00 v.1 |
| Modifications: | <ul style="list-style-type: none"> • SOT1226 (X2SON5) package changed to SOT1226-3 (X2SON5) package. • Table 5: Derating values for P_{tot} total power dissipation updated. | | | |
| 74AUP1T00 v.1 | 20171123 | Product data sheet | - | - |

15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

| | |
|--|-----------|
| 1. General description | 1 |
| 2. Features and benefits | 1 |
| 3. Ordering information | 1 |
| 4. Marking | 2 |
| 5. Functional diagram | 2 |
| 6. Pinning information | 2 |
| 6.1. Pinning..... | 2 |
| 6.2. Pin description..... | 2 |
| 7. Functional description | 3 |
| 8. Limiting values | 3 |
| 9. Recommended operating conditions | 3 |
| 10. Static characteristics | 4 |
| 11. Dynamic characteristics | 7 |
| 11.1. Waveforms and test circuit..... | 8 |
| 12. Package outline | 10 |
| 13. Abbreviations | 12 |
| 14. Revision history | 12 |
| 15. Legal information | 13 |

© Nexperia B.V. 2022. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 25 January 2022
