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Upgrade to Domain Specific kits



Upgrade		
From	To	Part Number
Spartan®-6 FPGA Evaluation Kit	Spartan-6 FPGA Connectivity Kit	UP-S6-SP605-CONN-G
<p>Upgrade Kit Deliverables</p> <ul style="list-style-type: none"> Four SMA Cables Spartan®-6 FPGA Connectivity Kit Compact Flash card – 2GB License for ISE® Design Suite Embedded Edition (Device Locked to Spartan-6 LX45T) FULL seat of Production Netlist of Northwest Logic x1 Packet DMA Back End Core (node-locked License for one-seat) Documentation <ul style="list-style-type: none"> Hardware Setup Guide for the Spartan-6 FPGA Connectivity Kit Getting Started with the Spartan-6 FPGA Connectivity Kit USB Stick containing <ul style="list-style-type: none"> Spartan-6 Connectivity Targeted Reference Design: PCIe-GbE ISE Project Files, Simulation and Implementation Environment RTL Design Files Northwest Logic – PCIe Packet DMA (Simulation Model + Encrypted Netlist with hardware timeout, User Guide) Targeted Reference Design Software Interface - Device Drivers, Application, Demonstration Spartan-6 Connectivity Targeted Reference Design User Guide Example Designs and demonstrations Documentation <ul style="list-style-type: none"> Spartan-6 FPGA Connectivity Kit Hardware Setup Guide Spartan-6 FPGA Connectivity Kit Getting Started Guide Board Design and Layout Files Fedora Core 10 Live Media CD (Linux OS for Intel-compatible PCs) 		
Upgrade		
From	To	Part Number
Spartan-6 FPGA Embedded Kit	Spartan-6 FPGA Connectivity Kit	UP-S6-EMBD-CONN-G
<p>Upgrade Kit Deliverables</p> <ul style="list-style-type: none"> Four SMA Cables Spartan-6 FPGA Connectivity Kit Compact Flash card – 2GB FULL seat of Production Netlist of Northwest Logic x1 Packet DMA Back End Core (node-locked License for one-seat) Documentation <ul style="list-style-type: none"> Hardware Setup Guide for the Spartan-6 FPGA Connectivity Kit Getting Started with the Spartan-6 FPGA Connectivity Kit USB Stick containing <ul style="list-style-type: none"> Spartan-6 Connectivity Targeted Reference Design: PCIe-GbE ISE Project Files, Simulation and Implementation Environment RTL Design Files Northwest Logic – PCIe Packet DMA (Simulation Model + Encrypted Netlist with hardware timeout, User Guide) Targeted Reference Design Software Interface - Device Drivers, Application, Demonstration Spartan-6 Connectivity Targeted Reference Design User Guide Example Designs and demonstrations Documentation <ul style="list-style-type: none"> Spartan-6 FPGA Connectivity Kit Hardware Setup Guide Spartan-6 FPGA Connectivity Kit Getting Started Guide § Board Design and Layout Files Fedora Core 10 Live Media CD (Linux OS for Intel-compatible PCs) 		

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