



## Product Change Notification / SYST-28KIJR336

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### Date:

01-Feb-2022

### Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

### PCN Type:

Document Change

### Notification Subject:

Data Sheet - dsPIC33CK512MP608 Family Data Sheet Document Revision

### Affected CPNs:

[SYST-28KIJR336\\_Affected\\_CPN\\_02012022.pdf](#)

[SYST-28KIJR336\\_Affected\\_CPN\\_02012022.csv](#)

### Notification Text:

SYST-28KIJR336

Microchip has released a new Product Documents for the dsPIC33CK512MP608 Family Data Sheet of devices. If you are using one of these devices please read the document located at [dsPIC33CK512MP608 Family Data Sheet](#).

**Notification Status:** Final

#### Description of Change:

1) This revision incorporates the following updates:

- Sections:

- Updates "Advanced Analog Features", "Peripheral Features", "Functional Safety Readiness", "13. High-Speed, 12-Bit Analog-to-Digital Converter", "13.1 ADC Features Overview", "13.2 Temperature Sensor".
- Adds "9.4.1 Primary Oscillator Pin Functionality", "14.1 Overview", "16. Universal Asynchronous Receiver Transmitter (UART)" and "Product Identification System".

- Registers:

- Updates 7.7.4 Interrupt Request Flags Register 3, 7.7.5 Interrupt Request Flags Register 4, 7.7.8 Interrupt Request Flags Register 7, 7.7.10 Interrupt Request Flags Register 9, 7.7.12 Interrupt Request Flags Register 11, 7.7.17 Interrupt Enable Register 3, 7.7.18 Interrupt Enable Register 4, 7.7.74 Interrupt Control Register 2, 7.7.75 Interrupt Control Register 3, 9.11.4 FRC Oscillator Tuning Register, 12.6.9 Combinatorial PWM Logic Control Register C, 12.6.10 PWM Event Output Control Register E, 13.4.8 ADC Control Register 4 High, 13.4.23 ADC Channel Trigger 0

Selection Register Low through 13.4.33 ADC Channel Trigger 5 Selection Register Low, 14.4.4 DACx Control Low Register, 17.1.1 SPIx Control Register 1 Low, 22.6.4 CCPx Control 2 High Register, 22.6.6 CCPx Status Register, 23.1.1 CLCx Control Register Low and 28.1.1 Deadman Timer Control Register.

- Adds 9.11.12 Reference Clock Trim High Register, 20.1.2 Timer1 Counter Register and 20.1.3 Period Register 1.
- Corrects bit ranges for PWM Control registers and CAN Control/Status registers.
- Updates address offsets for PTG registers.

- Tables:

- Updates Table 7-1, Table 7-2, Table 10-1, Table 8-4, Table 22-6, Table 31-2, Table 33-10 and Table 33-45.
- Adds Table 33-42.

- Figures:

- Updates Figure 4-2, Figure 4-3 and Figure 14-1.

2) Adds minor text edits throughout document.

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 1 Feb 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[dsPIC33CK512MP608 Family Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

DSPIC33CK512MP608-E/PT  
DSPIC33CK512MP608-H/PT  
DSPIC33CK512MP608-I/PT  
DSPIC33CK512MP608T-E/PT  
DSPIC33CK512MP608T-I/PT