

Features

- N-Channel Switch with Low $R_{DS(ON)}$
- Operated at Low Logic Level Gate Drive
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

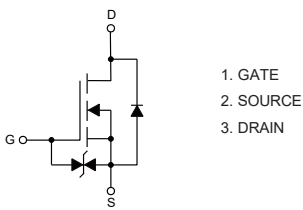
Maximum Ratings

- Operating Junction Temperature Range: -55°C to $+150^{\circ}\text{C}$
- Storage Temperature Range: -55°C to $+150^{\circ}\text{C}$
- Maximum Thermal Resistance: 625°C/W Junction to Ambient

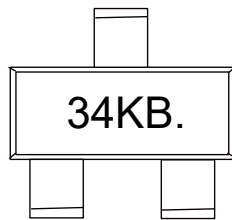
Parameter	Symbol	Rating	Unit
Drain -source Voltage	V_{DS}	20	V
Gate -Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	0.75	A
Pulsed Drain Current $\Phi_{I}^{\oplus D}$	I_{DM}	3.0	A
Power Dissipation $\Phi_{I}^{\oplus D}$	P_D	0.2	W

- Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
 3. This test is performed with no heat sink at $T_a=25^{\circ}\text{C}$.

Internal Structure and Marking Code

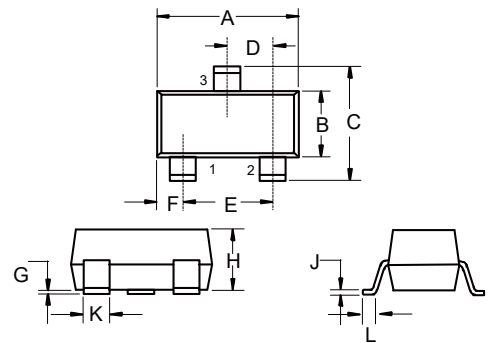


1. GATE
2. SOURCE
3. DRAIN



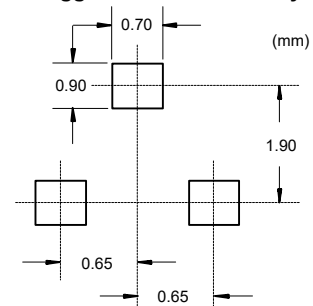
N-Channel MOSFET

SOT-323



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.071	0.087	1.80	2.20	
B	0.045	0.053	1.15	1.35	
C	0.083	0.096	2.10	2.45	
D	0.026		0.65		TYP.
E	0.047	0.055	1.20	1.40	
F	0.012	0.016	0.30	0.40	
G	0.000	0.004	0.00	0.10	
H	0.035	0.044	0.90	1.10	
J	0.002	0.010	0.05	0.25	
K	0.006	0.016	0.15	0.40	
L	0.010	0.018	0.26	0.46	

Suggested Solder Pad Layout



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	20			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 10V$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage ^(Note4)	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	1.1	V
Drain-Source On-Resistance ^(Note4)	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=500mA$			300	m Ω
		$V_{GS}=2.5V, I_D=400mA$			400	m Ω
		$V_{GS}=1.8V, I_D=200mA$			700	m Ω
Diode Forward Voltage ^(Note4)	V_{SD}	$V_{GS}=0V, I_S=500mA$			1.2	V
Dynamic Characteristics^(Note5,6)						
Input Capacitance	C_{iss}	$V_{DS}=16V, V_{GS}=0V, f=1MHz$		33		pF
Output Capacitance	C_{oss}			20		
Reverse Transfer Capacitance	C_{rss}			10		
Total Gate Charge	Q_g	$V_{GS}=4.5V, V_{DS}=10V, I_D=1A$		800		pC
Gate-Source Charge	Q_{gs}			290		
Gate-Drain Charge	Q_{gd}			160		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=4.5V, V_{DS}=10V, I_{DS}=0.5A, R_G=10\Omega$		4		ns
Turn-On Rise Time	t_r			18		
Turn-Off Delay Time	$t_{d(off)}$			11.6		
Turn-Off Fall Time	t_f			24		

Note 4. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

5. Switching characteristics are independent of operating junction temperature.

6. Guaranteed by Design, Not Subject to Production Testing.

Curve Characteristics

Fig. 1 - Output Characteristics

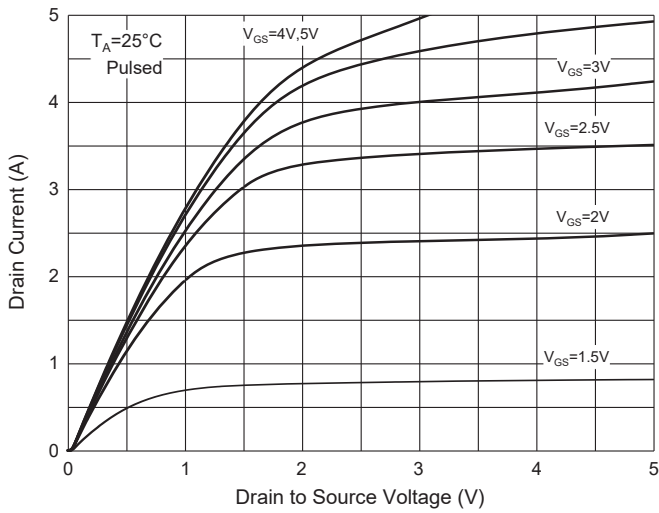


Fig. 2 - Transfer Characteristics

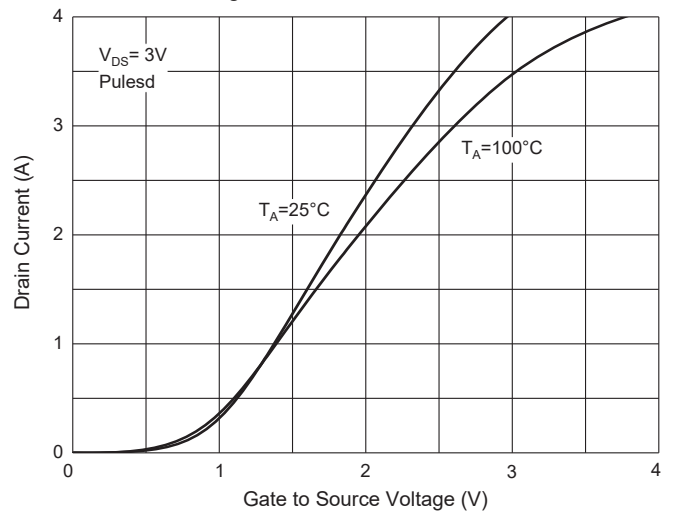


Fig. 3 - $R_{DS(ON)} - I_D$

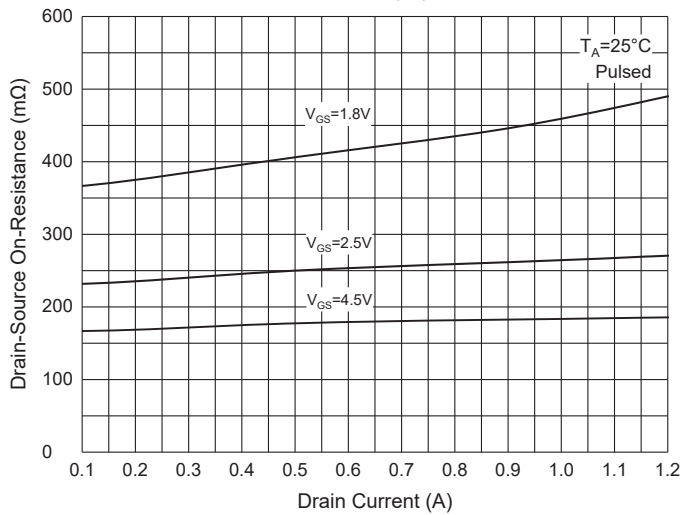


Fig. 4 - Threshold Voltage

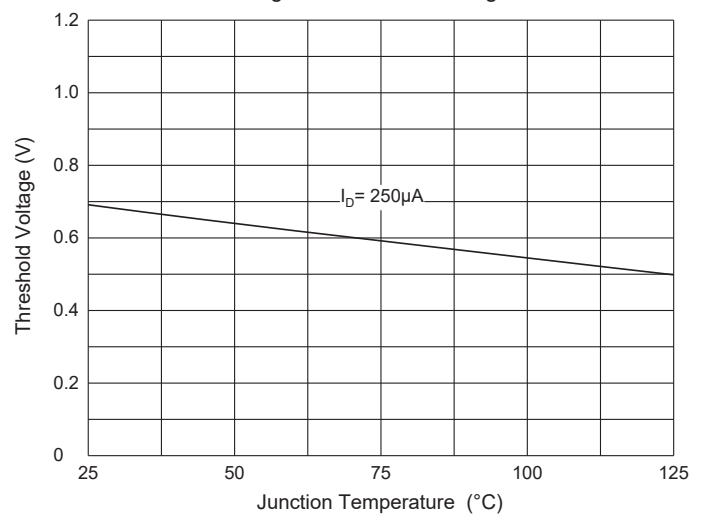


Fig. 5 - Gate Charge

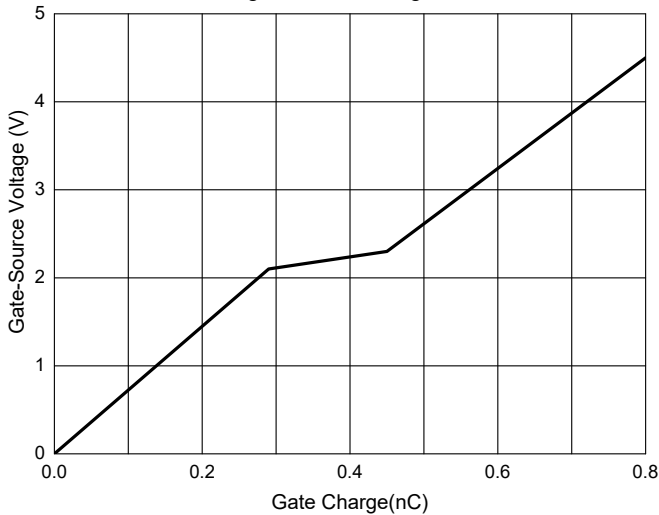
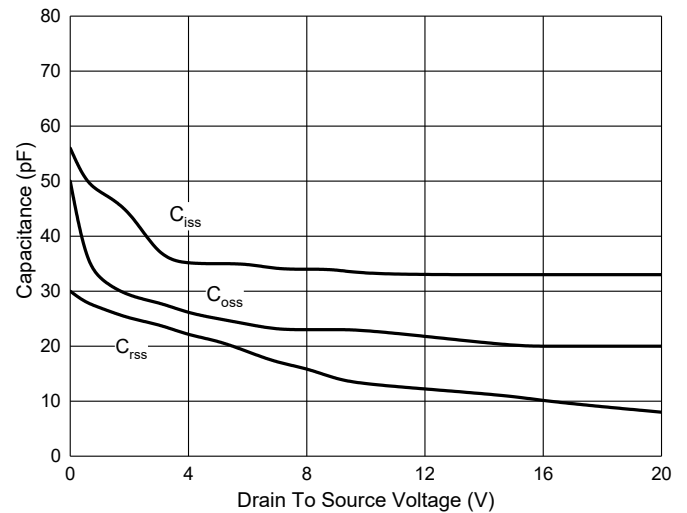


Fig. 6 - Capacitance Characteristics



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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