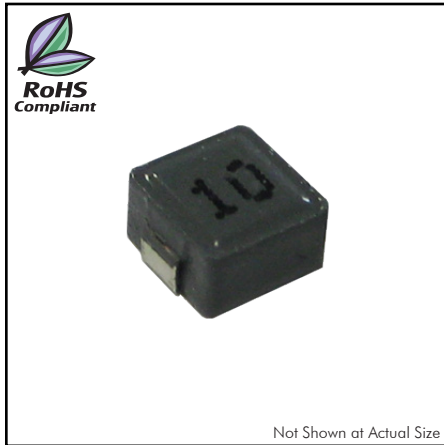


# CTMMP1307AF Series

From 1.0 $\mu$ H to 10 $\mu$ H



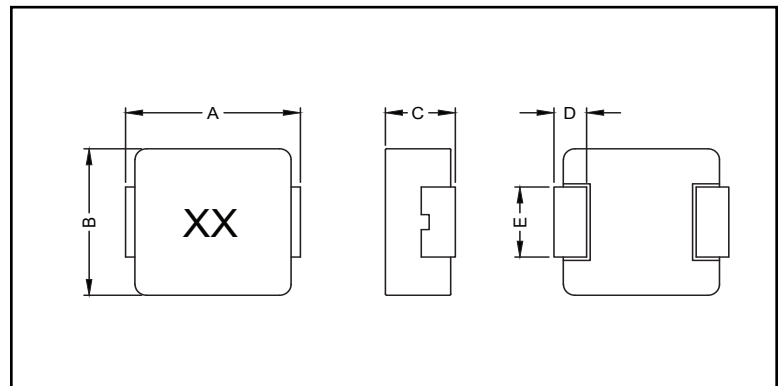
## SPECIFICATIONS

Parts are available in  $\pm 20\%$  inductance tolerance only.  
 \*I<sub>rms</sub> DC current (A) will cause an approximately  $\Delta T$  of 40°C (keep 1min.)  
 \*\*I<sub>sat</sub> DC current (A) will cause L<sub>0</sub> to drop approximately 30%

Part Number	Inductance @0A ( $\mu$ H)	*I <sub>rms</sub> Typ. (A)	**I <sub>sat</sub> Typ. (A)	DCR Typ. (m $\Omega$ )	DCR Max. (m $\Omega$ )
CTMMP1307AF-1R0M	1.00	4.0	5.0	32	38
CTMMP1307AF-1R5M	1.50	3.8	4.0	42	50
CTMMP1307AF-2R2M	2.20	3.5	3.7	65	75
CTMMP1307AF-3R3M	3.30	3.0	3.5	125	145
CTMMP1307AF-4R7M	4.70	2.6	3.0	172	200
CTMMP1307AF-5R6M	5.60	2.2	2.6	205	238
CTMMP1307AF-6R8M	6.80	1.9	2.2	260	300
CTMMP1307AF-8R2M	8.20	1.6	1.9	340	390
CTMMP1307AF-100M	10.0	1.4	1.6	366	422

## PHYSICAL DIMENSIONS

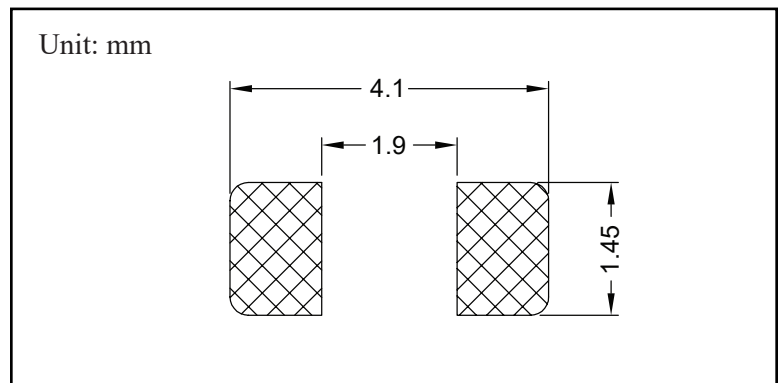
Size	A	B	C	D	E
mm	3.5 $\pm$ 0.2	3.2 $\pm$ 0.2	1.8 $\pm$ 0.2	0.7 $\pm$ 0.2	1.2 $\pm$ 0.2
inches	0.138 $\pm$ 0.008	0.126 $\pm$ 0.008	0.071 $\pm$ 0.008	0.028 $\pm$ 0.008	0.047 $\pm$ 0.008



## CHARACTERISTICS

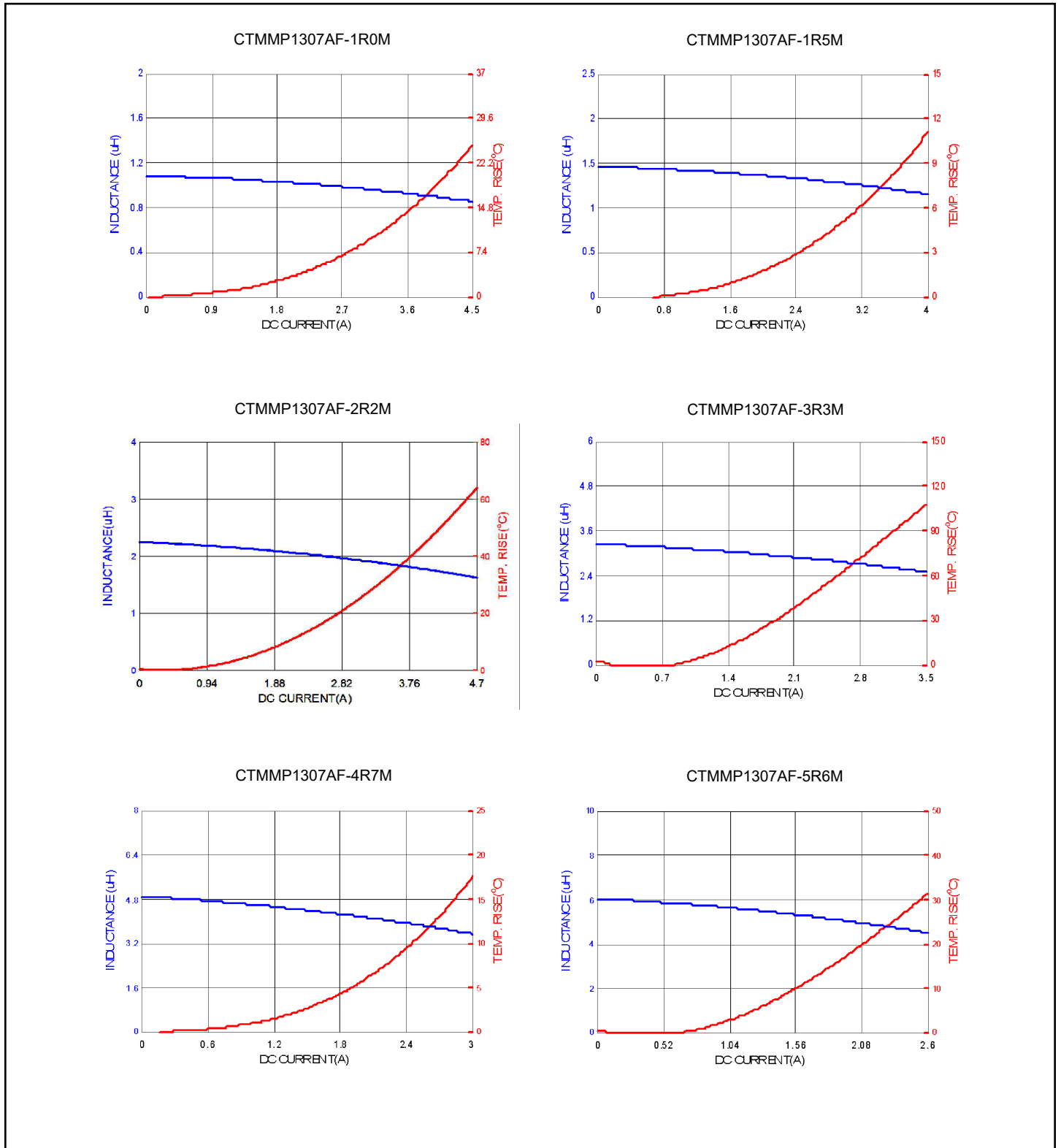
**Description:** SMD (shielded) Power Inductor  
**Applications:** Notebook, Desktop, Server applications, High current power supplies, Battery powered devices, DC/DC converter for Field Programmable Gate Array (FPGA).  
**Operating Temperature:** -40°C to 125°C [The part temperature (ambient + temp. rise)] should not exceed 125°C under worst case operating conditions. Component placement, PWB trace, size, thickness and other cooling provisions will affect the part temperature. Part temperature should be verified in the end application.  
**Inductance Tolerance:**  $\pm 20\%$   
**Testing:** Inducance measured at 100kHz/1.0V  
**Packaging:** Tape & Reel.  
**Marking:** Parts are marked with inductance value.  
**Miscellaneous:** **RoHS Compliant.**  
**Samples available. See website for ordering information.**

## PAD LAYOUT



## CTMMP1307AF

### CHARACTERISTIC CURVES



## CTMMP1307AF

### CHARACTERISTIC CURVES

