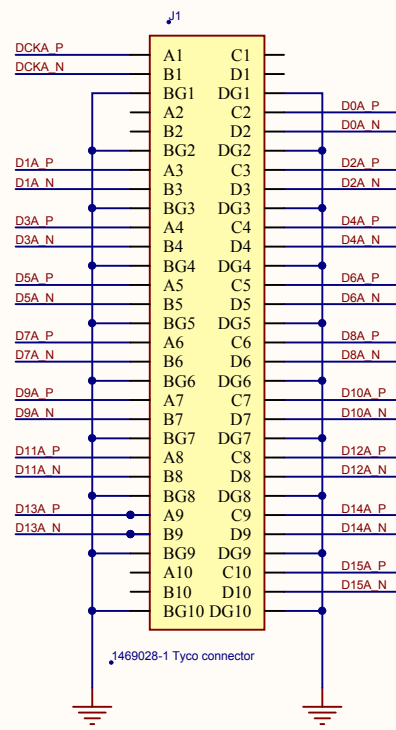


R15	IO_L10N_2
R16	IO_L10P_2
W17	IO_L11N_2
V17	IO_L11P_2
U13	IO_L12N_D2_MISO3_2
U14	IO_L12P_D1_MISO2_2
V15	IO_L13N_D10_2
U15	IO_L13P_M1_2
AB18	IO_L14N_D12_2
AA18	IO_L14P_D11_2
AB17	IO_L15N_2
Y17	IO_L15P_2
AB14	IO_L16N_VREF_2
AA14	IO_L16P_2
W15	IO_L17N_2
Y16	IO_L17P_2
W13	IO_L18N_2
V13	IO_L18P_2
AB16	IO_L19N_2
AA16	IO_L19P_2
AA22	IO_L1N_M0_CMPMISO_2
Y14	IO_L20N_2
W14	IO_L20P_2
AB15	IO_L21N_2
Y15	IO_L21P_2
U12	IO_L22N_2
T12	IO_L22P_2
R13	IO_L23N_2
T14	IO_L23P_2
Y12	IO_L29N_GCLK2_2
W12	IO_L29P_GCLK3_2
AB21	IO_L2N_CMPMOSI_2
AA21	IO_L2P_CMPCLK_2
AB13	IO_L30N_GCLK0_USERCCLK_2
Y13	IO_L30P_GCLK1_D13_2
AB12	IO_L31N_GCLK30_D15_2
AA12	IO_L31P_GCLK31_D14_2
AB11	IO_L32N_GCLK28_2
Y11	IO_L32P_GCLK29_2
AB20	IO_L3N_MOSI_CSI_B_MISO0_2
T11	IO_L40N_2
R11	IO_L40P_2
AB10	IO_L41N_VREF_2
AA10	IO_L41P_2
W11	IO_L42N_2
V11	IO_L42P_2
AB9	IO_L43N_2
Y9	IO_L43P_2
Y10	IO_L44N_2
W10	IO_L44P_2
AB8	IO_L45N_2
AA8	IO_L45P_2
V7	IO_L46N_2
W8	IO_L46P_2
Y8	IO_L47N_2
W9	IO_L47P_2
AB7	IO_L48N_RDWR_B_VREF_2
Y7	IO_L48P_D7_2
AB6	IO_L49N_D4_2
AA6	IO_L49P_D3_2
T17	IO_L4N_VREF_2
T18	IO_L4P_2
V9	IO_L50N_2
U9	IO_L50P_2
U8	IO_L51N_2
T8	IO_L51P_2
U10	IO_L52N_2
T10	IO_L52P_2
Y6	IO_L53N_2
W6	IO_L53P_2
AB5	IO_L54N_2
Y5	IO_L54P_2
AB4	IO_L57N_2
AA4	IO_L57P_2
AB3	IO_L58N_2
Y3	IO_L58P_2
R8	IO_L59N_2
R9	IO_L59P_2
AB19	IO_L5N_2
Y19	IO_L5P_2
R7	IO_L60N_2
T7	IO_L60P_2
Y4	IO_L62N_D6_2
W4	IO_L62P_D5_2
V5	IO_L63N_2
U6	IO_L63P_2
AB2	IO_L64N_D9_2
AA2	IO_L64P_D8_2
T5	IO_L65N_CSO_B_2
Y18	IO_L6N_2
W18	IO_L6P_2
T15	IO_L7N_2
T16	IO_L7P_2
U16	IO_L8N_2
U17	IO_L8P_2
V18	IO_L9N_2
V19	IO_L9P_2
Y20	CMPCS_B_2

SPARTAN6_LX45

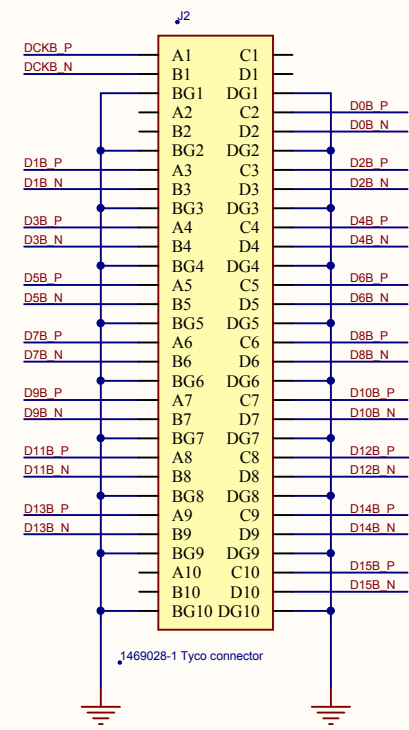
INPUT A CONNECTOR

NOTE: DIFFERENTIAL PAIRS ROUTED AT 1% LENGTH, 100-OHM DIFFERENTIAL



INPUT B CONNECTOR

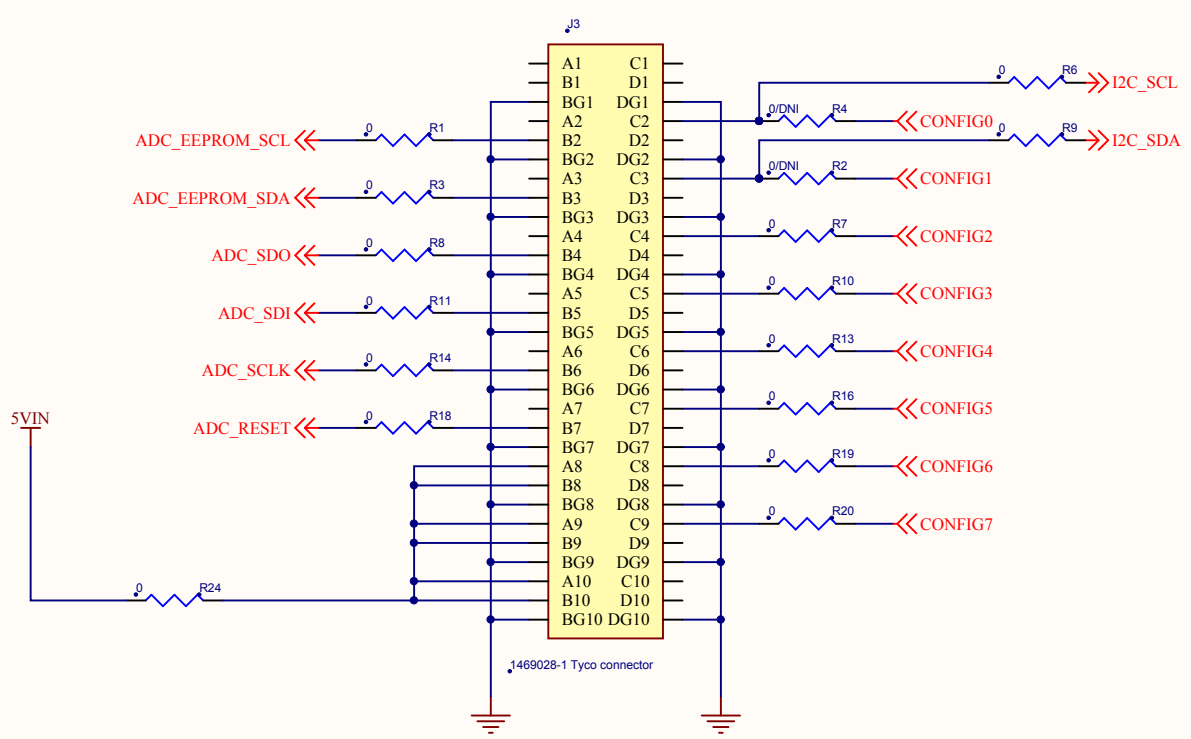
NOTE: DIFFERENTIAL PAIRS ROUTED AT 1% LENGTH, 100-OHM DIFFERENTIAL



NOTE: D8A_P AND D8A_N ARE EXPECTED TO BE USED FOR LVDS OVERRANGE SIGNAL

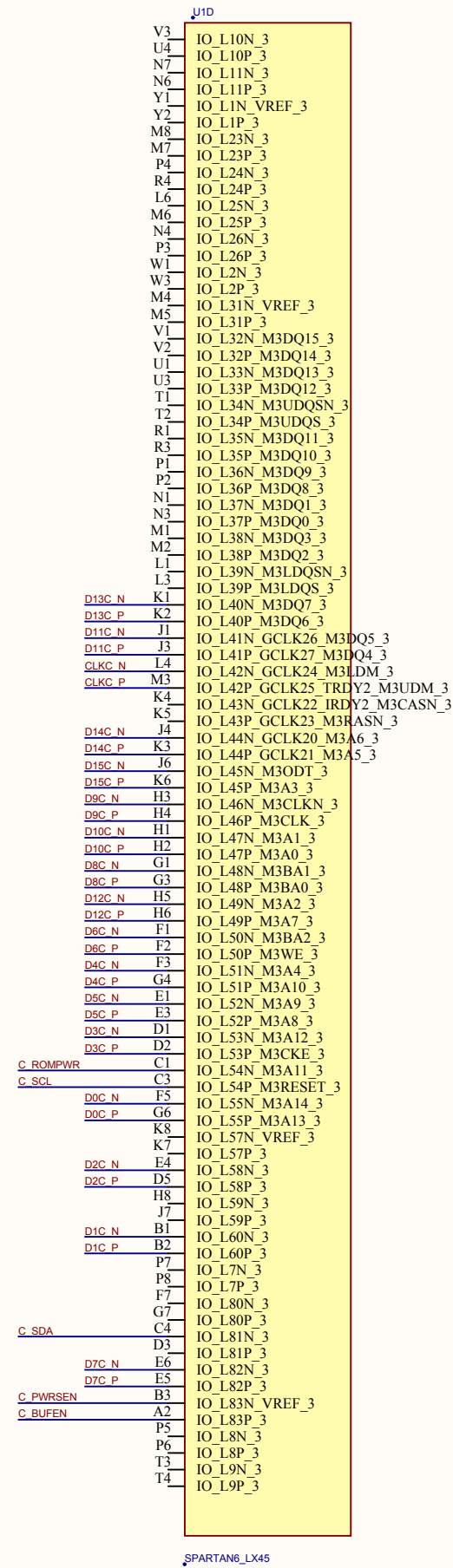
NOTE: CONFIG[0:15] IS RESERVED FOR FUTURE USE, ROUTED SINGLE-ENDED

CONFIGURATION CONNECTOR



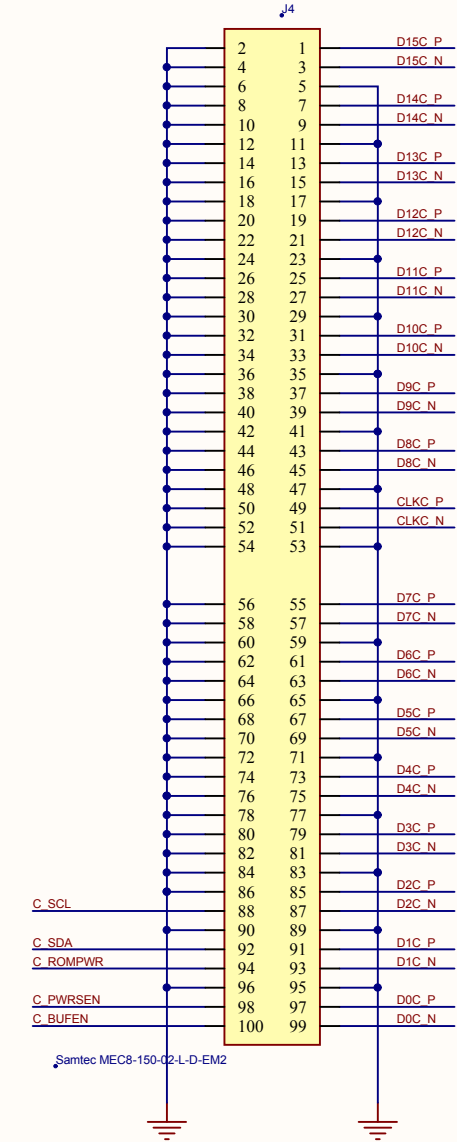
NOTE: CONFIG INTERFACE PIN A1 SHOULD BE 38mm FROM INPUT A PIN A1 WHICH SHOULD BE 32mm FROM INPUT B PIN A1. (CENTER OF PINS)

Title		
Microchip Technology		
Size	Document Number	Rev
A	ADM00506 (Data Capture Card)	A
Date:	Sheet 2 of 7	

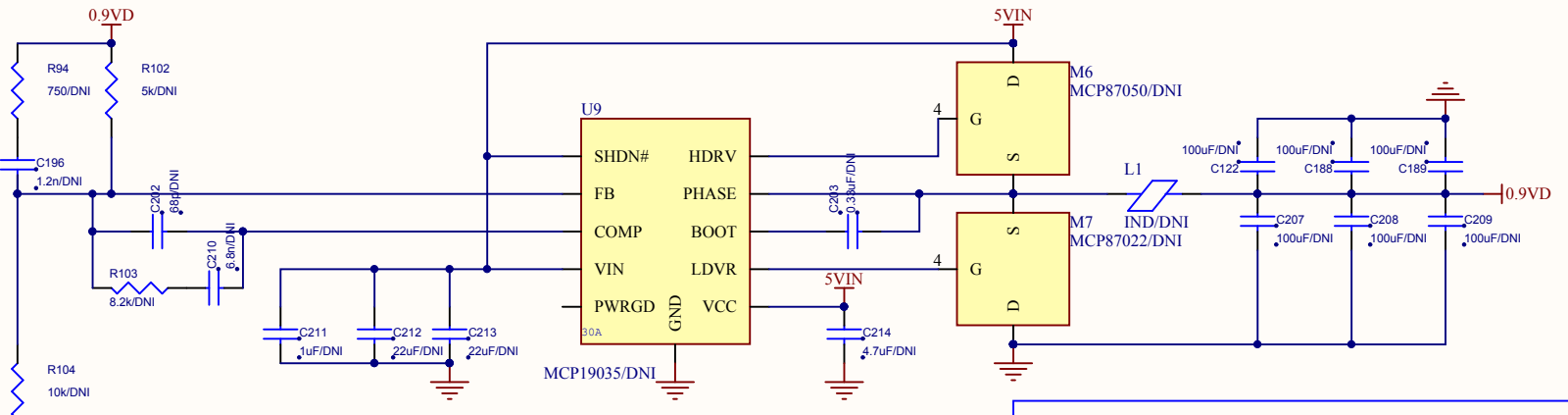
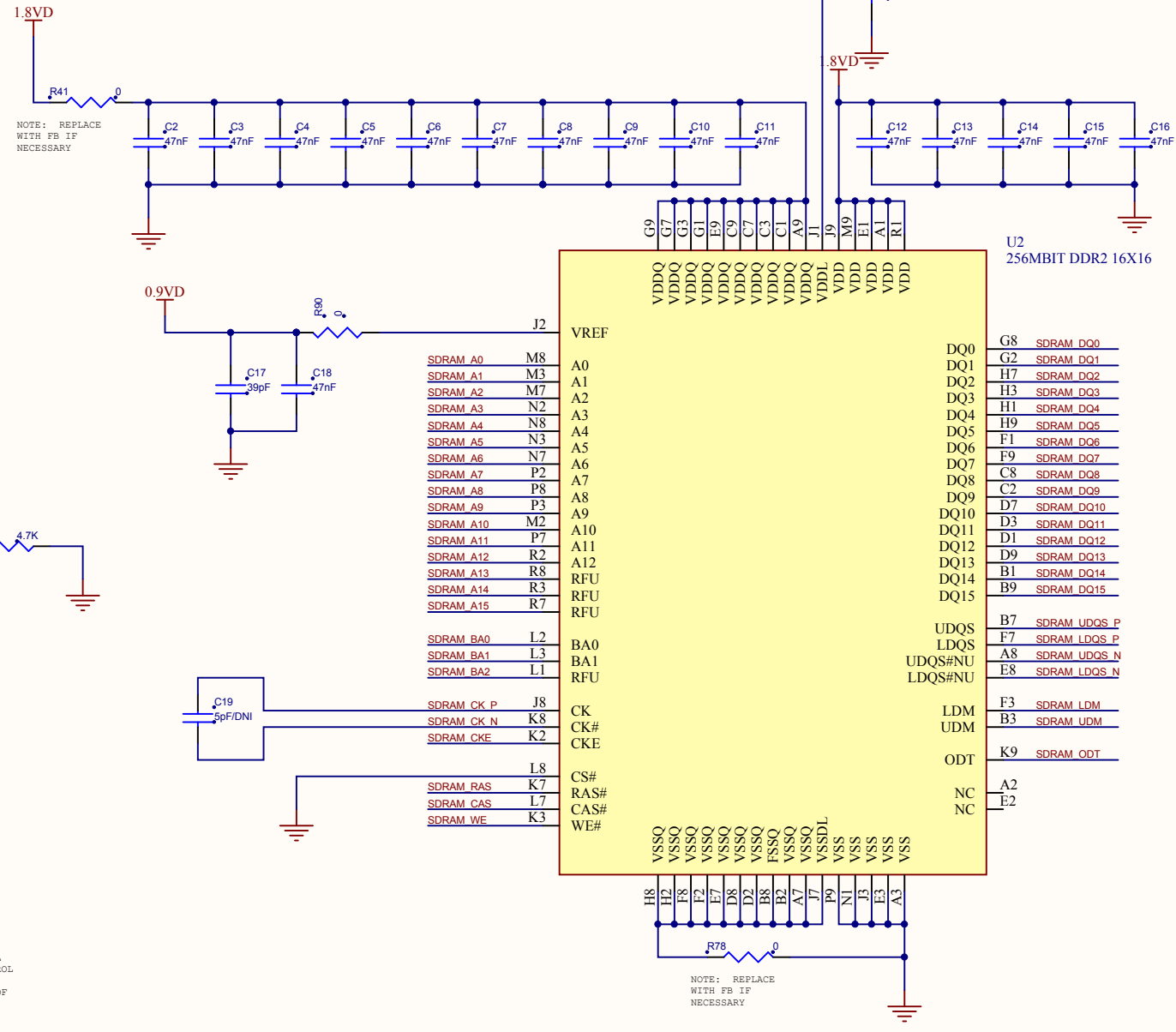
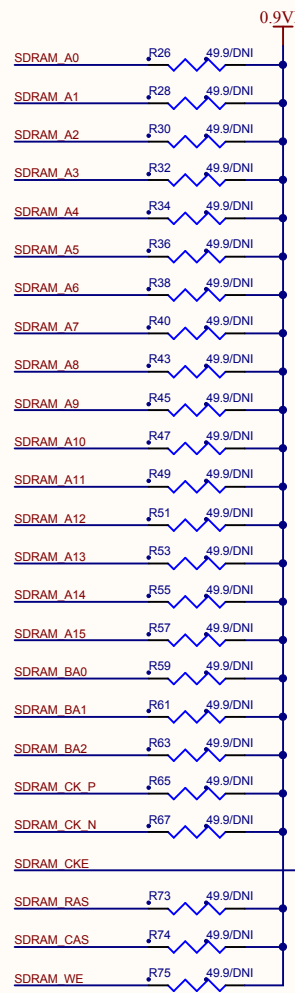
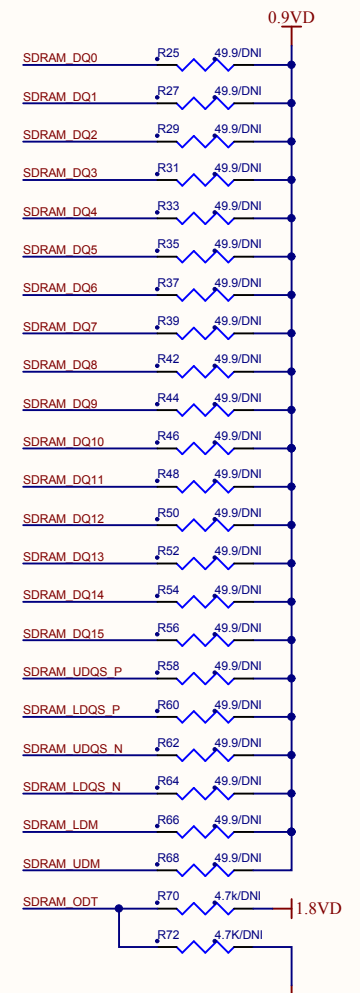
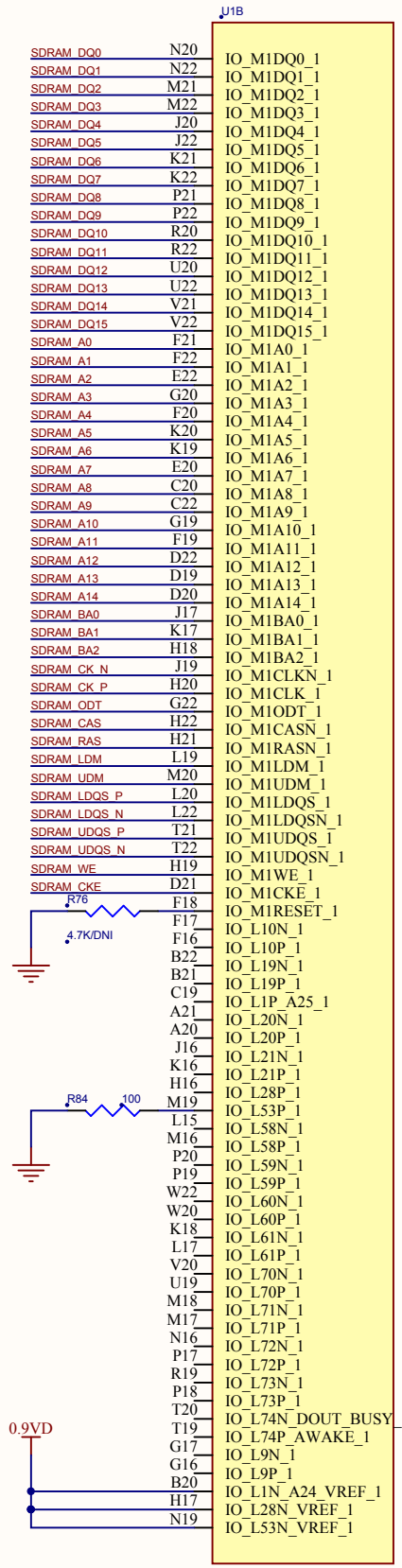


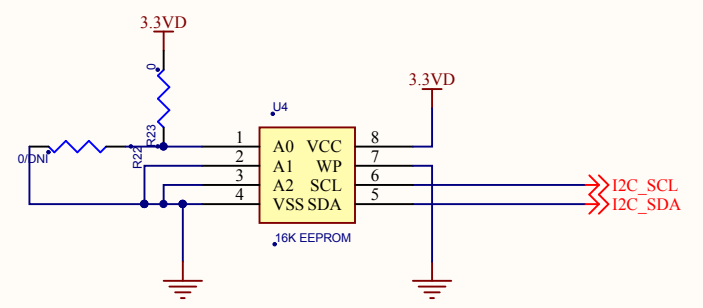
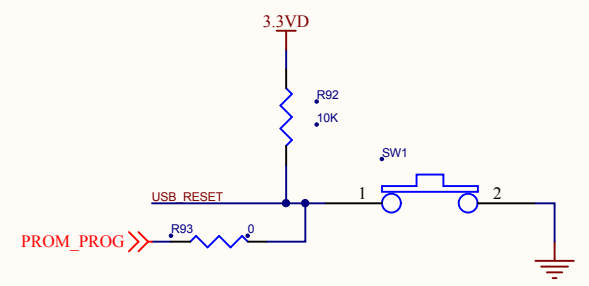
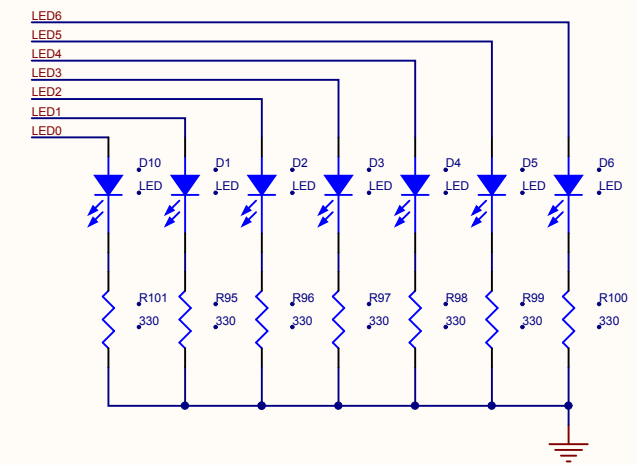
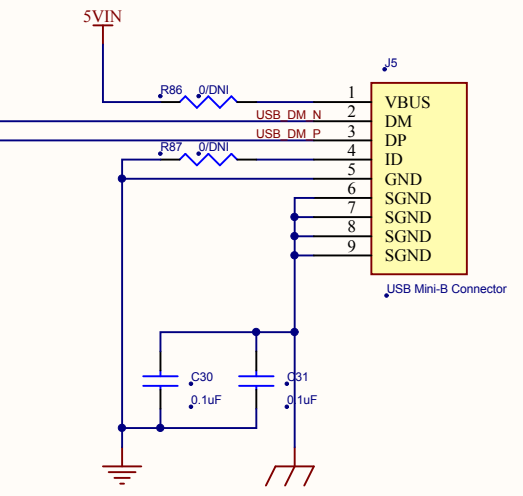
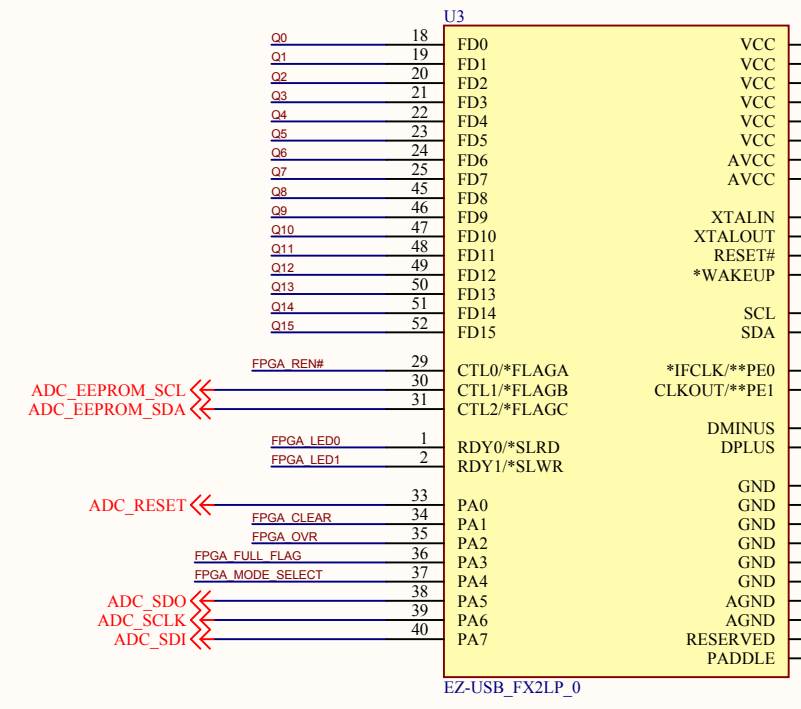
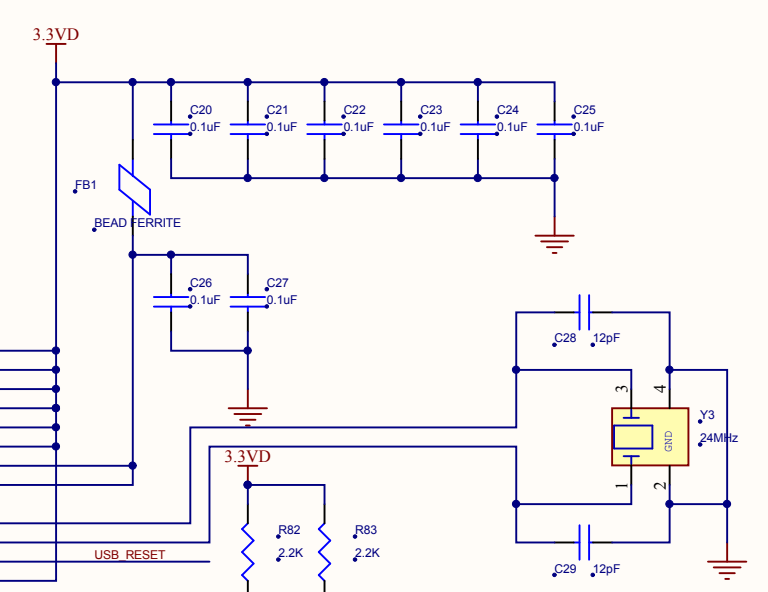
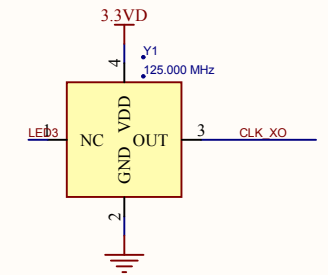
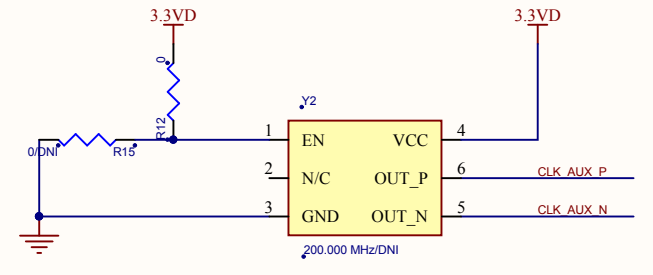
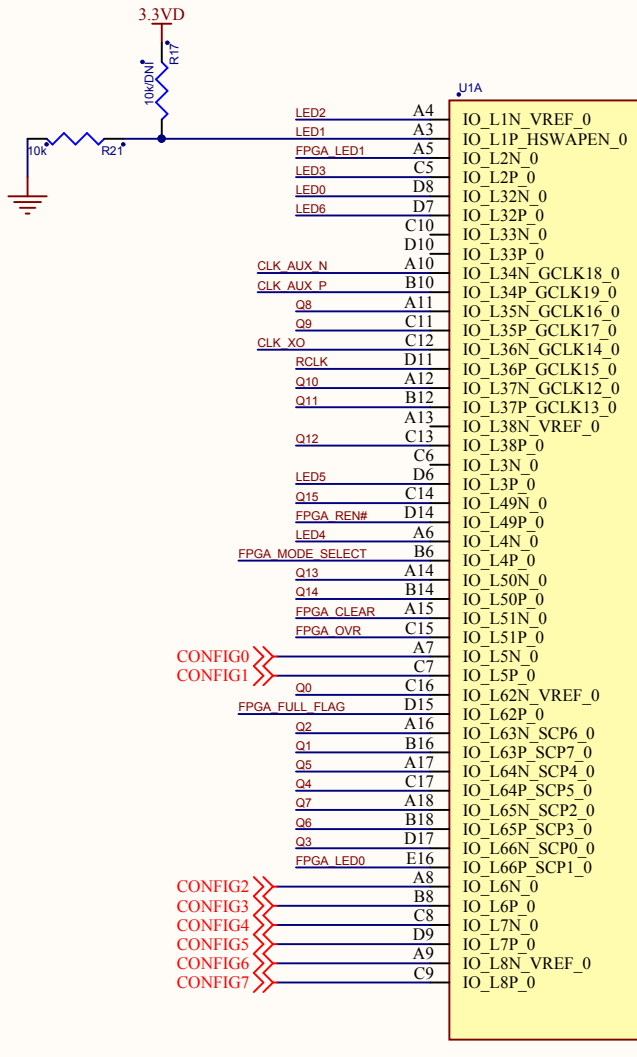
NOTE: DIFFERENTIAL PAIRS ROUTED AT 14 LENGTH, 100-OHM DIFFERENTIAL

INPUT C CONNECTOR

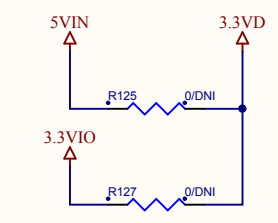
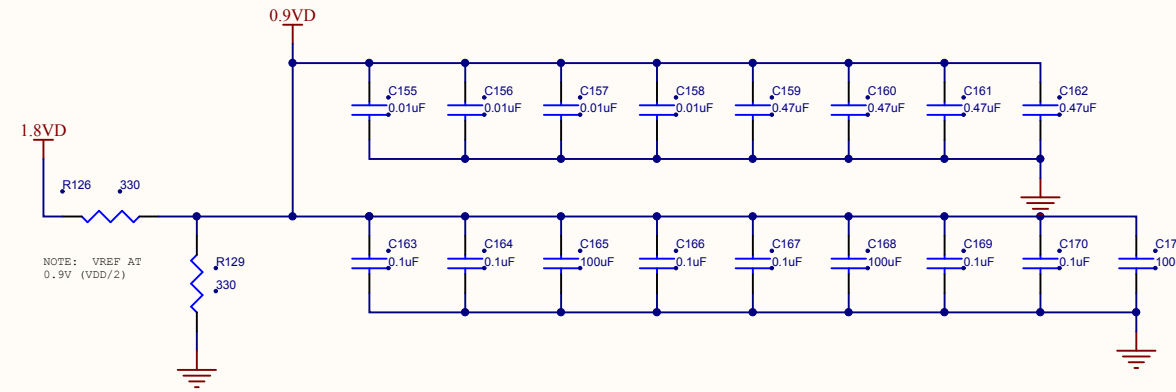
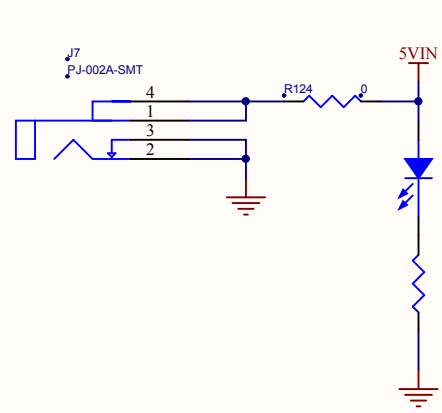


Title		
Microchip Technology		
Size	Document Number	Rev
A	ADM00506 (Data Capture Card)	A
Date:	Sheet 3 of 7	

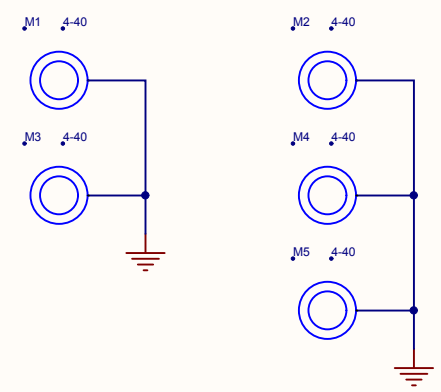




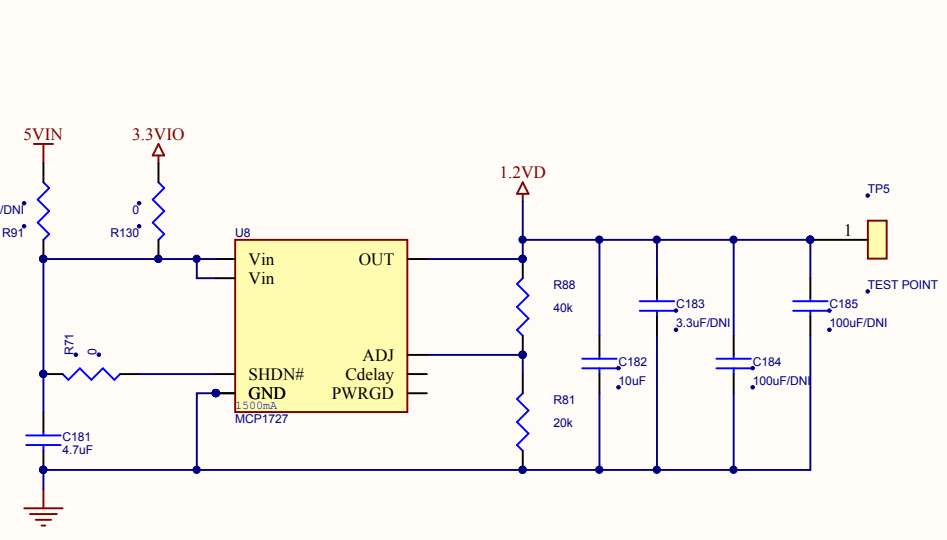
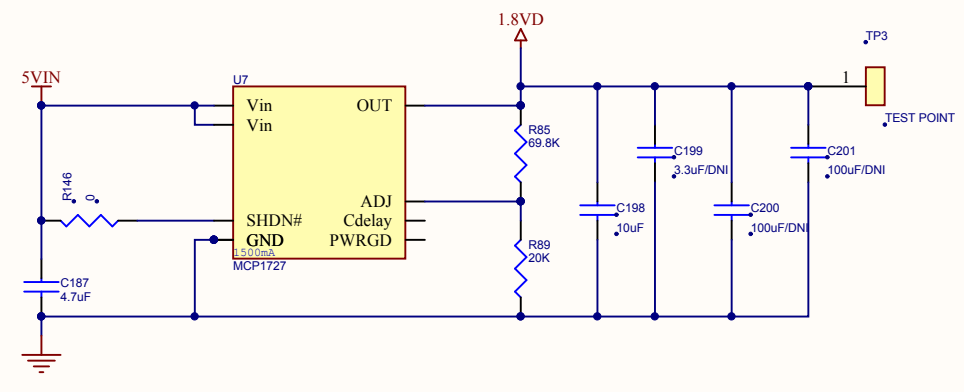
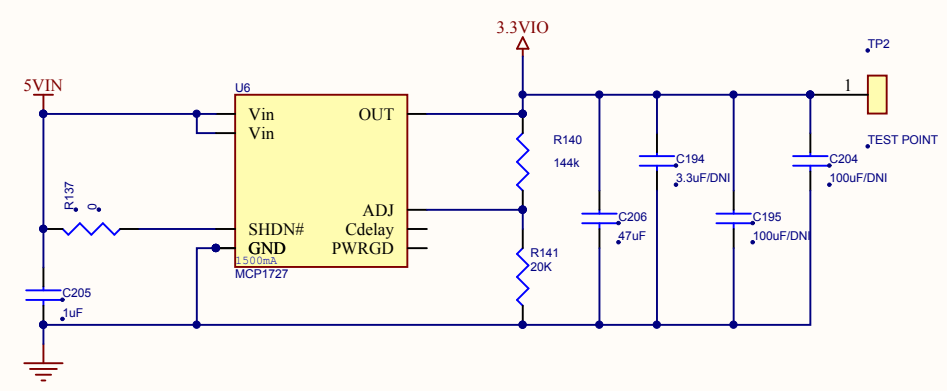
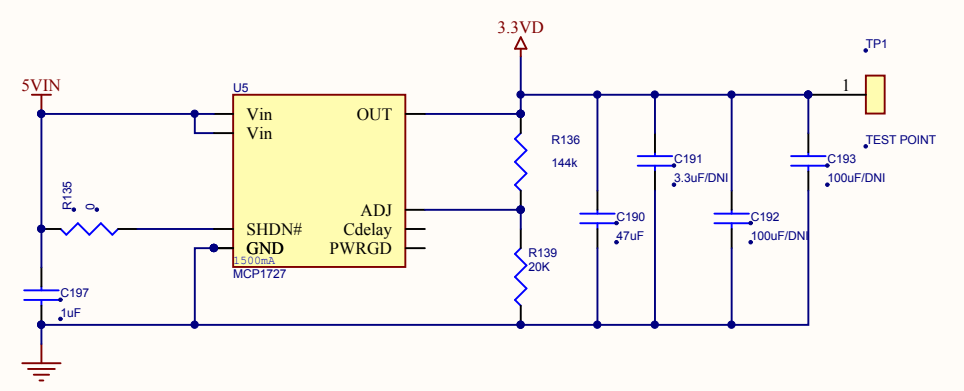
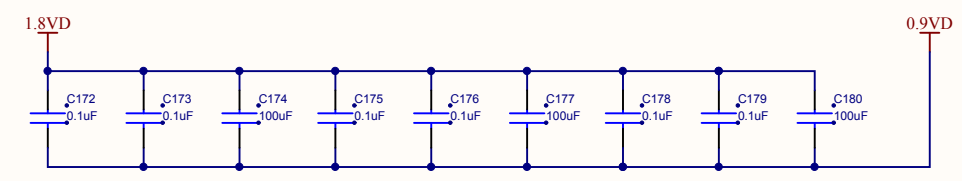
Title		
Microchip Technology		
Size	Document Number	Rev
A	ADM00506 (Data Capture Card)	A
Date:	Sheet	5 of 7



NOTE: PLACE MOUNTING HOLES AT CORNERS OF PCB



NOTE: USE IF SUPPLYING 3.3V TO EXTERNAL POWER CONNECTOR. MUST ALSO REMOVE OR SHUTDOWN 3.3V REGULATOR.



Title		
Microchip Technology		
Size	Document Number	Rev
A	ADM00506 (Data Capture Card)	A
Date:	Sheet 7	of 7