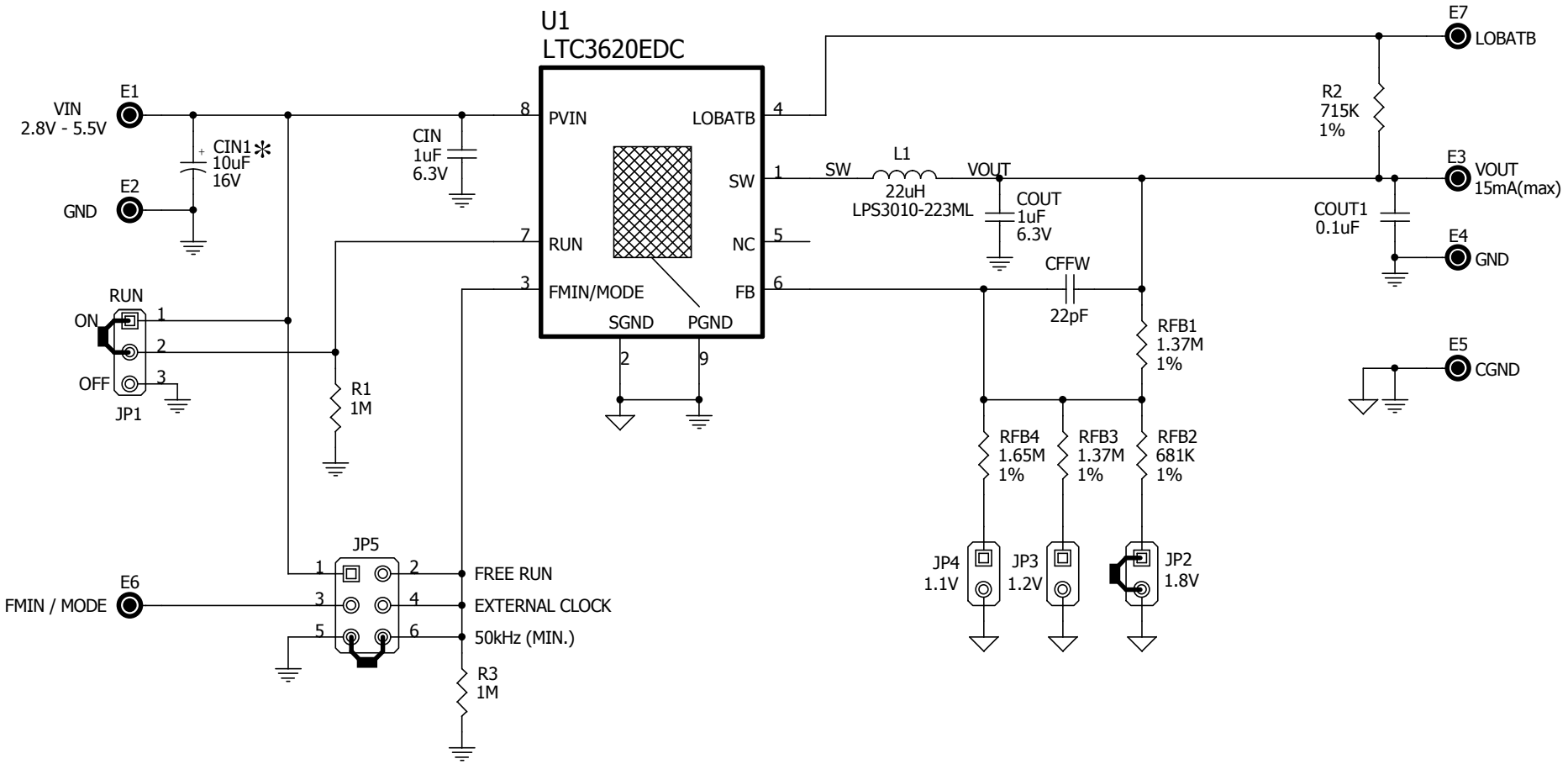
✻ CIN1 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC1460A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

REVISION #				REVISION HISTORY		
SD	PC	AD	FD	DESCRIPTION	DATE	APPROVED
0	0	0	0	1st Release		
2	2	1	2	L1 from "LPS3010" to "LPS3008" series.	4/9/09	ChuenmingT
3	2	1	2	L1 from "LPS3008" to "LPS3010" series.	6/26/09	ChuenmingT



APPROVALS

DRAWN: Rudy Bautista
ENGINEER: Tom Gross/Chuen MT
APPROVED:
CHECKED:
Date: Tuesday, August 04, 2009

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Title: LTC3620EDC
High Efficiency Monolithic Buck Regulator

SD | Document Number: Demo Circuit 1460A | Rev 3

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