

- PoE+ application with 720mA current capability
- Designed to meet IEEE802.3at
- Operating Temperature Range: -40°C to +85°C

Electrical Specifications @ 25°C

OCL(μH Min) @ 100kHz, 100mV With 11mA DC Bias	OCL(μH Min) @ 100kHz, 100mV With 19mA DC Bias	Turns Ratio chip : cable (±3%)	DCR (Ω Max)	Return Loss (dB Min)	
				0.5-40MHz	40-100MHz
350	120	1CT: 1CT	1.1	-18	-12-20log(f/80MHz)

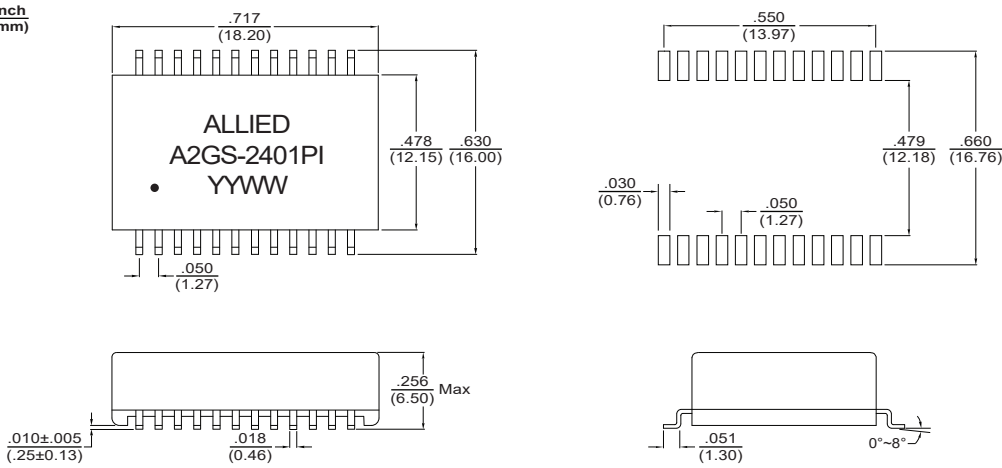
Electrical Specifications @ 25°C

InsertionLoss (dB Min)				Cross Talk (dB Min)	CDMR (dB Min)	HI-POT Isolation (Vrms)
0.3-1MHz	65MHz	100MHz	125MHz			
-1.1	-0.8	-1.0	-1.2	-33-20logz(f/100MHz)	-35	1500

All specifications subject to change without notice.

MECHANICAL

Dimensions: inch (mm)



SCHEMATICS

Unless otherwise specified, all tolerances are ±0.25(0.010)

