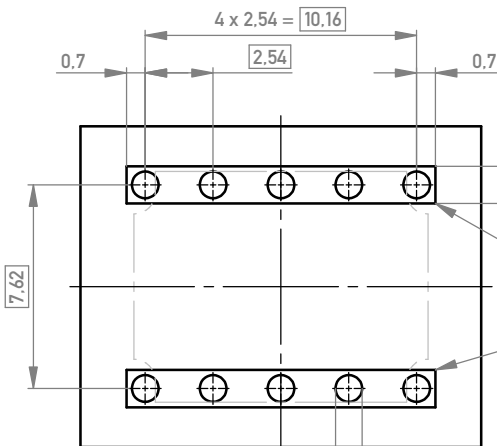


M 2:1

Leiterplattenbohrbild
PCB drillhole pattern



Dieser Bereich muß gleiches Potential auf der LP-Oberfläche haben

This area must have same electrical potential on surface of PCB

$\text{⌀} 0.05$ $\text{⌀} 1 \pm 0.1$

alle Löcher/
all holes

214786	M4
214785	M3
214784	6-32UNC
214783	8-32UNC
Id.-Nr.: Part No.	Gewinde Thread

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d Index	23.11.2020 Date	www.ERNI.com		Class	EPSVA