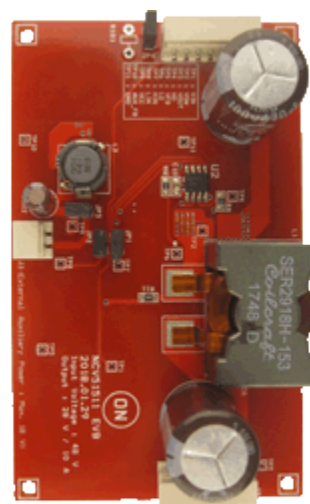




NCV51511SYNCKBUCKGEVB: NCV51511 EVAL BRD

The Evaluation board is developed to evaluate performance of high side and low side gate driver and the target application is 300W non-isolation Synchronous Buck 48 V $\pm 10\%$ input voltage and 28 V regulated output voltage. To supply bias voltage both synchronous buck and NCV51511, the NCV33163 control the auxiliary power output around 10 V. And the main synchronous buck controller generate high side and low side PWM input signal for NCV51511. The NCV51511 drive the high side and low side external MOSFET depend on the input PWM signal and VDD level.



Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Boards	NCV51511SYNCKBUCKGEVB_BOM_ROHS.REV0.PDF	NCV51511SYNCKBUCKGEVB_BOM_ROHS.rev0 - 46 KB	0
Eval Boards	NCV51511SYNCKBUCKGEVB_GERBER.REV0.ZIP	NCV51511SYNCKBUCKGEVB_GERBER.rev0 - 125 KB	0
Eval Boards	NCV51511SYNCKBUCKGEVB_SCHEMATIC.REV0.PDF	NCV51511SYNCKBUCKGEVB_SCHEMATIC.rev0 - 68 KB	0
Eval Boards	NCV51511SYNCKBUCKGEVB_TEST_PROCEDURE.REV0.PDF	NCV51511SYNCKBUCKGEVB_TEST_PROCEDURE.rev0 - 109 KB	0